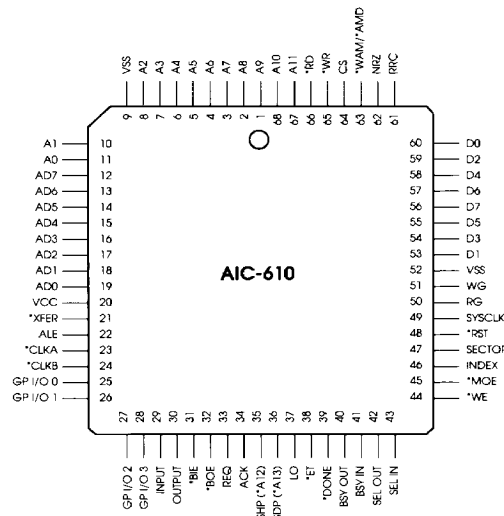


AIC-610

Integrated Programmable Storage Controller



AIC-610 Quad PLCC Package

FEATURES

Peripheral Data Controller

- Controls embedded, ST412/506, ESDI, and SMD interface hard disk drives
- Works with all disk encoding schemes
- User-modifiable RAM-based control sequencer
- User-programmable internal, 32-bit, 48-bit ECC polynomial, and 16-bit CRC, or variable length external polynomial
- Transfer rate up to 15 Mbits/sec
- Soft-or hard-sector drives
- Multiple sector transfer
- Sector-level defect handling
- Noninterleaved operation
- User-programmable sector length up to a full track

Buffer Data Management

- Buffer sizes from 256 to 64 KBytes
- Buffer size of 16 KBytes accessed directly
- DMA handshake logic
- Supervises data transfers to buffer with overrun control
- Dual-port circular FIFO buffer control
- Port priority resolver resolves host/peripheral requests

System Bus Interface

- Arbitration logic allows stacking
- Programmable request for arbitration while system bus in busy state
- Simplified SCSI interface implementation with AIC-500 SCSI interface adapter

Processor Interface

- Multiplexed address/data bus interface
- Interrupt driven operation

Technologies

- CMOS and NMOS
- Software-compatible with AIC-011/AIC-301
- 68-pin QUAD PLCC