



AK61664Z 64K x 16 SRAM Module

DESCRIPTION

The Accuthek AK61664 SRAM Module consists of fast high performance SRAMs mounted on a low profile 40 pin ZIP Board. The module utilizes four 28 pin 64K x 4 SRAMs in SOJ packages and four decoupling capacitor chips on the front and back side of a printed circuit board.

The SRAMs are interconnected on the module to have common I/O functions, single output enable functions and common chip select. The modules can be supplied in a variety of access time values from 12 nSEC to 45 nSEC in CMOS or BiCMOS technology.

The Accuthek module is designed for the lowest height off the board, consistent with the availability of commonly available SRAM SOJ package configurations.

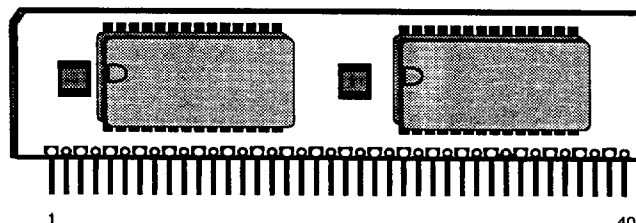
FEATURES

- 1 MB SRAM in high density configuration
- 40 pin ZIP format
- Common I/O, single $\overline{OE}$ functions and common chip select $\overline{CE}$ Function
- Low height 0.500 inch ZIP maximum
- Fast Access Times range from 12 nSEC to 45 nSEC, CMOS or BiCMOS
- TTL compatible inputs and outputs
- Single 5 volt power supply
- Operating temperature range in free air, 0°C to 70°C

- Power (Typical)
CMOS
3.2 Watt Active
1.2 Watt Standby

- BiCMOS**
4.8 Watt Active
1.3 Watt Standby

Front View 40-Pin ZIP



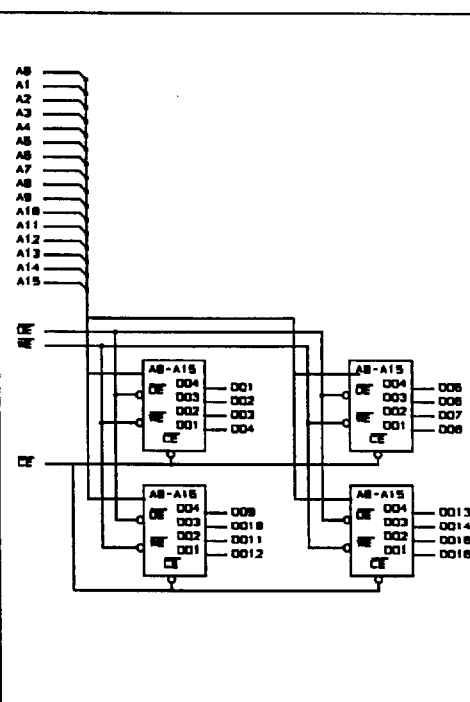
PIN NOMENCLATURE

A0 - A15	Address Inputs
$\overline{WE}$	Write Enable
$\overline{OE}$	Output Enable
$\overline{CE}$	Chip Select
DQ1 - DQ16	Data In/Data Out
Vcc	5v Supply
Vss	Ground

PIN ASSIGNMENT

PIN #	SYMBOL	PIN #	SYMBOL
1	DQ16	21	A11
2	Vss	22	A4
3	DQ15	23	A10
4	DQ1	24	A5
5	DQ14	25	A9
6	DQ2	26	A6
7	DQ13	27	A8
8	DQ3	28	A7
9	Vss	29	DQ12
10	DQ4	30	DQ5
11	A15	31	DQ11
12	A0	32	DQ6
13	A14	33	Vss
14	A1	34	DQ7
15	A13	35	DQ10
16	A2	36	DQ8
17	A12	37	DQ9
18	A3	38	$\overline{OE}$
19	Vcc	39	$\overline{CE}$
20	Vss	40	$\overline{WE}$

FUNCTIONAL DIAGRAM



MODULE OPTIONS

Leaded ZIP: AK61664Z
Single In-Line Package

0107647 0000067 683

ORDERING INFORMATION

PART NUMBER CODING INTERPRETATION

Position 1 2 3 4 5 6 7 8

1 Product

AK = Accutek Memory

2 Type

4 = Dynamic RAM
5 = CMOS Dynamic RAM
6 = Static RAM

3 Organization/Word Width

1 = by 1 16 = by 16
4 = by 4 32 = by 32
8 = by 8 36 = by 36
9 = by 9

4 Size/Bits Depth

64 = 64K 4096 = 4 MEG
256 = 256K 8192 = 8 MEG
1024 = 1 MEG 16384 = 16 MEG

5 Package Type

G = Single In-Line Package (SIP)
S = Single In-Line Module (SIM)
D = Dual In-Line Package (DIP)
W = .050 Inch Pitch Edge Connect
Z = Zig-Zag In-Line Package (ZIP)

6 Special Designation

P = Page Mode
N = Nibble Mode
K = Static Column Mode
W = Write Per Bit Mode
V = Video Ram

7 Separator

- = Commercial 0°C to +70°C
M = Military Equivalent Screened (-55°C to +125°C)
I = Industrial Temperature Tested (-45°C to +85°C)
X = Burned In

8 Speed (first two significant digits)

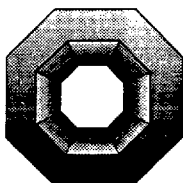
DRAMS		SRAMS	
60	= 60 nS	12	= 12 nS
70	= 70 nS	20	= 20 nS
80	= 80 nS	25	= 25 nS
10	= 100 nS	35	= 35 nS

The numbers and coding on this page do not include all variations available but are shown as examples of the most widely used variations. Contact Accutek if other information is required.

EXAMPLES:

AK61664Z-12

64K x 16, 12 nSEC SRAM ZIP Module

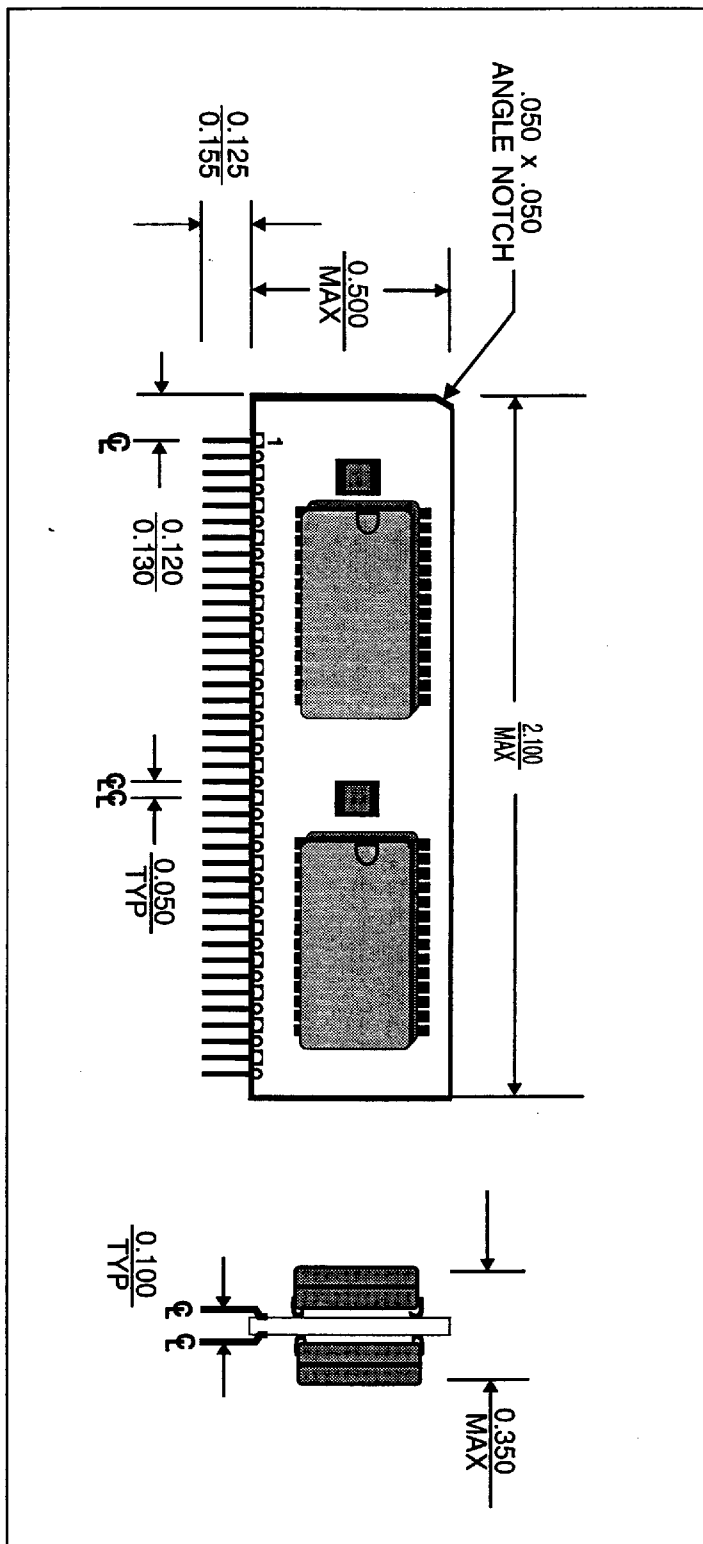


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MECHANICAL DIMENSIONS

Inches



Accutek Reserves the right to make changes in specifications at any time and without notice. Accutek does not assume any responsibility for the use of any circuitry described; no circuit patent licenses are implied. Preliminary data sheets contain minimum and maximum limits based upon design objectives, which are subject to change upon full characterization over the specific operating conditions.

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