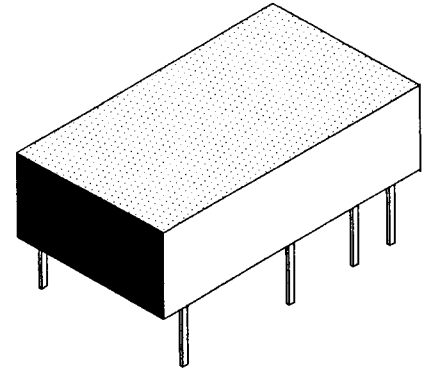


14 PIN, 5 TAP TTL, BOX STYLE ACTIVES

DC Electrical Characteristics			Min	Max	Unit
Parameter	Test Conditions				
V _{OH}	High-Level Output Voltage	V _{CC} =min. V _{IL} =max. I _{OH} =max	2.7		V
V _{OL}	Low Level Output Voltage	V _{CC} =min. V _{IH} =min. I _{OL} =max		0.5	V
V _{IK}	Input Clamp Voltage	V _{CC} =min. I _I =-18mA		-1.2	V
I _{IH}	High-Level Input Current	V _{CC} =max. V _{IN} =2.7V		50	uA
I _{IL}	Low-Level Input Current	V _{CC} =max. V _{IN} =5.25V		1.0	mA
I _{OS}	Short Circuit Output Current	V _{CC} =max. V _{IN} =0.5V		-2	mA
		V _{CC} =max. V _{OUT} =0	-40	-100	mA
ICCH	High-Level Supply Current	(One output at a time) V _{CC} =max. V _{IN} =OPEN		75	mA
ICCL	Low-Level Supply Current	V _{CC} =max. V _{IN} =0		75	mA
T _{RO}	Output Rise Time	T _d ≤ 500nS(0.75to2.4Volts)		4	nS
		T _d > 500nS		5	nS
N _H	Fanout High-Level Output	V _{CC} =max. V _{OH} =2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	V _{CC} =max. V _{OH} =0.5V		10 TTL LOAD	



Recommended Operating Conditions			Min	Max	Unit
V _{CC}	Supply Voltage		4.75	5.25	V
V _{IH}	High-level Input voltage		2.0		V
V _{IL}	Low-Level Input Voltage			0.8	V
I _{OH}	High-Level Output Current			-1.0	mA
I _{OL}	Low-Level Output Current			20	mA
PW	Pulse Width % of Total Delay		40		%
TA	Operating Free-Air Temperature		0	+70	°C

Input Pulse Test Conditions @ 25°C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
T _{RI}	Pulse Rise Time(0.75 - 2.4 Volts)	2.0	nS
PRR	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts

TOTAL DELAY ± 2 nS OR 5%	TAP DELAYS ± 2 nS OR 5%	POLARA P/N AKA - PINOUT
25	5	AKB-0025
30	6	AKB-0030
35	7	AKB-0035
40	8	AKB-0040
45	9	AKB-0045
50	10	AKB-0050
55	11	AKB-0055
60	12	AKB-0060
65	13	AKB-0065
70	14	AKB-0070
75	15	AKB-0075
80	16	AKB-0080
85	17	AKB-0085
90	18	AKB-0090
100	20	AKB-0100
125	25	AKB-0125
150	30	AKB-0150
175	35	AKB-0175
200	40	AKB-0200
250	50	AKB-0250
300	60	AKB-0300
400	80	AKB-0400
500	100	AKB-0500

* ANY VALUES NOT SHOWN, AVAILABLE UPON REQUEST.

