

Features

- Low On-Resistance (17 Ω typ) Minimizes Distortion and Error Voltages
- Low Glitching Reduces Step Errors in Sample-and-Holds. Charge Injection, 2 pC typ
- Split-Supply Operation ($\pm 3V$ to $\pm 8V$)
- Improved Second Sources for MAX320/MAX321/MAX322
- On-Resistance Matching Between Channels, 0.2 Ω typ
- On-Resistance Flatness, <2 Ω typ
- Low Off-Channel Leakage, <5nA @ +85 $^{\circ}$ C
- TTL/CMOS Logic Compatible
- Fast Switching Speed, t_{ON} < 150 ns
- Guaranteed Break-Before-Make action (PS322only) eliminates momentary crosstalk
- Rail-to-Rail Analog Signal Dynamic Range
- Low Power Consumption, <1.25mW
- MSOP Package Minimizes Board Area

Applications

- Audio Switching and Routing
- Portable Instruments
- Data Acquisition Systems
- Sample-and-Holds
- Telecommunication Systems
- Battery-Powered Systems

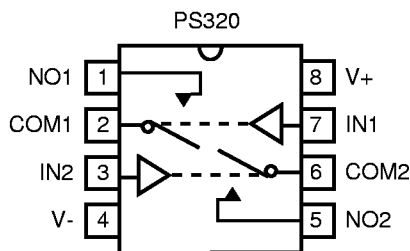
Description

The PS320/PS321/PS322 are improved high precision, medium voltage analog switches designed to operate with $\pm 3V$ to $\pm 8V$ power supplies. The PS320 is a dual, single-pole single-throw (SPST), normally open (NO) switch. The PS321 has the same pinout as the PS320 but it has two normally closed (NC) switches. The PS322 has one normally open (NO) and one normally closed (NC) switch per package. Each switch conducts current equally well in either direction when on. In the off state each switch blocks voltages up to the power-supply rails.

With $\pm 5V$ power supplies, the PS320/PS321/PS322 guarantee <35 Ω on-resistance. On-resistance matching between channels is within 2 Ω . On-resistance flatness is less than 4 Ω over the specified range. All three devices guarantee low leakage currents (<100 pA @ 25 $^{\circ}$ C, <10nA @ +85 $^{\circ}$ C) and fast switching speeds (t_{ON} < 150ns). Break-before-make switching action protects against momentary crosstalk (PS322).

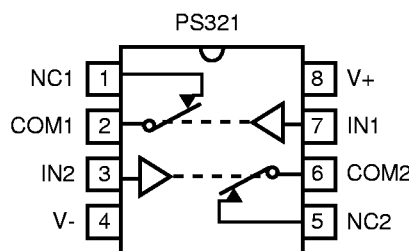
For single-supply operation the PS323/PS324/PS325 are recommended.

Functional Diagrams, Pin Configurations, and Truth Tables



Top View

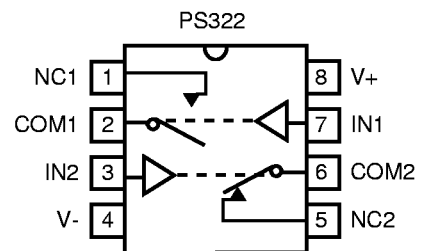
PS320	
Logic	Switch
0	OFF
1	ON



Top View

Switches shown for logic "0" input

PS321	
Logic	Switch
0	ON
1	OFF



Top View

PS322		
Logic	Switch 1	Switch 2
0	OFF	ON
1	ON	OFF

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Absolute Maximum Ratings

Voltages Referenced to V-

V+ -0.3V to +17V

GND -0.3V to +17V

V_{IN}, V_{COM}, V_{NC}, V_{NO} (Note 1) (V-) -2V to (V+) +2V
or 30mA, whichever occurs first

Current (any terminal) 30mA

Peak Current, COM, NO, NC

(pulsed at 1ms, 10% duty cycle) 100mA

ESD per Method 3015.7 >2000V

Thermal Information

Continuous Power Dissipation

Plastic DIP (derate 6mW/°C above +70°C) 500mW

Narrow SO (derate 6mW/°C above +70°C) 450mW

MSOP (derate 4mW/°C above +70°C) 330mW

Storage Temperature -65°C to +150°C

Lead Temperature (soldering, 10s) +300°C

Note 1: Signals on NC, NO, COM, or IN exceeding V+ or V- are clamped by internal diodes. Limit forward diode current to maximum current rating

Caution: Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.

Electrical Specifications - Dual Supplies

(V_± = ±5V ±10%, GND = 0V, V_{INH} = 3.5V, V_{INL} = 1V)

Parameter	Symbol	Conditions	Temp. (°C)	Min ⁽²⁾	Typ ⁽¹⁾	Max ⁽²⁾	Units
Analog Switch							
Analog Signal Range ⁽³⁾	V _{ANALOG}		Full	V-		V+	V
On Resistance	R _{ON}	V+ = 4.5V, V- = -4.5V , V _{NO} or V _{NC} = ±3.5V I _{COM} = 1mA,	25		16	35	Ω
			Full			45	
On-Resistance Match Between Channels ⁽⁴⁾	ΔR _{ON}	V _{NO} or V _{NC} = ±3V, I _{COM} = 1mA, V+ = 5V, V- = -5V	25		0.3	2	
			Full			4	
On-Resistance Flatness ⁽⁵⁾	R _{FLAT(ON)}	V+ = 5V, V- = -5V, I _{COM} = 1mA , V _{NO} or V _{NC} = ±3V	25		1	4	
			Full			6	
NO or NC Off Leakage Current ⁽⁶⁾	I _{NO(OFF)} or I _{NC(OFF)}	V+ = 5.5V, V- = -5.5V, V _{COM} = ±4.5V, V _{NO} or V _{NC} = ∓4.5V	25	-0.1	-0.01	0.1	nA
			Full	-5		5	
COM Off Leakage Current ⁽⁶⁾	I _{COM(OFF)}	V+ = 5.5V, V- = -5.5V V _{COM} = ±4.5V, V _{NO} or V _{NC} = ∓4.5V	25	-0.1	-0.01	0.1	
			Full	-5		5	
COM On Leakage Current ⁽⁶⁾	I _{COM(ON)}	V+ = 5.5V , V- = -5.5V, V _{COM} = ±4.5V V _{NO} or V _{NC} = ∓4.5V	25	-0.2	-0.04	0.2	
			Full	-10		10	

Electrical Specifications - Dual Supplies(continued)

 $(V_{\pm} = \pm 5V \pm 10\%, GND = 0V, V_{INH} = 3.5V, V_{INL} = 1V)$

Parameter	Symbol	Conditions	Temp. (°C)	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Units
Logic Input							
Input Current with Input Voltage High	I _{INH}	V _{IN} = 2.4V, all others = 0.8V	Full	-0.5	0.005	0.5	μA
Input Current with Input Voltage Low	I _{INL}	V _{IN} = 0.8V, all others = 2.4V		-0.5	0.005	0.5	
Logic High Input Voltage	V _{AH}			3.5			V
		3V < V+ < 8V, V- = -V+			0.6V+		
Logic Low Input Voltage	V _{AL}					1	
		3V < V+ < 8V, V- = -V+			0.3V+		
Dynamic							
Turn-On Time	t _{ON}	V _{COM} = ±3V, Figure 2	25		65	150	ns
			Full			175	
Turn-Off Time	t _{OFF}		25		35	100	
			Full			150	
Break-Before-Make TimeDelay ⁽³⁾	t _D	PS322 only, R _L = 300Ω, C _L = 35pF Figure 3	25	2	5		
Charge Injection ⁽³⁾	Q	C _L =1nF, V _{GEN} = 0V, R _{GEN} = 0Ω, Figure 4	25		2	5	pC
Off Isolation ⁽⁷⁾	OIRR	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 5			-72		dB
Crosstalk	X _{TALK}	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 6			-85		
NC or NO Off Capacitance	C _(OFF)	f =1MHz, Figure 7			9		pF
COM Off Capacitance	C _{COM(OFF)}	f =1MHz, Figure 7			9		
COM On Capacitance	C _{COM(ON)}	f =1MHz, Figure 8			22		
Supply							
Power-Supply Range	V+, V-			±2.7		±8	V
Positive Supply Current	I+	V+ = 5.5V, V- = -5.5V, V _{IN} = 0V or V+, all channels on or off	25		80	125	μA
			Full			200	
NegativeSupply Current	I-		25	-125	-80		
			Full	-200			

Notes:

- The algebraic convention, where the most negative value is a minimum and the most positive is a maximum, is used in this data sheet.
- Typical values are for DESIGN AID ONLY, not guaranteed or subject to production testing.
- Guaranteed by design
- $\Delta R_{ON} = \Delta R_{ON\max} - \Delta R_{ON\min}$
- Flatness is defined as the difference between the maximum and minimum value of on-resistance measured.
- Leakage parameters are 100% tested at maximum rated hot temperature and guaranteed by correlation at +25°C.
- Off Isolation = $20 \log_{10} [V_{COM} / (V_{NC\text{ or } V_{NO}})]$. See figure 5.

Electrical Specifications - Single +5V Supply

(V+ = +5V ±10%, V- = 0V, GND = 0V, V_{INH} = 2.4V, V_{INL} = 0.8V)

Parameter	Symbol	Conditions	Temp (°C)	Min ⁽¹⁾	Typ ⁽²⁾	Max ⁽¹⁾	Units
Analog Switch							
Analog Switch Range ⁽³⁾	V _{ANALOG}			0V		V+	V
On-Resistance	R _{ON}	V+ = 5.0V, V- = 0V, I _{COM} = 1.0mA, V _{NO} or V _{NC} = 3.5V,	25		25	65	Ω
			Full			75	
On-Resistance Match Between Channels ⁽⁴⁾	ΔR _{ON}	V+ = 5V, I _{COM} = 1.0mA, V _{NO} or V _{NC} = 3V	25		0.5	2	
			Full			4	
On-Resistance Flatness ⁽⁵⁾	R _{FLAT(ON)}	V+ = 5V, V- = 0V I _{COM} = 1.0mA, V _{NO} or V _{NC} = 3V, 2V, 1V	25			6	
			Full			8	
NO or NC Off Leakage Current ⁽⁹⁾	I _{NO(OFF)} or I _{NC(OFF)}	V+ = 5.5V, V- = 0V V _{COM} = 0V, V _{NO} or V _{NC} = 4.5V	25	-0.2	0.01	0.2	nA
			Full	-2.5		2.5	
COM Off Leakage Current ⁽⁹⁾	I _{COM(OFF)}	V+ = 5.5V, V _{COM} = 4.5V, V _{NO} or V _{NC} = 0V	25	-0.2	0.01	0.2	
			Full	-2.5		2.5	
COM On Leakage Current ⁽⁹⁾	I _{COM(ON)}	V _{COM} = 4.5V V _{NO} or V _{NC} = 4.5V V+ = 5.5V, V- = 0V	25	-0.4	-0.04	0.4	
			Full	-5		5	
Digital Logic Input							
Input Current with Input Voltage High	I _{INH}	V _{IN} =2.4V, all others =0.8V	Full	-1	0.005	1	μA
Input Current with Input Voltage Low	I _{INL}	V _{IN} =0.8V, all others =2.4V		-1	0.005	1	
Dynamic							
Turn-On Time	t _{ON}	V _{COM} = 3V, Figure 2	25		160	250	ns
			Full			300	
Turn-Off Time	t _{OFF}		25		60	125	
			Full			175	
Break-Before-Make Time Delay ⁽³⁾	t _D	PS383 only	25	10	20		
Charge Injection ⁽³⁾	V _{CIE}	CL = 1nF, V _{GEN} = 0V, R _{GEN} = 0VΩ, Figure 4	25		2	5	pC
Supply							
Power-Supply Range	V+			2.7		16	V
Positive Supply Current	I+	V+ = 5.5V, V _{IN} = 0V or V+, all channels on or off	Full	-1	0.01	1	μA
Negative Supply Current	I-		Full	-1	-0.01	1	

Test Circuits/Timing Diagrams

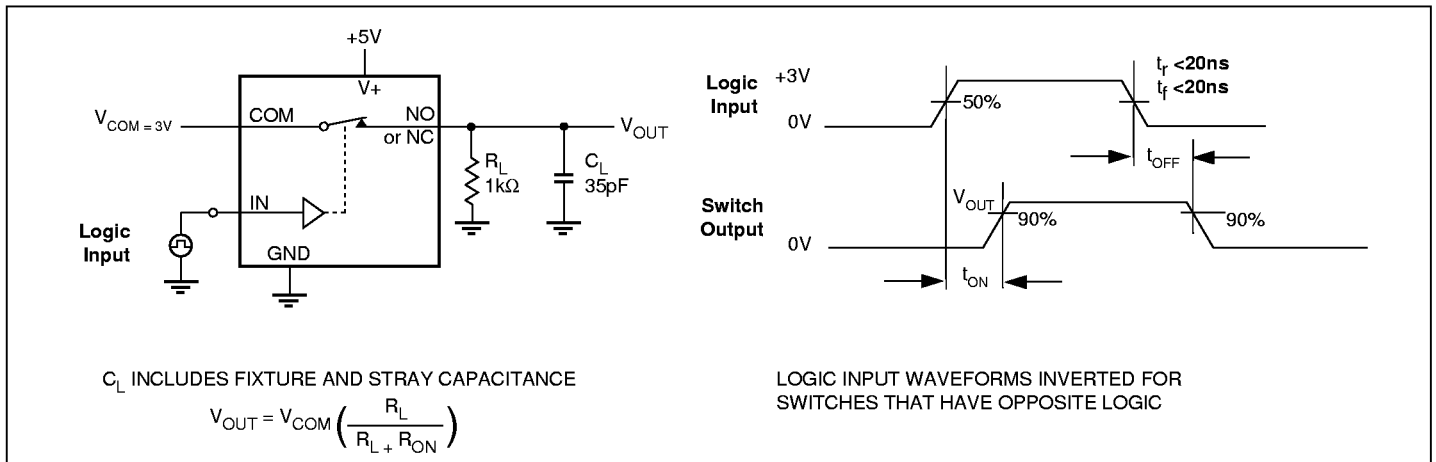


Figure 2. Switching Time

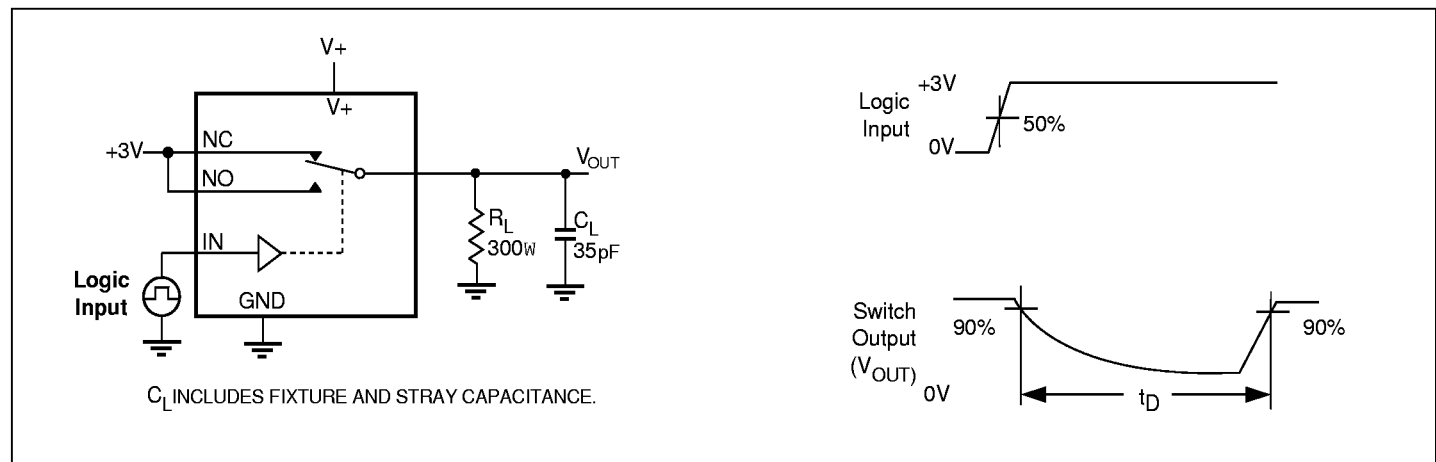


Figure 3. Break-Before-Make Interval (PS383 only)

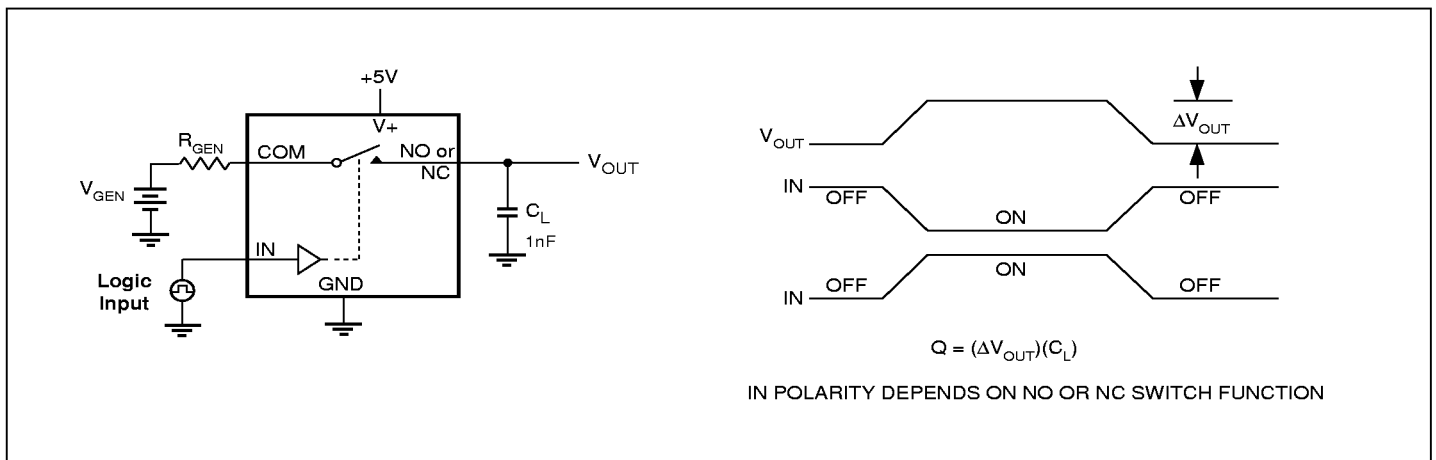


Figure 4. Charge Injection

Test Circuits/Timing Diagrams (continued)

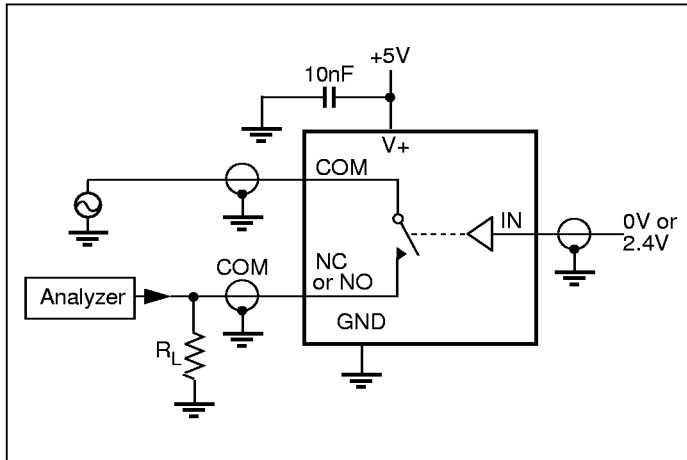


Figure 5. Off Isolation

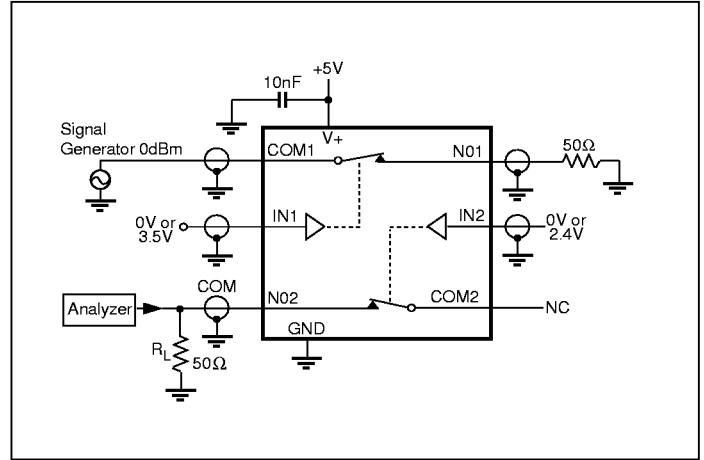


Figure 6. Crosstalk

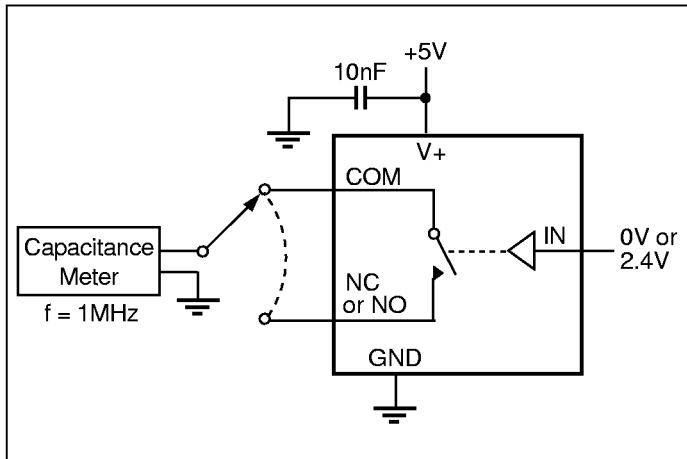


Figure 7. Channel-Off Capacitance

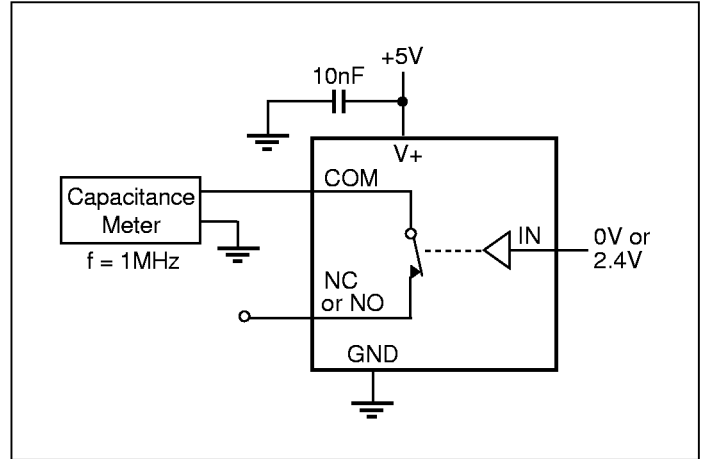


Figure 8. Channel-On Capacitance

Ordering Information

PART	Temp. Range	Package
PS320CPA	0°C to +70°C	8 Plastic DIP
PS320CSA	0°C to +70°C	8 Narrow SO
PS320CUA	0°C to +70°C	8 MSOP
PS320EPA	-40°C to +85°C	8 Plastic DIP
PS320ESA	-40°C to +85°C	8 Narrow SO
PS321CPA	0°C to +70°C	8 Plastic DIP
PS321CSA	0°C to +70°C	8 Narrow SO
PS321CUA	0°C to +70°C	8 MSOP

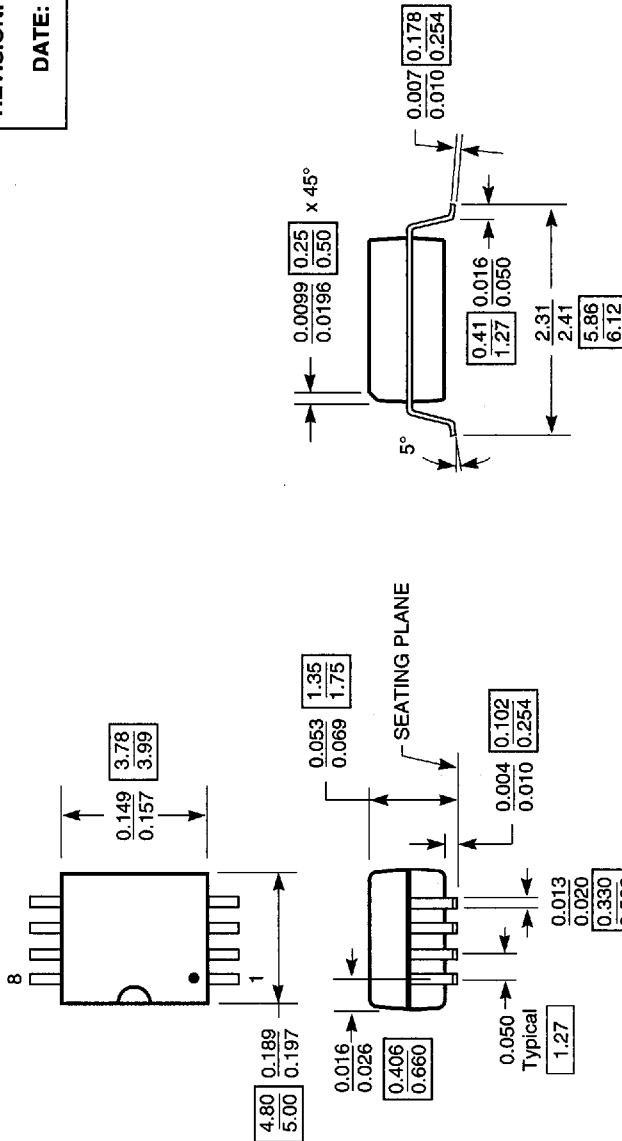
PART	Temp. Range	Package
PS321EPA	-40°C to +85°C	8 Plastic DIP
PS321ESA	-40°C to +85°C	8 Narrow SO
PS322CPA	0°C to +70°C	8 Plastic DIP
PS322CSA	0°C to +70°C	8 Narrow SO
PS322CUA	0°C to +70°C	8 MSOP
PS322EPA	-40°C to +85°C	8 Plastic DIP
PS322ESA	-40°C to +85°C	8 Narrow SO

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8-PIN SOIC (150 MIL WIDE)

PACKAGE CODE: W8

DESCRIPTION:

PACKAGE CODE: W8

DESCRIPTION:

PACKAGE CODE: W8

DESCRIPTION:

PACKAGE CODE: W8

DESCRIPTION:

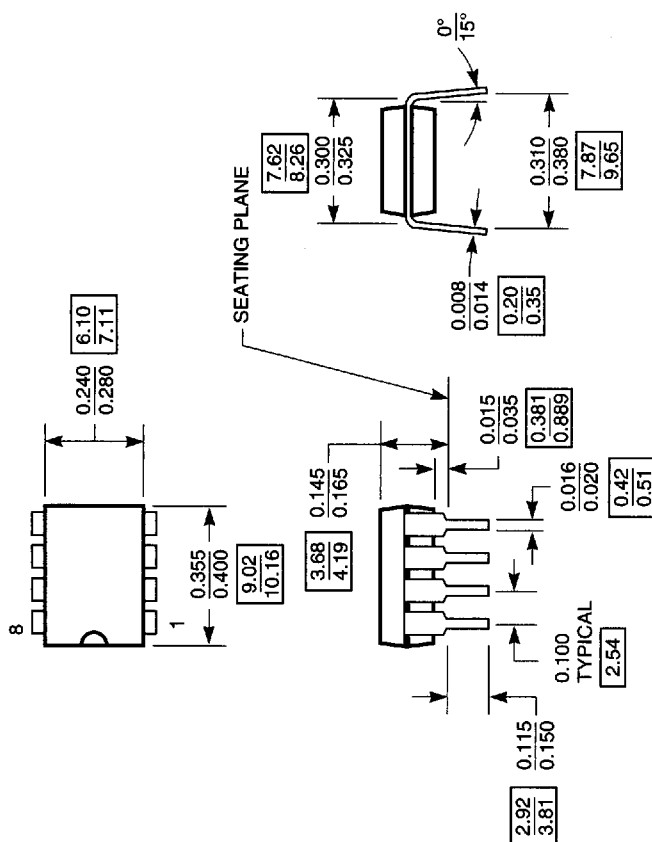
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