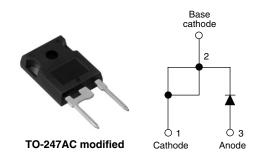


Vishay High Power Products

Input Rectifier Diode, 40 A



PRODUCT SUMMARY							
V _F at 40 A	1.1 V						
I _{FSM}	475 A						
V _{RRM}	800/1200 V						

DESCRIPTION/FEATURES

The 40EPS..PbF rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.



Typical applications are in input rectification and these products are designed to be used with Vishay HPP Switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I _{F(AV)}	Sinusoidal waveform	40	А							
V _{RRM}	Range	800/1200	V							
I _{FSM}		475	А							
V _F	40 A, T _J = 25 °C	1.1	V							
TJ		- 40 to 150	°C							

VOLTAGE RATINGS										
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA							
40EPS08PbF	800	900	1							
40EPS12PbF	1200	1300	I							

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	ARAMETER SYMBOL TEST CONDITIONS								
Maximum average forward current	I _{F(AV)}	$T_C = 105 \ ^{\circ}C$, 180° conduction half sine wave	40						
Maximum peak one cycle non-repetitive surge current	I _{FSM}	10 ms sine pulse, rated V_{RRM} applied	400	А					
		10 ms sine pulse, no voltage reapplied	475						
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	800	A ² s					
Maximum I-clor lusing	1-1	10 ms sine pulse, no voltage reapplied 1131		A-S					
Maximum I²√t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied	11 310	A²√s					

* Pb containing terminations are not RoHS compliant, exemptions may apply

40EPS..PbF High Voltage Series

Vishay High Power Products Input Rectifier Diode, 40 A



ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS				
Maximum forward voltage drop	V	20 A, T _J = 25 °C		1.0	V				
	V _{FM}	40 A, T _J = 25 °C		1.1	v				
Forward slope resistance	r _t	T.I = 150 °C		7.16	mΩ				
Threshold voltage	V _{F(TO)}	1j=150 C		0.74	V				
Maximum reverse leakage current	I _{RM}	$T_J = 25 ^{\circ}C$		0.1	mA				
Maximum reverse leakage current		T _J = 150 °C	V_{R} = Rated V_{RRM}	1.0	ША				

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storrage temperature range		T _J , T _{Stg}		- 40 to 150	°C				
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.6					
Maximum thermal resistance, junction to ambient		R _{thJA}	R _{thJA}		°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, flat, smooth and greased	0.2					
Approximate weight				6	g				
Approximate weight				0.21	oz.				
Mounting torque	minimum			6 (5)	kgf · cm				
Mounting torque –	maximum			12 (10)	(lbf ⋅ in)				
Marking device			Coop atula TO 247AC modified (JEDEC)	40EPS08					
			Case style TO-247AC modified (JEDEC)		PS12				



40EPS..PbF High Voltage Series

RMS limit

60

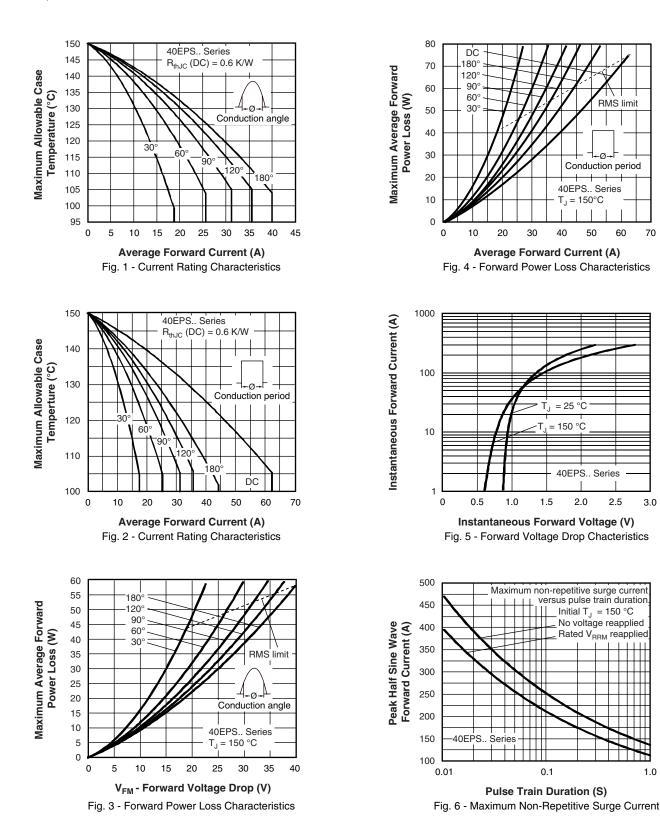
2.5

3.0

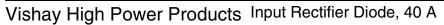
70

50

Input Rectifier Diode, 40 A Vishay High Power Products



1.0



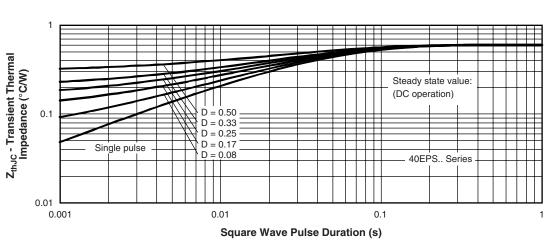


Fig. 7 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code	40	E	Р	S	12	PbF]
		2	3	4	5	6	-
	1		rent ratii cuit confi	•	,		
	3	E =	Single o	0			
	4	P =	TO-247 e of silic		lified		
	_	21	Standa		ery rect	ifier	08
	5 6		tage rati one = St	0	product	ion	12 :
		• PI	bF = Lea	ad (Pb)-i	free		

LINKS TO RELATED DOCUMENTS							
Dimensions http://www.vishay.com/doc?95253							
Part marking information	http://www.vishay.com/doc?95255						

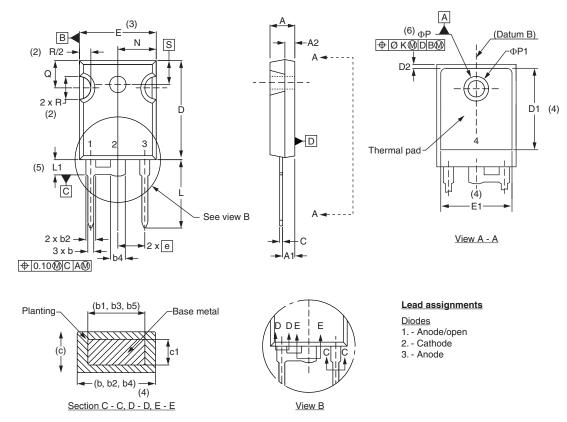
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Outline Dimensions





DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			e	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ΦK	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ΦP	3.56	3.66	0.14	0.144	
С	0.38	0.86	0.015	0.034			ΦP1	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	1.78	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

⁽¹⁾ Dimensioning and tolerance per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

(6) ΦP to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC outline TO-247 with exception of dimension c

Revision: 21-Jun-11

1

Document Number: 95253

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