**PNP/NPN Epitaxial Planar Silicon Transistors** 



## RA104S/RC104S

# Switching Applications (with Bias Resistances)

## **Applications**

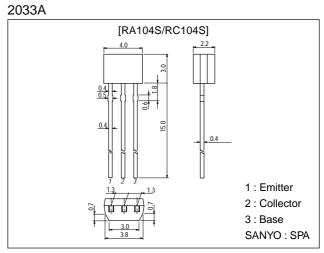
• Switching circuits, inverter circuits, interface circuits, driver circuits.

## **Features**

- · On-chip bias resistances (R1=10k $\Omega$ , R2=47k $\Omega$ ).
- $\cdot$  Compact package (SPA).

## **Package Dimensions**

unit:mm



(): RA104S

## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		(–)50	V
Collector-to-Emitter Voltage	V <sub>CEO</sub>		(–)50	V
Emitter-to-Base Voltage	VEBO		(–)6	V
Input Voltage	VIN		(-)30	V
Collector Current	ΙC		(–)100	mA
Collector Current (Pulse)	ICP		(–)200	mA
Collector Dissipation	PC		300	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector Cutoff Current	ICBO	V <sub>CB</sub> =(-)40V, I <sub>E</sub> =0			(–)0.1	μA
	ICEO	V <sub>CE</sub> =(-)40V, I <sub>B</sub> =0			(–)0.5	μA
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =(-)5V, I <sub>C</sub> =0	(–)67	(–)88	(–)125	μA
DC Current Gain	hFE	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)5mA	70			
Gain-Bandwidth Product	fT	V <sub>CE</sub> =(-)10V, I <sub>C</sub> =(-)5mA		250		MHz
				(200)		MHz
Output Capacitance	Cob	V <sub>CB</sub> =(-)10V, f=1MHz		3.5		pF
				(5.3)		pF
Marking : RA104S : A104, RC104S : C10			·	Contin	ued on n	ext page.

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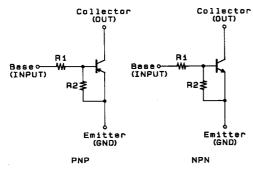
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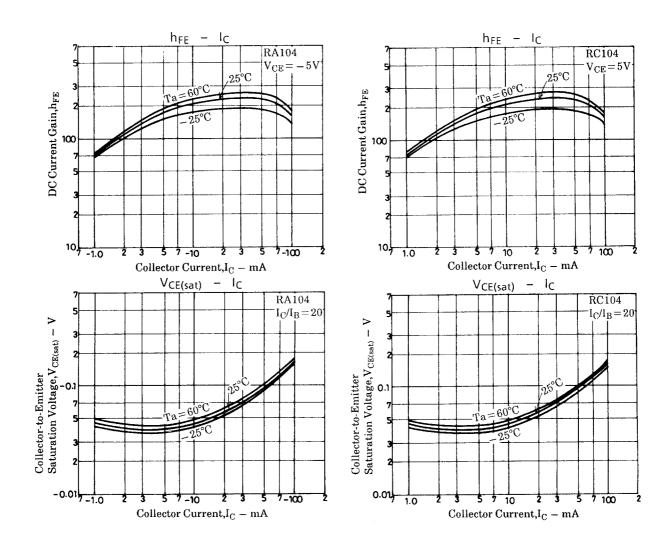
SANYO Electric Co., Ltd. Semiconductor Bussiness Headquaters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

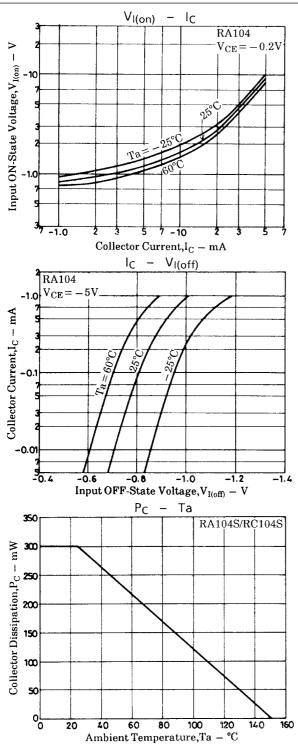
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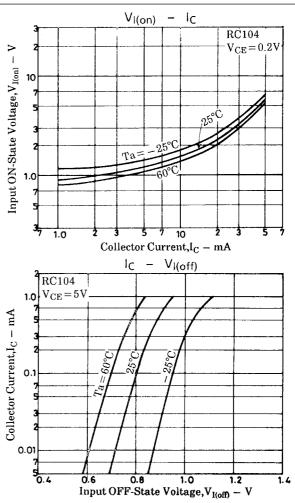
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	VCE(sat)	I <sub>C</sub> =(-)10mA, I <sub>B</sub> =(-)0.5mA		(–)0.1	(–)0.3	V
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =(-)10µA, I <sub>E</sub> =0	(–)50			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =(−)100µA, R <sub>BE</sub> =∞	(–)50			V
Input OFF-State Voltage	VIN(off)	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)100µA	(–)0.6	(–)0.8	(–)1.0	V
Input ON-State Voltage	V <sub>IN(on)</sub>	V <sub>CE</sub> =(-)0.2V, I <sub>C</sub> =(-)5mA	(–)0.7	(–)1.0	(–)2.0	V
Input Resistance	R1		7	10	13	kΩ
Resistance Ratio	R1/R2			0.213		

### **Electrical Connection**









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