

TOSHIBA Transistor  
Silicon PNP Epitaxial Type (PCT process) Silicon NPN Epitaxial Type (PCT process)

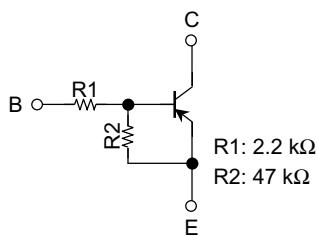
# R N 4 9 A 1

Switching, Inverter Circuit, Interface Circuit  
and Driver Circuit Applications.

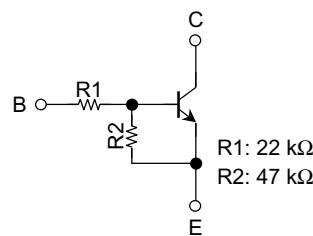
- Including two devices in US6 (ultra super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

## Equivalent Circuit and Bias Resistor Values

Q1

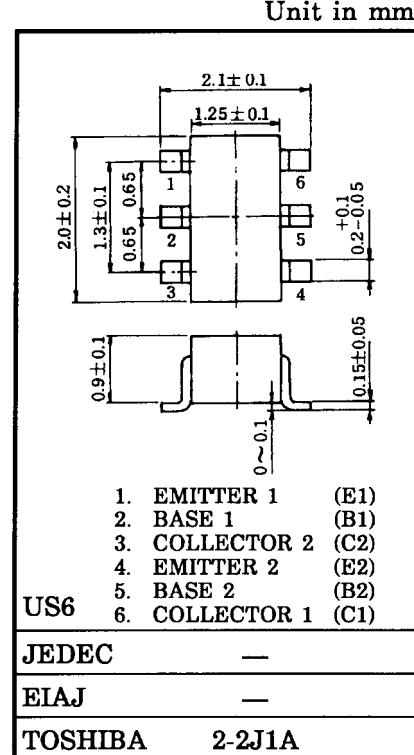


Q2



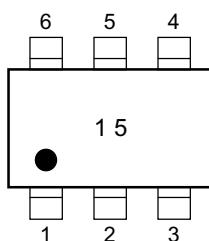
Q1: RN2105F

Q2: RN1108F

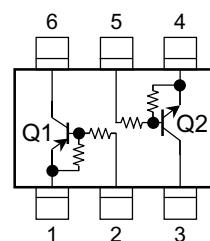


Weight : 6.8mg

## Marking Circuit (top view)



## Equivalent



- 961001EA1
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  - The information contained herein is subject to change without notice.

**Q1 Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	$V_{CEO}$	-50	V
Emitter-base voltage	$V_{EBO}$	-5	V
Collector current	$I_C$	-100	mA

**Q2 Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	50	V
Collector-emitter voltage	$V_{CEO}$	50	V
Emitter-base voltage	$V_{EBO}$	7	V
Collector current	$I_C$	100	mA

**Q1, Q2 Common Maximum Ratings ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Rating	Unit
Collector power dissipation	$P_C$ (Note)	200	mW
Junction temperature	$T_j$	150	°C
Storage temperature range	$T_{stg}$	-55 to 150	°C

Note: Total rating

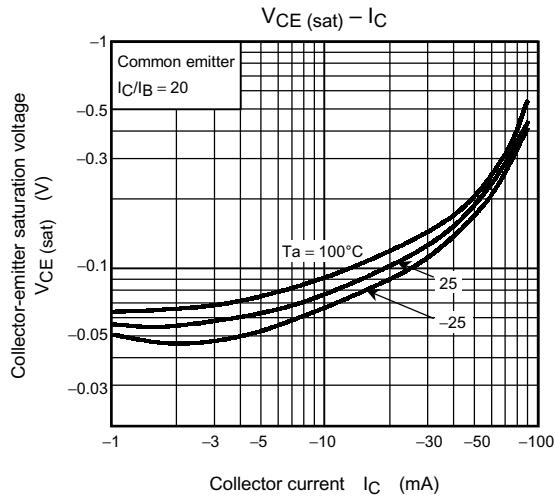
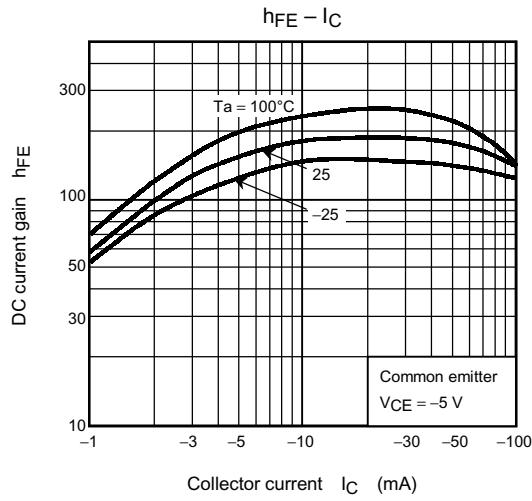
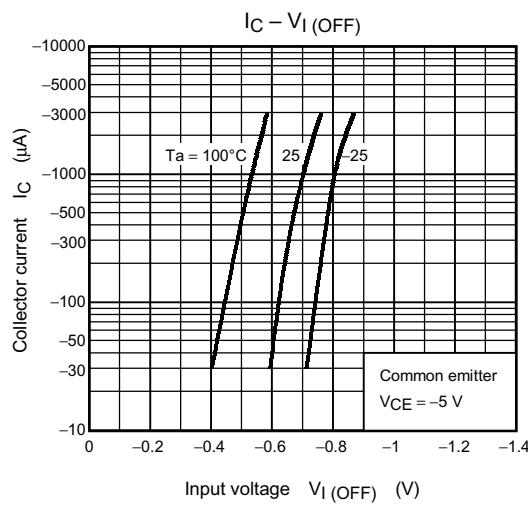
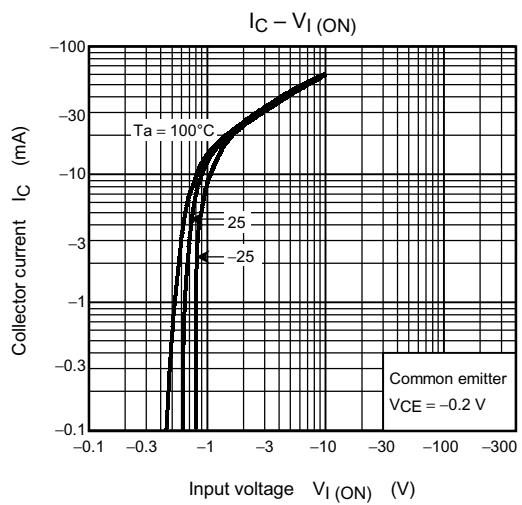
**Q1 Electrical Characteristics ( $T_a = 25^\circ C$ )**

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	$I_{CBO}$	$V_{CB} = -50 V, I_E = 0$	—	—	-100	nA
	$I_{CEO}$	$V_{CE} = -50 V, I_B = 0$	—	—	-500	
Emitter cut-off current	$I_{EBO}$	$V_{EB} = -5 V, I_C = 0$	-0.078	—	-0.145	mA
DC current gain	$h_{FE}$	$V_{CE} = -5 V, I_C = -10 mA$	80	—	—	
Collector-emitter saturation voltage	$V_{CE} (\text{sat})$	$I_C = -5 mA, I_B = -0.25 mA$	—	-0.1	-0.3	V
Input voltage (ON)	$V_I (\text{ON})$	$V_{CE} = -0.2 V, I_C = -5 mA$	-0.6	—	-1.1	V
Input voltage (OFF)	$V_I (\text{OFF})$	$V_{CE} = -5 V, I_C = -0.1 mA$	-0.5	—	-0.8	V
Transition frequency	$f_T$	$V_{CE} = -10 V, I_C = -5 mA$	—	200	—	MHz
Collector output capacitance	$C_{ob}$	$V_{CB} = -10 V, I_E = 0, f = 1 MHz$	—	3	6	pF
Input resistor	R1	—	1.54	2.2	2.86	kΩ
Resistor ratio	R1/R2	—	0.0421	0.0468	0.0515	

**Q2 Electrical Characteristics ( $T_a = 25^\circ C$ )**

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	$I_{CBO}$	$V_{CB} = 50 V, I_E = 0$	—	—	100	nA
	$I_{CEO}$	$V_{CE} = 50 V, I_B = 0$	—	—	500	
Emitter cut-off current	$I_{EBO}$	$V_{EB} = 7 V, I_C = 0$	0.078	—	0.145	mA
DC current gain	$h_{FE}$	$V_{CE} = 5 V, I_C = 10 mA$	80	—	—	
Collector-emitter saturation voltage	$V_{CE} (\text{sat})$	$I_C = 5 mA, I_B = 0.25 mA$	—	0.1	0.3	V
Input voltage (ON)	$V_I (\text{ON})$	$V_{CE} = 0.2 V, I_C = 5 mA$	1.0	—	2.6	V
Input voltage (OFF)	$V_I (\text{OFF})$	$V_{CE} = 5 V, I_C = 0.1 mA$	0.6	—	1.16	V
Transition frequency	$f_T$	$V_{CE} = 10 V, I_C = 5 mA$	—	250	—	MHz
Collector output capacitance	$C_{ob}$	$V_{CB} = 10 V, I_E = 0, f = 1 MHz$	—	3	6	pF
Input resistor	R1	—	15.4	22	28.6	kΩ
Resistor ratio	R1/R2	—	0.421	0.468	0.515	

Q1



Q2

