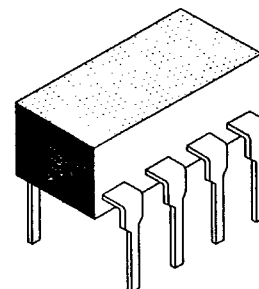


POLARA**8 PIN, MINI-DIP, TRIPLE OUTPUT, TTL ACTIVES****AVA-SERIES**

T-47-13

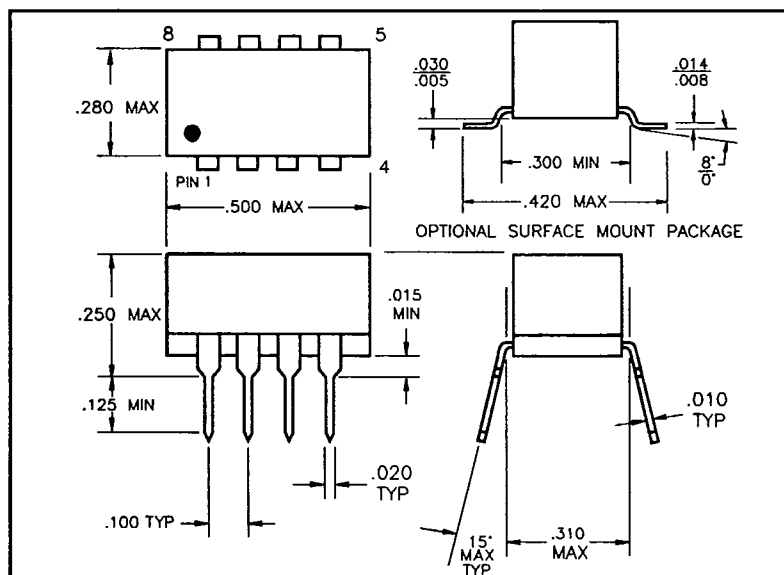
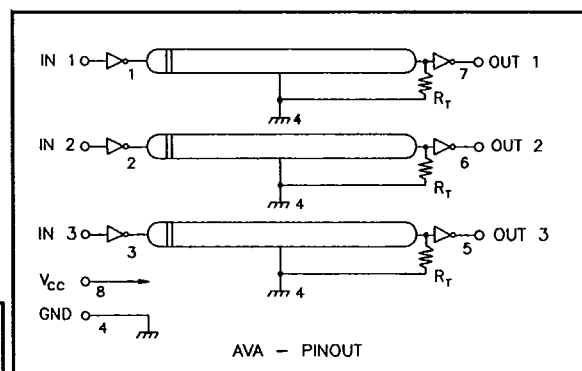
DC Electrical Characteristics		Test Conditions	Min	Max	Unit
VOH	High-Level Output Voltage	VCC=min. V _L =max. I _{OH} =max	2.7		V
VOL	Low Level Output Voltage	VCC=min. V _H =min. I _{OL} =max		0.5	V
V _{IK}	Input Clamp Voltage	VCC=min. I _I =I _{IK}		-1.2	V
I _{IH}	High-Level Input Current	VCC=max. V _{IN} =2.7V		50	uA
I _{IL}	Low-Level Input Current	VCC=max. V _{IN} =5.25V		1.0	mA
I _{OS}	Short Circuit Output Current	VCC=max. V _{OUT} =0	-40	-100	mA
I _{CCH}	High-Level Supply Current	VCC=max. V _{IN} =OPEN		75	mA
I _{CCL}	Low-Level Supply Current	VCC=max. V _{IN} =0		75	mA
T _{RO}	Output Rise Time	T _d ≤ 500ns (0.75 to 2.4 Volts)		4	ns
		T _d > 500ns		5	ns
N _H	Fanout High-Level Output	VCC=max. V _{OH} =2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	VCC=max. V _{OH} =0.5V		10 TTL LOAD	



Recommended Operating Conditions		Min	Max	Unit
VCC	Supply Voltage	4.75	5.25	V
V _{IH}	High-level Input voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle	40		%
T _A	Operating Free-Air Temperature	0	+70	°C

* These two values are inter-dependent

Input Pulse Test Conditions @ 25°C		Unit
E _{IN}	Pulse Input Voltage	3.2 Volts
PW	Pulse Width % of Total Delay	110 %
TRI	Pulse Rise Time (0.75 - 2.4 Volts)	2.0 ns
PRR	Pulse Repetition Rate @ T _d ≤ 500ns	1.0 MHz
	Pulse Repetition Rate @ T _d > 500 ns	500 KHz
VCC	Supply Voltage	5.0 Volts



DELAYS ± 2 nS OR 5%	POLARA P/N AVA - PINOUT
5	AVA-0005
10	AVA-0010
15	AVA-0015
20	AVA-0020
25	AVA-0025
30	AVA-0030
35	AVA-0035
40	AVA-0040
45	AVA-0045
50	AVA-0050
60	AVA-0060
70	AVA-0070
80	AVA-0080
90	AVA-0090
100	AVA-0100
150	AVA-0150
200	AVA-0200
250	AVA-0250

ADD "-S" SUFFIX TO P/N, TO ORDER SURFACE MOUNT PACKAGE.

DELAYS TIMES REFERENCED FROM INPUT.