

RX3034B70W

T-33-15

## PULSED MICROWAVE POWER TRANSISTORS

NPN silicon planar epitaxial microwave power transistor, intended for use in a common-base class-C broadband pulse power amplifier with a frequency range of 3.0 to 3.4 GHz.

It is recommended for radar applications.

### Features

- Interdigitated structure; giving a high emitter efficiency
- Diffused emitter ballasting resistors; capable of withstanding a high VSWR and providing excellent current sharing
- Gold metallization; ensuring excellent stability of the characteristics and giving a prolonged working life
- Multicell geometry; giving good balance of dissipated power and low thermal resistance
- Input and output matching cells; simplifying circuit design.

The transistor is housed in a metal-ceramic flange envelope (FO-125A).

### QUICK REFERENCE DATA

Microwave performance up to  $T_{mb} = 25^{\circ}\text{C}$  in an unneutralized common-base class-C broadband amplifier, typical values.

| mode of operation                                      | f GHz            | V <sub>CC</sub> V | P <sub>L</sub> W | G <sub>p</sub> dB | $\eta_C$ % | $\bar{z}_i; \bar{Z}_L$ $\Omega$ |
|--|------------------|-------------------|------------------|-------------------|------------|---------------------------------|
| class-C<br>$t_p = 100 \mu\text{s};$<br>$\delta = 10\%$ | 3.0<br>to<br>3.4 | 40                | 80               | 6                 | 35         | see Fig. 5                      |

### MECHANICAL DATA

FO-125A (see Fig. 1).

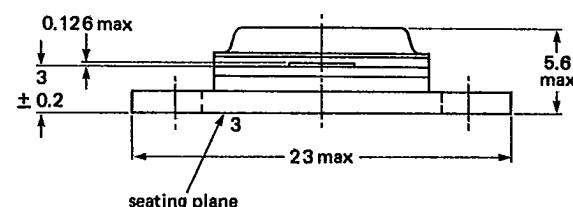
Dimensions in mm

RX3034B70W

T-33-15

**MECHANICAL DATA**

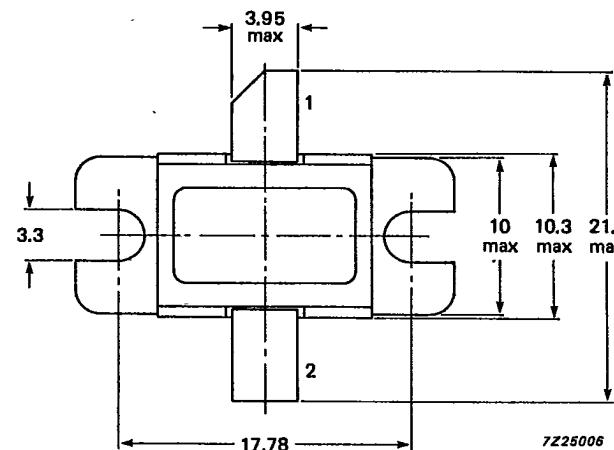
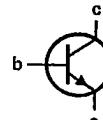
Fig. 1 FO-125A.

Base is connected  
to the seating plane**Pinning:**

1 = collector

2 = emitter

3 = base



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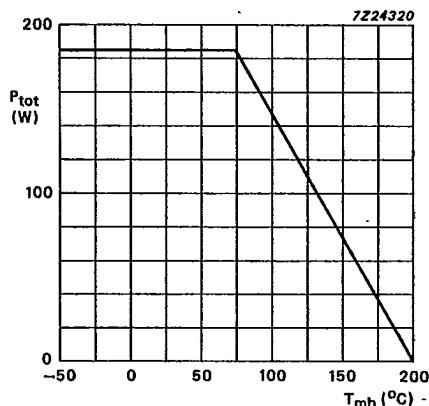
**RATINGS**

Limiting values in accordance with the Absolute Maximum System (IEC 134).

Collector-base voltage (open emitter)  $V_{CBO}$  max. 50 VCollector-emitter voltage,  
 $R_{BE} = 10 \Omega$   
open base  $V_{CER}$  max. 50 V  
 $V_{CEO}$  max. 15 VEmitter-base voltage (open collector)  $V_{EBO}$  max. 3.5 VCollector current (peak)\*  $I_C$  max. 8.5 ATotal power dissipation up to  $T_{mb} = 75^\circ\text{C}$ \*  $P_{tot}$  max. 185 WStorage temperature range  $T_{stg}$  -65 to +200  $^\circ\text{C}$ Junction temperature  $T_j$  max. 200  $^\circ\text{C}$ Soldering temperature up to 0.3 mm  
from the case;  $t_{sld} \leq 10$  s  $T_{sld}$  max. 235  $^\circ\text{C}$ **THERMAL RESISTANCE (at  $T_j = 100^\circ\text{C}$ )**From junction to mounting base  $R_{th j-mb}$  max. 1.7 K/WFrom mounting base to heatsink  $R_{th mb-h}$  typ. 0.3 K/WEquivalent thermal impedance under  
pulse microwave conditions  
 $t_p = 100 \mu\text{s}; \delta = 10\%$   $Z_{th j-mb}$  typ. 0.55 K/W

\* Maximum value under normal pulsed microwave operating conditions.

T-33-15

Fig. 2 Power derating curve;  $t_p = 100 \mu s$ ;  $\delta = 10\%$ .

## DEVELOPMENT DATA

## CHARACTERISTICS

 $T_{mb} = 25^{\circ}\text{C}$  unless otherwise specified

## Collector cut-off current

$V_{CB} = 50 \text{ V}; I_E = 0$   
 $V_{CB} = 30 \text{ V}; I_E = 0$   
 $V_{CB} = 50 \text{ V}; R_{BE} = 10 \Omega$

$I_{CBO}$  max. 30 mA  
 $I_{CBO}$  max. 150  $\mu\text{A}$   
 $I_{CER}$  max. 150 mA

## Emitter cut-off current

$V_{EB} = 1.5 \text{ V}; I_C = 0$   
 $V_{EB} = 3.5 \text{ V}; I_C = 0$

$I_{EBO}$  max. 150  $\mu\text{A}$   
 $I_{EBO}$  max. 7 mA

## APPLICATION INFORMATION

Microwave performance up to  $T_{mb} = 25^{\circ}\text{C}$  in a class-C broadband amplifier under pulsed conditions.

| mode of operation                                 | f GHz            | $V_{CC}^*$ V | $P_L$ W   | $G_P$ dB   | $\eta_C$ % | $\bar{z}_I, \bar{z}_L$ $\Omega$ |
|---|------------------|--------------|-----------|------------|------------|---------------------------------|
| class-C<br>$t_p = 100 \mu s$ ;<br>$\delta = 10\%$ | 3.0<br>to<br>3.4 | 40           | $\geq 70$ | $\geq 5.4$ | $\geq 30$  | see Fig. 5                      |

\* During pulse.

RX3034B70W

T-33-15

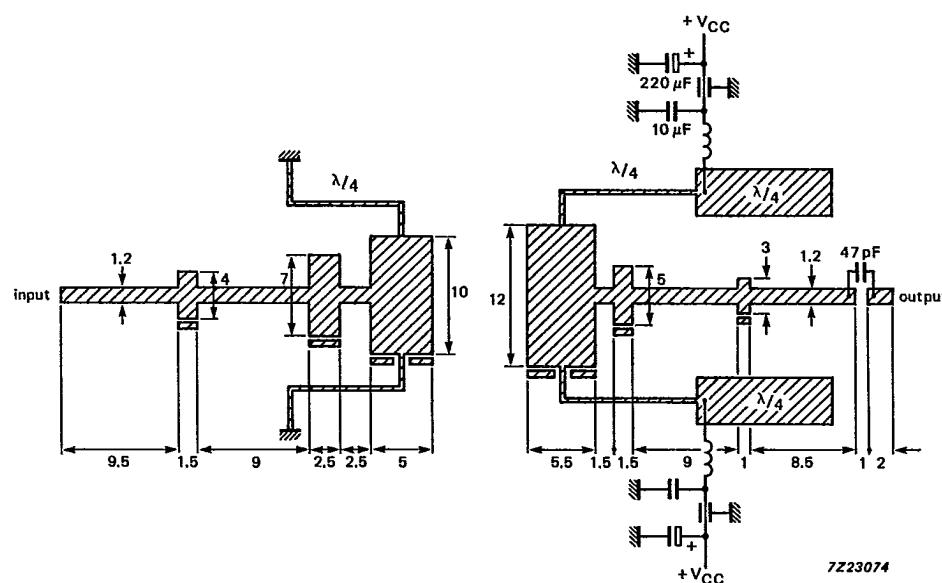


Fig. 3 Broadband test circuit for 3.0 to 3.4 GHz. (dimensions in mm).  
PTFE fibreglass printed circuit board;  $\epsilon_r = 2.55$ ; thickness 0.4 mm.

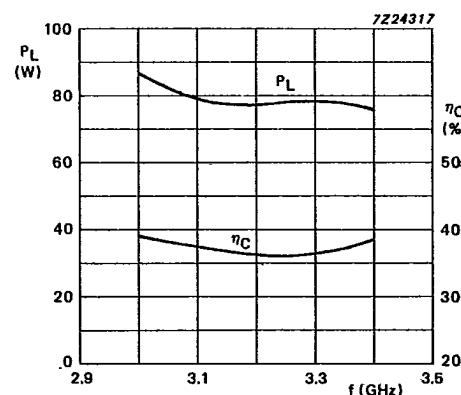


Fig. 4 Load power and collector efficiency as a function of frequency\*;  
 $V_{CC} = 40$  V,  $t_p = 100 \mu s$ ;  $\delta = 10\%$ ; typical values.

\* In a broadband test circuit as shown in Fig. 3.

Pulsed microwave power transistor

RX3034B70W

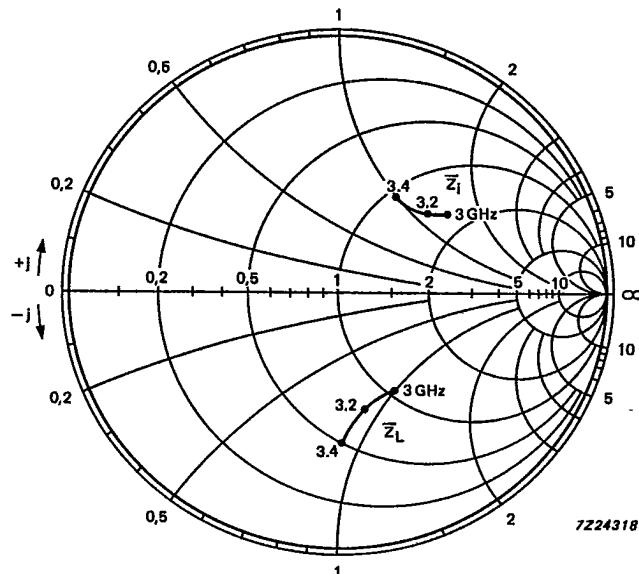
T-33-15

Fig. 5 Input and optimum load impedance as a function of frequency;  
 $Z_0 = 5 \Omega$ ;  $V_{CC} = 40$  V; typical values.

DEVELOPMENT DATA

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347