BA3819F

Comparator array, five point, with power on circuit

The BA3819F is a mechanical controller for 3 V cassette tape recorders.

It consists of five comparators and a comparator reference level circuit together with a power-on circuit.

The power-on circuit allows the comparator circuit to be switched off when there is no input.

By using the BA3819F and a few external components, a soft touch mechanical control can be built for use with 2-line remote control units.

It can also be used as an LED level meter driver.

Features

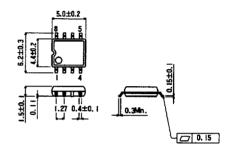
- available in a SOP8 package
- low voltage operation, 2.0 ~ 5.0V
- large output current, I_{Omax} = 5 mA
- has a standby mode in which operating current is 0 mA

Applications

- cassette player mechanical control
- headphone stereos
- portable CD players
- 2-wire remote control system
- LED dot display level meter

Dimensions (Units: mm)

BA3819F (SOP8)



Block diagram

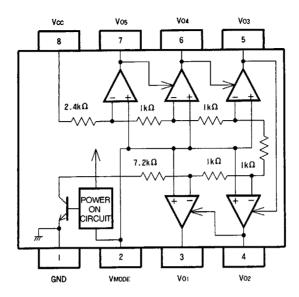
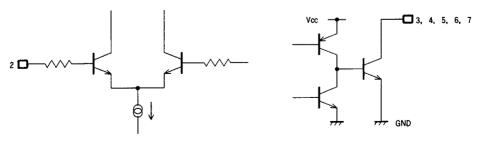


Table 1 Pin description

Pin no	Pin name	Function			
1	GND	Substrate GND			
2	V _{MODE}	Comparator input			
3	V ₀₁	Comparator 1 output			
4	V ₀₂	Comparator 2 output			
5	V ₀₃	Comparator 3 output			
6	V ₀₄	Comparator 4 output			
7	V ₀₅	Comparator 5 output			
8	V _{CC}	Power supply			

Input and output equivalent circuits



Absolute maximum ratings ($T_a = 25^{\circ}C$)

Parameter	Symbol	Limits	Unit	Conditions		
Power supply voltage	V _{CC}	6.0	V			
Output current	l _{out}	5	mA			
Power dissipation	P _d	450	mW	Reduce power by 4.5 mW/°C for each degree above 25°C.		
Operating temperature	T _{opr}	-25 ~ +75	°C			
Storage temperature	T _{stg}	-55 ~ +125	°C			

Recommended operating conditions ($T_a = 25^{\circ}C$)

Parameter	Symbol	Limits	Unit
Power supply voltage	v_{cc}	2.0 ~ 5.0	٧

Electrical characteristics (unless otherwise noted, $T_a = 25$ °C, $V_{CC} = 3$ V)

Parameter	Symbol	Min	Typical	Max	Unit	Conditions
Circuit current 1	I _{CC1}		0	10	μА	$V_{MODE} = 0 V$
Circuit current 2	I _{CC2}	1.4	3.1	4.8	mA	V _{MODE} = 3 V
Comparator level 1	V _{C1}	1.52	1.58	1.64	V	$R_L = 560 \Omega$
Comparator level 2	V _{C2}	1.74	1.80	1.86	V	$R_L = 560 \Omega$
Comparator level 3	V _{C3}	1.97	2.03	2.09	٧	$R_L = 560 \Omega$
Comparator level 4	V _{C4}	2.20	2.26	2.32	V	$R_L = 560 \Omega$
Comparator level 5	V _{C5}	2.42	2.48	2.54	٧	$R_L = 560 \Omega$
Output saturation voltage	V _{OL}		0.12	0.5	٧	$R_L = 560 \Omega$

Figure 1 Test circuit

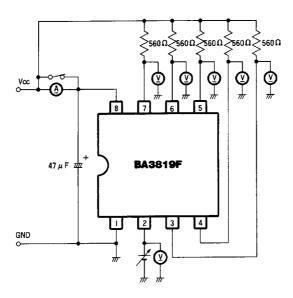
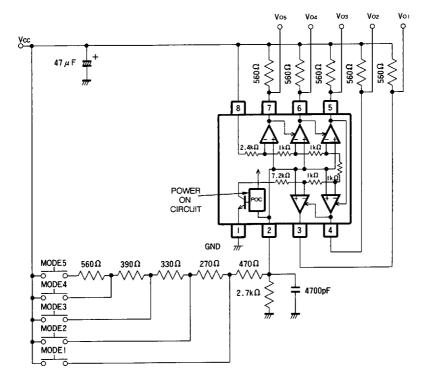


Figure 2 Application example



Circuit operation

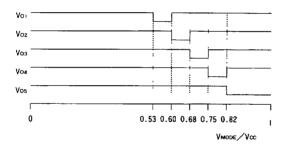
The input voltage from the V_{MODE} pin and the standard voltage from the comparator standard level circuit are inputs into the two pins of each comparator respectively. The output is an open collector. It is pulled up using the external resistor.

When the input voltage is lower than the standard voltage, the output transistor is turned OFF by the internal comparator. Consequently, the comparator output is HIGH.

In contrast, when the input voltage is higher than the standard voltage, the output transistor is ON. Consequently, the comparator output is LOW. In this case, the output transistor of the lower comparator is muted, that is, the lower comparator output is HIGH. Therefore, only one output of the five is LOW.

If the input voltage is below about $0.8~\mathrm{V}$, the standby function becomes active and the entire circuit is powered off.

Figure 3 Input and output voltage data



Precautions for use

External resistor precision

The external resistors are used to set the input voltage by their ratios. Consequently, particularly in low voltage operation, the differences in the resistor value can have a significant effect on the comparator operation. Therefore, use resistors with a precision better than 1% (0.5% is recommended).

Input voltage setting

Make sure to set the intermediate value of the two values of the comparator level as a guideline for the input voltage. Because the output level may become unstable, do not make the input voltage the same as the standard voltage.

Electrical characteristic curves

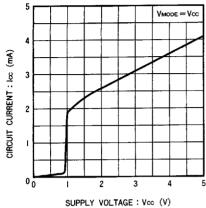
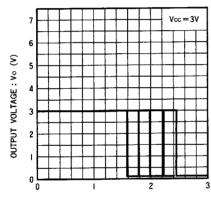
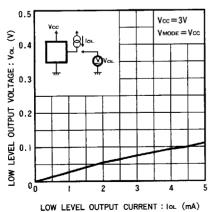


Figure 4

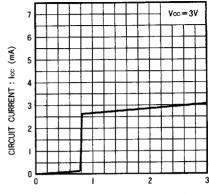


MODE INPUT VOLTAGE: VMODE (V)
Figure 6



W LEVEL OUTPUT CORRENT : TOL (III)

Figure 8



MODE INPUT VOLTAGE : VMODE (V)



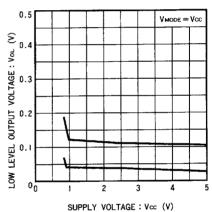


Figure 7