

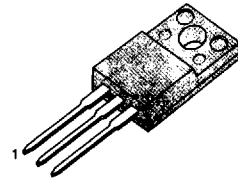
FEATURES

- Extremely Lower $R_{DS(on)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability

PRODUCT SUMMARY

Part Number	V _{DS}	R _{DS(on)}	I _D
SSS60N06	60V	0.018 Ω	36A
SSS60N05	50V	0.018 Ω	36A

TO-220F



1.Gate 2.Drain 3.Source

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	SSS60N06	SSS60N05	Unit
Drain-Source Voltage (1)	V _{DS}	60	50	V _{dc}
Drain-Gate Voltage ($R_{GS}=1.0M\Omega$) (1)	V _{DGR}	60	50	V _{dc}
Gate-Source Voltage	V _{GS}	± 20		V _{dc}
Continuous Drain Current T _C =25 °C	I _D	36		A _{dc}
Continuous Drain Current T _C =100 °C	I _D	25.2		A _{dc}
Drain Current - Pulsed (3)	I _{DM}	240		A _{dc}
Gate Current - Pulsed	I _{GM}	± 1.5		A _{dc}
Single Pulsed Avalanche Energy (4)	E _{AS}	216		mJ
Avalanche Current	I _{AS}	36		A
Total Power Dissipation at T _C =25 °C	P _D	52		Watts
Derate above 25 °C		0.34		W/°C
Operating and Storage Junction Temperature Range	T _J , T _{STG}	-55 to +150		°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T _L	300		°C

Notes : (1) T_J=25°C to 175°C(2) Pulse test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by max. junction temperature

(4) L=60mH, V_{dd}=30V, R_G=25 Ω , Starting T_J=25°C

ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage					
	SSS60N06	60	-	-	V	V _{GS} =0V, I _D =250 μ A
	SSS60N05	50	-	-	V	
V _{GS(th)}	Gate Threshold Voltage	2.0	-	4.0	V	V _{DS} =V _{GS} , I _D =1mA
I _{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V _{GS} =20V
I _{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V _{GS} =-20V
I _{DSS}	Zero Gate Voltage Drain Current	-	-	50	μ A	V _{DS} =Max. Rating, V _{GS} =0V
		-	-	250	μ A	V _{DS} =0.8 Max. Rating, V _{GS} =0V, T _C =125 $^\circ$ C
R _{DS(on)}	Static Drain-Source On-Resistance(2)	-	-	0.018	Ω	V _{GS} =10V, I _D =30A
g _{fs}	Forward Transconductance (2)	20	-	-	Ω	V _{DS} $\geq$ 15V, I _D =30A
C _{iss}	Input Capacitance	-	3500	-	pF	V _{GS} =0V, V _{DS} =25V, f=1.0MHz
C _{oss}	Output Capacitance	-	1020	-	pF	
C _{rss}	Reverse Transfer Capacitance	-	170	-	pF	
t _{d(on)}	Turn-On Delay Time	-	20	35	ns	V _{DD} =25V, I _D =60A, Z ₀ =9.1 Ω (MOSFET switching times are essentially independent of operating temperature)
t _r	Rise Time	-	10	25	ns	
t _{d(off)}	Turn-Off Delay Time	-	45	60	ns	
t _f	Fall Time	-	45	60	ns	
Q _g	Total Gate Charge (Gate-Source Plus Gate-Drain)	-	-	120	nC	V _{GS} =10V, I _D =60A, V _{DS} =0.8 Max. Rating (Gate charge is essentially independent of operating temperature)
Q _{gs}	Gate-Source Charge	-	20	-	nC	
Q _{gd}	Gate-Drain ("Miller") Charge	-	30	-	nC	

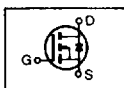
THERMAL RESISTANCE

Symbol	Characteristics		All	Units	Remark
R _{thJC}	Junction-to-Case	MAX	2.90	K/W	
R _{thCS}	Case-to-Sink	TYP	0.5	K/W	Mounting surface flat smooth, and greased
R _{thJA}	Junction-to-Ambient	MAX	62.5	K/W	Typical socket mount

Notes : (1) T_J=25 $^\circ$ C to 175 $^\circ$ C(2) Pulse test : Pulse width $\leq$ 300 μ s, Duty Cycle $\leq$ 2%

(3) Repetitive rating : Pulse width limited by max. junction temperature

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	-	-	60	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
I_{SM}	Pulse Source Current (Body Diode) (3)	-	-	240	A	
V_{SD}	Diode Forward Voltage (2)	-	-	2	V	$T_J=25^\circ\text{C}$, $I_S=60\text{A}$, $V_{GS}=0\text{V}$
t_{rr}	Reverse Recovery Time	-	160	-	ns	$T_J=25^\circ\text{C}$, $I_F=60\text{A}$, $dI_F/dt=100\text{A}/\mu\text{S}$

Notes : (1) $T_J=25^\circ\text{C}$ to 175°C (2) Pulse test : Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by max. junction temperature

