

HIGH PERFORMANCE BRUSHLESS DC MOTOR DRIVER

BC10/BC20

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PRELIMINARY

FEATURES

- 10V TO 500V MOTOR SUPPLY AT 20 A CONTINUOUS AND 40 A PEAK OUTPUT CURRENT, BC20
- 10V TO 100V MOTOR SUPPLY AT 10 A CONTINUOUS AND 20 A PEAK OUTPUT CURRENT, BC10
- OPERATION WITH 10.8V TO 16V VCC, ALLOWING NOMINAL 12V OR 15 V VCC SUPPLIES
- THREE PHASE FULL BRIDGE OPERATION WITH 2 OR 4 QUADRANT PWM
- AUTOMATIC BRAKING WHEN USING 2 QUADRANT PWM
- THERMAL PROTECTION
- TOP AND BOTTOM RAIL SHORT CIRCUIT PROTECTION
- ANTI SHOOT THROUGH DESIGN
- 50 KHZ INTERNALLY SET PWM FREQUENCY, WHICH MAY BE LOWERED WITH EXTERNAL CAPACITOR S
- SELECTABLE 60° OR 120° COMMUTATION SEQUENCES
- COMMUTATION TRANSITIONS OUTPUT FOR DERIVING SPEED CONTROL
- MAY BE USED OPEN LOOP, OR WITHIN A FEEDBACK LOOP
- ANALOG MOTOR CURRENT MONITOR OUTPUT, MAY BE USED FOR TORQUE CONTROL OR FOR TRANSCONDUCTANCE AMPLIFIER DRIVE.
- ANALOG REFERENCE, FEEDBACK, AND TORQUE INPUTS

APPLICATIONS

• 3 PHASE BRUSHLESS MOTOR CONTROL



DESCRIPTION

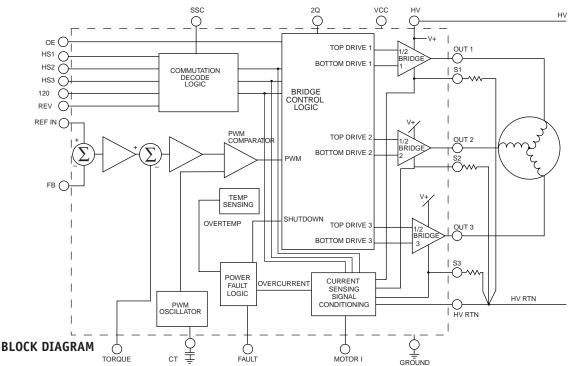
The BC10 and BC20 Brushless DC Motor Controllers provide the necessary functions to control conventional 3 phase brushless dc motors in an open loop or closed loop system. The BC10 is able to control motors requiring up to 1 kW continuous input power; the BC20 is able to control larger motors requiring up to 10 kW continuous input power.

Both controllers drive the motors, generate the PWM, decode the commutation patterns, multiplex the current sense, and provide error amplification. Operation with either 60° or 120° commutation patterns may be selected with a logic input.

Current sense multiplexing is used to make the current monitor output always proportional to the active motor coils current. Therefore the current monitor output may be used in generating transconductance drive for easy servo compensation.

The controller may generate 4-quadrant PWM for applications requiring continuous transition through zero velocity, or 2 quadrant PWM for electrically quieter operation in unidirectional applications. Direction of rotation may be reversed in 2-quadrant mode by using the reverse command input. When in 2-quadrant mode if the motor is stopped or decelerating dynamic braking is automatically applied. In this way deceleration profiles may be fol-

lowed even when using 2-quadrant PWM.



BC10/BC20

		BC10	BC20
ABSOLUTE MAXIMUM RATINGS	MOTOR VOLTAGE, V+ CIRCUIT SUPPLY, Vcc OUTPUT CURRENT, peak OUTPUT CURRENT, continuos POWER DISSIPATION, internal ANALOG INPUT VOLTAGE DIGITAL INPUT VOLTAGE TEMPERATURE, pin solder, 10s TEMPERATURE, junction ¹ TEMPERATURE RANGE, storage	100V 16V 20A 10 A TBD -0.3V to Vcc+0.3V -0.3V to 5.35V 300°C 150°C -65 to 150°C	500V 16V 40A 20A TDB -0.3V to Vcc +0.3 03V tp 5.35V 300°C 150°C -65 to 150°C
	OPERATING TEMPERATURE, case	−25 to 85°C	-25 to 85°C
SPECIFICATIONS	OPERATING TEMPERATURE, case	-25 to 85°C	-25 to 85°C

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
ERROR AMP OFFSET VOLTAGE BIAS CURRENT GAIN, DC ² COMMON MODE VOLTAGE RANGE UNITY GAIN BANDWIDTH		19.8 0	20	3.3 4 20.2 Vcc-2.5	mV pA db V MHz
INPUT AMP STAGE GAIN ² INPUT IMPEDANCE ² COMMON MODE VOLTAGE COMMON MODE REJECTION DIFFERENTIAL OFFSET UNITY GAIN BANDWIDTH		9.5 46	10 2 Vcc/2 80 TBD 1.4	10.5	V/V Kohm db MHz
OUTPUT TOTAL Ron EFFICIANCY, 10A SWITCHING FREQUENCY CURRENT, continuous, BC10 CURRENT, peak, BC10 CURRENT, continuous, BC20 CURRENT, peak, BC20 UNITY GAIN BANDWIDTH		10 20 20 40	TBD HIGH 50	10 20 20 40	Khz A A A A MHz
POWER SUPPLY VOLTAGE, V+ (BC10) VOLTAGE, V+ (BC20) VOLTAGE, Vcc		10 10 10.8		100 500 16	V V V

Long term operation at the maximum junction temperature will result in reduced product life. Derate internal power dissipation to achieve high MTTF.

CAUTION

The BC10 and BC20 are constructed from static sensitive components. ESD handling procedures must be observed.

^{2.} Set internally.

TYPICAL PERFORMANCE

PIN FUNCTION

SIGNAL	DESCRIPTION	PIN	
	· · · · · · · · · · · · · · · · · · ·		
		tbd	
	0 1	tbd	
OUT3	Half bridge output for driving motor coil	tbd	
S1	Source of the N-rail FET in half bridge 1	tbd	
S2	Source of the N-rail FET in half bridge 2	tbd	
S3	Source of the N-rail FET in half bridge 3	tbd	
HS1	Commutation sensor input 1	tbd	
HS2	Commutation sensor input 2	tbd	
HS3	Commutation sensor input 3	tbd	
120	Sets commutation logic for 120 0 phasing	tbd	
REV	Reverses direction when 2 quadrant PWM is used	tbd	
GROUND	Signal ground	tbd	
Vcc	Control circuit power	tbd	
REF IN	Velocity/speed input	tbd	
FB	Input for analog voltage proportional to velocity or speed	tbd	
TORQUE	Input for an analog voltage proportional to motor current	tbd	
MOTOR I	Analog voltage proportional to motor current	tbd	
SSC	HCMOS level pulse for each sensor state change.	tbd	
FAULT	HCMOS logic level output, a 0 indicates over temperature	tbd	
	or over current condition.		
OE	HCMOS 1 enables power FET operation	tbd	
CT	The PWM frequency may be lowered by installing a	tbd	
	capacitor between this output and ground.		
2Q	A logic 1 on this input enables 2 quadrant PWM	tbd	
	HV HVRTN OUT1 OUT2 OUT3 S1 S2 S3 HS1 HS2 HS3 120 REV GROUND Vcc REF IN FB TORQUE MOTOR I SSC FAULT OE CT	HV Unregulated high current motor supply voltage HVRTN Return line for the high motor current OUT1 Half bridge output for driving motor coil OUT2 Half bridge output for driving motor coil OUT3 Half bridge output for driving motor coil S1 Source of the N-rail FET in half bridge 1 S2 Source of the N-rail FET in half bridge 2 S3 Source of the N-rail FET in half bridge 3 HS1 Commutation sensor input 1 HS2 Commutation sensor input 2 HS3 Commutation sensor input 3 120 Sets commutation logic for 120 0 phasing REV Reverses direction when 2 quadrant PWM is used GROUND Signal ground Vcc Control circuit power REF IN Velocity/speed input FB Input for analog voltage proportional to velocity or speed TORQUE Input for an analog voltage proportional to motor current MOTOR I Analog voltage proportional to motor current SC HCMOS level pulse for each sensor state change. FAULT HCMOS logic level output, a 0 indicates over temperature or over current condition. OE HCMOS 1 enables power FET operation CT The PWM frequency may be lowered by installing a capacitor between this output and ground.	HV Unregulated high current motor supply voltage tbd HVRTN Return line for the high motor current tbd OUT1 Half bridge output for driving motor coil tbd OUT2 Half bridge output for driving motor coil tbd OUT3 Half bridge output for driving motor coil tbd OUT3 Half bridge output for driving motor coil tbd S1 Source of the N-rail FET in half bridge 1 tbd S2 Source of the N-rail FET in half bridge 2 tbd S3 Source of the N-rail FET in half bridge 3 tbd HS1 Commutation sensor input 1 tbd HS2 Commutation sensor input 2 tbd HS3 Commutation sensor input 2 tbd HS3 Commutation sensor input 3 tbd S20 Sets commutation logic for 120 0 phasing tbd REV Reverses direction when 2 quadrant PWM is used tbd GROUND Signal ground tbd Vcc Control circuit power tbd REF IN Velocity/speed input for All pour for an analog voltage proportional to velocity or speed tbd TORQUE Input for an analog voltage proportional to motor current tbd MOTOR I Analog voltage proportional to motor current tbd MOTOR I Analog voltage proportional to motor current tbd SSC HCMOS level pulse for each sensor state change. tbd FAULT HCMOS logic level output, a 0 indicates over temperature or over current condition. OE HCMOS 1 enables power FET operation tbd CT The PWM frequency may be lowered by installing a capacitor between this output and ground.