

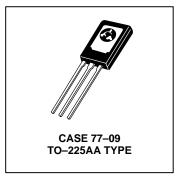
Plastic Medium Power Silicon PNP Transistor

... designed for use as audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

- DC Current Gain $h_{FE} = 40$ (Min) @ $I_C = 0.15$ Adc
- BD 136, 138, 140 are complementary with BD 135, 137, 139

BD136 BD138 BD140 BD140-10

1.5 AMPERE
POWER TRANSISTORS
PNP SILICON
45, 60, 80 VOLTS
10 WATTS



MAXIMUM RATINGS

Rating	Symbol	Туре	Value	Unit
Collector–Emitter Voltage	V _{CEO}	BD 136 BD 138 BD 140	45 60 80	Vdc
Collector–Base Voltage	V _{CBO}	BD 136 BD 138 BD 140	45 60 100	Vdc
Emitter–Base Voltage	V _{EBO}		5	Vdc
Collector Current	I _C		1.5	Adc
Base Current	I _B		0.5	Adc
Total Device Dissipation@ T _A = 25°C Derate above 25°C	P _D		1.25 10	Watts mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D		12.5 100	Watt mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}		-55 to +150	°C

BD136 BD138 BD140 BD140-10

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$\theta_{\sf JC}$	10	°C/W
Thermal Resistance, Junction to Ambient	θ_{JA}	100	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Туре	Min	Max	Unit
Collector–Emitter Sustaining Voltage* (I _C = 0.03 Adc, I _B = 0)		BV _{CEO}	BD 136 BD 138 BD 140	45 60 80	_ _ _	Vdc
Collector Cutoff Current $(V_{CB} = 30 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 30 \text{ Vdc}, I_E = 0, T_C = 125 ^{\circ}\text{C})$		I _{CBO}			0.1 10	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)		I _{EBO}		_	10	μAdc
DC Current Gain (I _C = 0.005 A, V _{CE} = 2 V)	ALL	h _{FE} *		25 40	 250	_
$(I_C = 0.15 \text{ A}, V_{CE} = 2 \text{ V})$ $(I_C = 0.5 \text{ A}, V_{CE} = 2 \text{ V})$	ALL BD140–10			63 25	160 —	
Collector–Emitter Saturation Voltage* (I _C = 0.5 Adc, I _B = 0.05 Adc)		V _{CE(sat)} *		_	0.5	Vdc
Base–Emitter On Voltage* (I _C = 0.5 Adc, V _{CE} = 2.0 Vdc)		V _{BE(on)} *		_	1	Vdc

^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

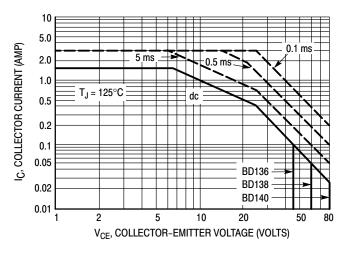
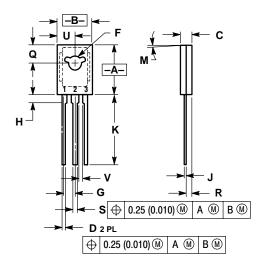


Figure 1. Active-Region Safe Operating Area

BD136 BD138 BD140 BD140-10

PACKAGE DIMENSIONS

TO-225AA **CASE 77-09 ISSUE W**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.425	0.435	10.80	11.04
В	0.295	0.305	7.50	7.74
С	0.095	0.105	2.42	2.66
D	0.020	0.026	0.51	0.66
F	0.115	0.130	2.93	3.30
G	0.094 BSC		2.39 BSC	
Н	0.050	0.095	1.27	2.41
J	0.015	0.025	0.39	0.63
K	0.575	0.655	14.61	16.63
M	5° TYP		5° TYP	
Q	0.148	0.158	3.76	4.01
R	0.045	0.065	1.15	1.65
S	0.025	0.035	0.64	0.88
U	0.145	0.155	3.69	3.93
٧	0.040		1.02	

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

NORTH AMERICA Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

Fax Response Line: 303-675-2167 or 800-344-3810 Toll Free USA/Canada

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

EUROPE: LDC for ON Semiconductor – European Support

German Phone: (+1) 303–308–7140 (Mon–Fri 2:30pm to 7:00pm CET)

Email: ONlit-german@hibbertco.com

French Phone: (+1) 303–308–7141 (Mon–Fri 2:00pm to 7:00pm CET)

Email: ONlit-french@hibbertco.com

English Phone: (+1) 303–308–7142 (Mon–Fri 12:00pm to 5:00pm GMT)

Email: ONlit@hibbertco.com

EUROPEAN TOLL-FREE ACCESS*: 00-800-4422-3781

*Available from Germany, France, Italy, UK, Ireland

CENTRAL/SOUTH AMERICA:

Spanish Phone: 303-308-7143 (Mon-Fri 8:00am to 5:00pm MST)

Email: ONlit-spanish@hibbertco.com

Toll-Free from Mexico: Dial 01-800-288-2872 for Access -

then Dial 866-297-9322

ASIA/PACIFIC: LDC for ON Semiconductor - Asia Support

Phone: 1-303-675-2121 (Tue-Fri 9:00am to 1:00pm, Hong Kong Time)

Toll Free from Hong Kong & Singapore:

001-800-4422-3781 Email: ONlit-asia@hibbertco.com

JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031

Phone: 81–3–5740–2700 **Email**: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local

Sales Representative.