

ZLDO330

ZLDO330

ABSOLUTE MAXIMUM RATING

Input Supply		Output Current	300mA
Voltage Range	-0.3 to 20V	Operating Temperature	-40 to 85°C
Shutdown Input		Storage Temperature	-55 to 150°C
Voltage Range	-0.3 to V_{in}	Power Dissipation	2W (Note 1)
Low Battery Output			
Voltage Range	-0.3 to 20V		

ELECTRICAL CHARACTERISTICS

TEST CONDITIONS (Unless otherwise stated) $T_{amb}=25^{\circ}\text{C}$, $I_L=10\text{mA}$, $C_s=10\text{pF}$, $C_{out}=1\mu\text{F}$

Parameter	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output voltage	V_o	$V_{in}=4.3\text{V}$	3.2	3.3	3.4	V
Output voltage temperature coefficient	$\frac{V_o}{\Delta T}$	$V_{in}=4.3\text{V}$ (Note 2, Note 4)		100	250	ppm/°C
Line regulation	ΔV_o	$V_{in}=4.3$ to 20V		15	52	mV
Load regulation	ΔV_o	$I_L=10$ to 300mA $V_{in}=4.3\text{V}$		45	78	mV
Dropout voltage (Note3)	$V_{in}-V_o$	$I_L=10\text{mA}$ $I_L=100\text{mA}$ $I_L=300\text{mA}$		6 30 100	10 75 200	mV
Quiescent current	I_q	$V_{in}=4.3\text{V}$, $I_L=0$		0.56	1	mA
Quiescent current at shutdown	I_{qs}	$V_{in}=4.3\text{V}$, $I_L=0$, $V_{shdn}=V_{in}$		11	30	μA
Shutdown control input current	I_{ins}	$V_{shdn}=V_{in}=4.3\text{V}$		2	10	μA
Shutdown control threshold voltage	V_{ts}	$V_{in}=4.3\text{V}$ low(on) high(off)	1.5		0.4	V
Output current in shutdown mode (Note4)	I_{Ls}	$V_{in}=20\text{V}$ $V_o=\text{Gnd}$		50	100	nA
Output noise voltage (Note4)	e_n	$V_{in}=4.3\text{V}$ $f=10\text{Hz}$ to 100kHz, $I_L=100\text{mA}$		190		$\mu\text{V RMS}$
Low battery detect threshold	$V_{in(bld)}$		$V_{out} + 0.2\text{V}$		$V_{out} + 0.4\text{V}$	V
Low battery flag output voltage	V_{bl}	$I_{bl}=100\mu\text{A}$, $V_{in}<V_o+200\text{mV}$		0.16	0.4	V
Low battery flag leakage current	I_{bl}	$V_{bl}=6\text{V}$, $V_{in}>V_o+400\text{mV}$		0.1	1	μA

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NOTES.

1. Maximum power dissipation of the device is calculated assuming the package is mounted on a PCB measuring 2 inches square.

2 Output voltage temperature coefficient is calculated as:-
$$\frac{V_O \text{ change} \times 1000000}{V_O \times \text{temperature change}}$$

3. Dropout voltage is defined as the input to output voltage differential at which the circuit ceases to regulate. The value is measured when the output voltage has dropped by 100mV from V_{out} measured at the nominal input $V_{in} = V_{out} + 1V$

4. Guaranteed by design.

FUNCTIONAL DESCRIPTION

The ZLDO is a high performance, ultra low dropout, low quiescent regulator. Available in SM8 surface mount packaging, the device is able to dissipate 2W(note 1) allowing complete design flexibility with an input span upto 20V and 300mA output current. The device quiescent is 1mA (typ) at 300mA load current. A low battery comparator signifies impending battery failure, whilst a shutdown function reduces quiescent current to a mere 11µA (typ). A precision bandgap reference gives $\pm 2.5\%$ output tolerance and good temperature characteristics over the range -40 to +85°C. AC performance is enhanced via the use of a small external capacitor.

PIN DEFINITIONS

Pin 1 LBF - Low Battery Flag. An open collector NPN output which pulls low on failing input supply.

Pin 2 SC - Shutdown Control. This high impedance logic compatible input disables the regulator when taken high. It includes a diode wired to V_{in} and so will pass current if taken more than 0.5V above V_{in} .

Pin 3 V_{in} - Voltage Input. The power supply to the regulator. The permissible input voltage range is -0.3 to 20V. An input capacitor is not mandatory but will be useful in reducing the coupling of noise from input to output and minimising the effect of sudden changes in load current on the input voltage.

Pin 4 N/C - Not Connected. Not internally connected and so can be left open or wired to any pin without affecting the performance of the regulator.

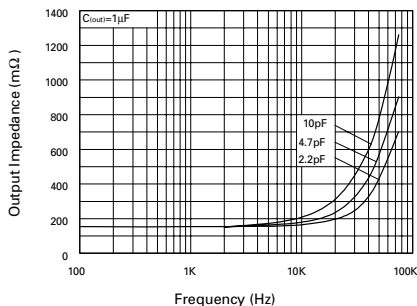
Pin 5 V_{out} - Voltage Output. The output of the regulator. An output capacitor of 1µF or greater and having low ESR should be wired in close proximity to the regulator to ensure stability for all loads.

Pin 6 D/C - Do Not Connect. This pin is wired to an internal circuit node of the regulator. No external connection should be made to this pin.

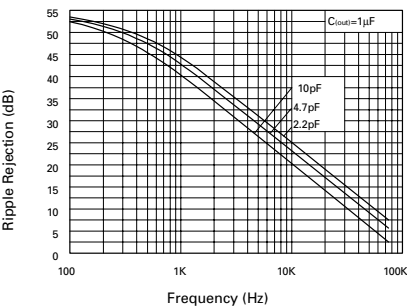
Pin 7 Gnd - Ground. The ground connection of the regulator against which the output voltage is referenced.

Pin 8 Spg - Shaping. The shaping node for the error amplifier of the regulator. A capacitor of 10pF wired from this pin to the output pin (pin 5) gives optimum stability. Improved AC can be achieved by reducing the value of this capacitor but stability may be impaired for some load conditions.

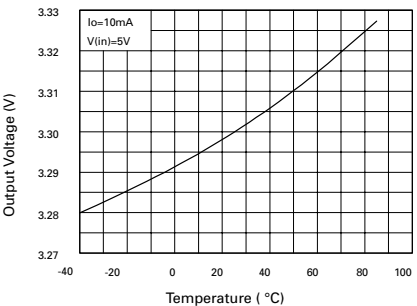
TYPICAL CHARACTERISTICS



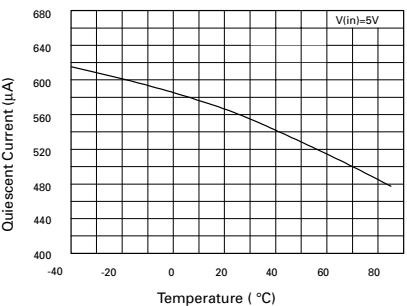
Output Impedance vs. Frequency



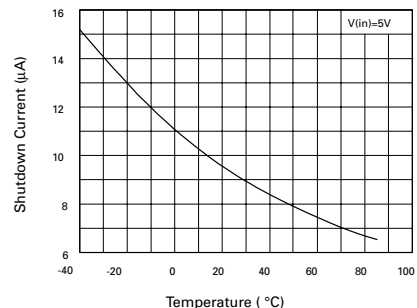
Ripple Rejection vs. Frequency



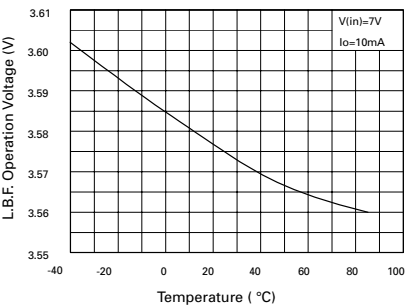
Output Voltage Temperature Coefficient



Quiescent Current vs. Temperature



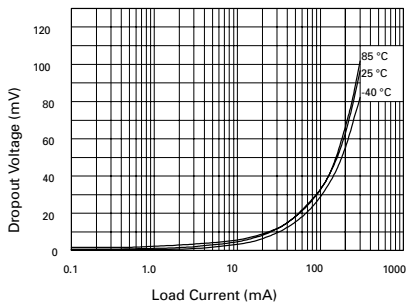
Shutdown Current vs. Temperature



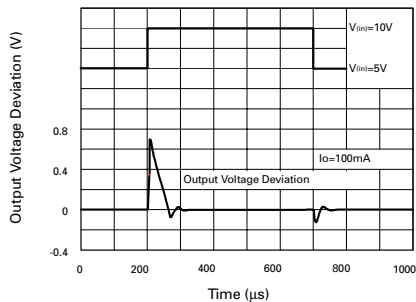
Low Battery Flag Operating Point

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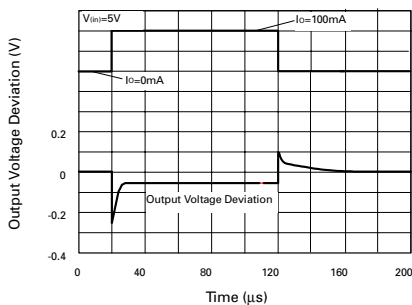
TYPICAL CHARACTERISTICS



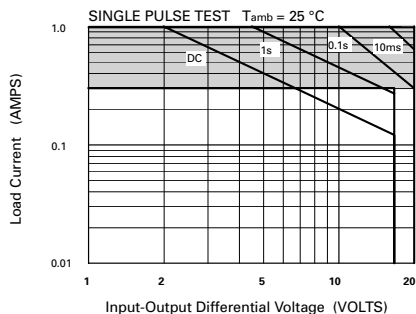
Dropout Voltage vs. Load Current



Line Transient Response



Load Transient Response



Safe Operating Area

Operation in shaded area is not guaranteed

APPLICATIONS

2). Post Converter Regulation

A common problem with multiple output switch mode converters is that only one output can be used in the feedback control loop of the switching regulator. Thus only one output is fully regulated. All other outputs are prone to tracking errors that occur if the load on any output changes significantly. By ensuring close coupling of all transformer windings and minimising the impedance of all outputs, these errors can be reduced but never eliminated. A simple way round this problem is to wind the switching regulator transformer to give a slightly higher voltage than required and regulate down from this to the desired voltage with a linear regulator. This is indicated in Figure 2. To keep losses low and so maintain the advantages of a switch mode supply, it is important that the voltage drop across this regulator is kept as low as possible, i.e. just high enough to compensate for the poor output impedance of the switching power supply but no higher. The low dropout voltage of the ZLDO330 allows this circuit technique to be implemented very effectively, giving a highly stable and accurate low noise supply.

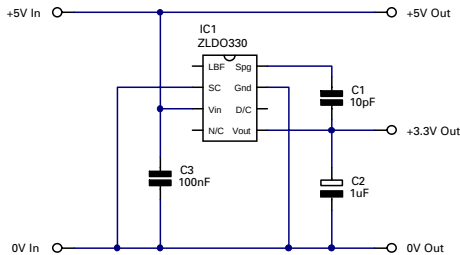


Figure 1

1). Simple 3.3V Supply

Using a circuit such as Figure 1, the ZLDO330 can easily provide a 3.3V logic supply from an available 5V rail where most standard regulators could not guarantee correct operation. Although this approach is not particularly energy efficient, if the load taken at 3.3V is not too large, then the added complexity and cost of a 3.3V switching converter may not be justifiable and so this linear solution can be preferable. This circuit will also give far less noise than a switching regulator which can be important when handling low level analogue signals or low voltage measurements.

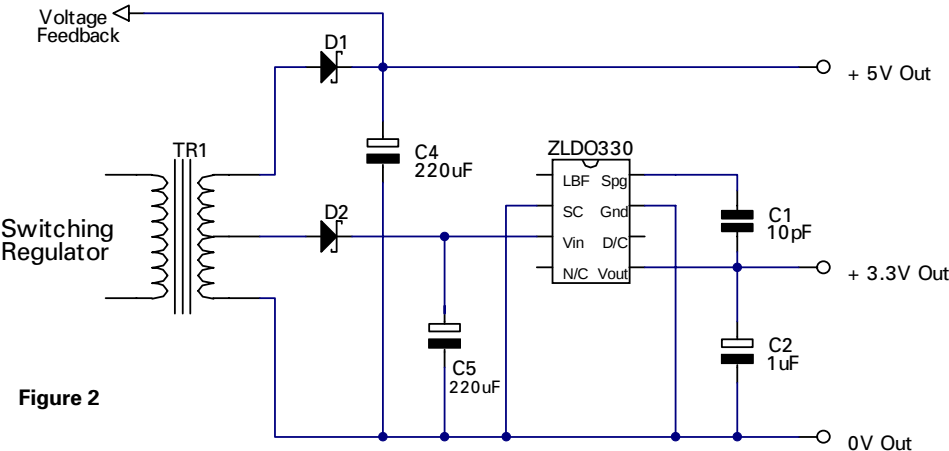


Figure 2

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3). Low Battery Flag

The ZLDO330 provides an output called Low Battery Flag (LBF). Unlike many regulators that only signal that they are falling out of regulation, the LBF output of the ZLDO330 indicates that the voltage drop across the regulator has fallen to less than typically 300mV and so supply failure is imminent.

4). Over Temperature Shutdown

The ZLDO330 regulator includes an over temperature shutdown circuit that disables the regulator if its chip temperature should exceed 125°C for any reason. Although intended to provide a limited guard against excessive internal power dissipation, this circuit will shut down the regulator if its ambient rises above 125°C.

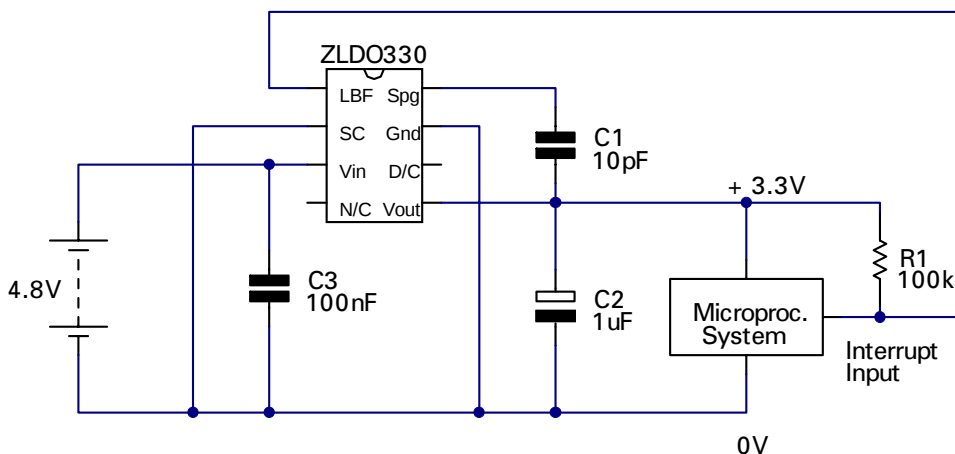


Figure 3

This improved warning gives both more time for the system supplied to shutdown gracefully and maintains regulation while this happens. This could be a vital point if measurements are under way and must be completed accurately for instance. The LBF output is driven by an open collector NPN transistor which pulls low when the supply to the regulator is failing. Figure 3 shows this output being used. Note that resistor R1 is necessary only if the interrupt logic does not include a pull-up resistor.

Thus, the regulator could be used to disable a circuit in the event of the ambient temperature within which the circuit is mounted becoming too high. Any internal power dissipation caused as a result of supplying load current, will reduce the ambient temperature at which shutdown occurs. Note that to achieve the extremely low dropout voltage and high current performance provided by the ZLDO devices, the parts can be damaged by sustained output shorts or excessive loads when combined with high input supply voltages. To ensure reliable operation, keep loads within the SOA graph boundaries indicated in the typical characteristics.

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5). Logic Controlled Power Supply

Fig.4 shows all that is necessary to allow a microprocessor to control a power supply based on the ZLDO330. The Shutdown Control pin (pin 2), is a logic compatible input that disables the regulator when a voltage in excess of 1.5V is applied. The current required to drive this input is less than 10 μ A. When the regulator is shutdown in this way, the quiescent current of the ZLDO330 falls to around 11 μ A. This makes the regulator suitable for a wide range of battery powered applications where intermittent operation occurs. The shutdown control pin should not be taken to a voltage higher than V_{in} if low quiescent supply current is important. The shutdown control is a high impedance input and so if not required, should be wired to the ground pin (pin 7).

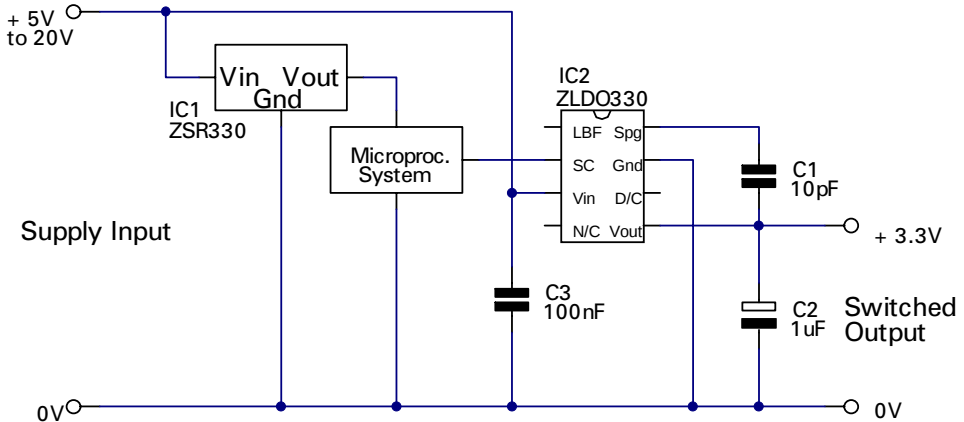
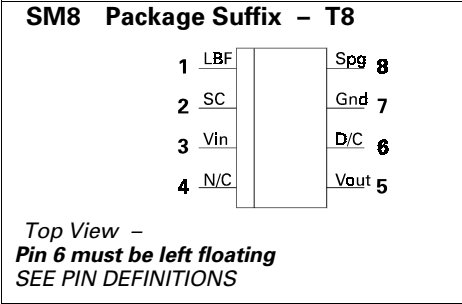


Figure 4

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CONNECTION DIAGRAM



ORDERING INFORMATION

Part Number	Package	Part Mark
ZLDO330T8	SM8	ZLDO330