

PNP 5 GHz wideband transistor

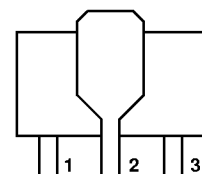
BFQ149

DESCRIPTION

PNP transistor in a SOT89 envelope. It is intended for use in UHF applications such as broadband aerial amplifiers (30 to 860 MHz) and in microwave amplifiers such as radar systems, spectrum analyzers, etc., using SMD technology.

PINNING

PIN	DESCRIPTION
Code: FG	
1	emitter
2	collector
3	base



Bottom view MBK514

Fig.1 SOT89.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CEO}	collector-emitter voltage	open base	—	—	–15	V
I_C	DC collector current		—	—	–100	mA
P_{tot}	total power dissipation	up to $T_s = 135\text{ °C}$ (note 1)	—	—	1	W
h_{FE}	DC current gain	$I_C = -70\text{ mA}$; $V_{CE} = -10\text{ V}$; $T_j = 25\text{ °C}$	20	50	—	
f_T	transition frequency	$I_C = -75\text{ mA}$; $V_{CE} = -10\text{ V}$; $f = 500\text{ MHz}$; $T_j = 25\text{ °C}$	4	5	—	GHz
G_{UM}	maximum unilateral power gain	$I_C = -50\text{ mA}$; $V_{CE} = -10\text{ V}$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ °C}$	—	12	—	dB
F	noise figure	$I_C = -50\text{ mA}$; $V_{CE} = -10\text{ V}$; $R_s = 60\text{ }\Omega$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ °C}$	—	3.75	—	dB

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	—	–20	V
V_{CEO}	collector-emitter voltage	open base	—	–15	V
V_{EBO}	emitter-base voltage	open collector	—	–3	V
I_C	DC collector current		—	–100	mA
I_{CM}	peak collector current	$f > 1\text{ MHz}$	—	–150	mA
P_{tot}	total power dissipation	up to $T_s = 135\text{ °C}$ (note 1)	—	1	W
T_{stg}	storage temperature		–65	150	°C
T_j	junction temperature		—	150	°C

Note

- T_s is the temperature at the soldering point of the collector tab.

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THERMAL RESISTANCE

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE
$R_{th\ j-s}$	thermal resistance from junction to soldering point	up to $T_s = 135\text{ °C}$ (note 1)	40 K/W

Note

1. T_s is the temperature at the soldering point of the collector tab.

CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0$; $V_{CB} = -10\text{ V}$;	–	–	100	nA
h_{FE}	DC current gain	$I_C = -70\text{ mA}$; $V_{CE} = -10\text{ V}$	20	50	–	
f_T	transition frequency	$I_C = -70\text{ mA}$; $V_{CE} = -10\text{ V}$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ °C}$	4	5	–	GHz
C_c	collector capacitance	$I_E = 0$; $V_{CB} = -10\text{ V}$; $f = 1\text{ MHz}$	–	2	–	pF
C_e	emitter capacitance	$I_C = 0$; $V_{EB} = -0.5\text{ V}$; $f = 1\text{ MHz}$	–	4	–	pF
C_{re}	feedback capacitance	$I_C = 0$; $V_{CE} = -10\text{ V}$; $f = 1\text{ MHz}$	–	1.7	–	pF
G_{UM}	maximum unilateral power gain (note 1)	$I_C = -50\text{ mA}$; $V_{CE} = -10\text{ V}$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	12	–	dB
F	noise figure	$I_C = -50\text{ mA}$; $V_{CE} = -10\text{ V}$; $R_s = 60\text{ }\Omega$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	3.75	–	dB

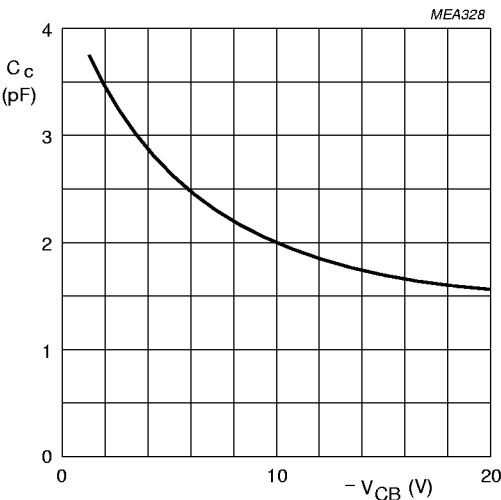
Note

1. G_{UM} is the maximum unilateral power gain, assuming S_{12} is zero and

$$G_{UM} = 10 \log \left(\frac{|S_{21}|^2}{(1 - |S_{11}|^2)(1 - |S_{22}|^2)} \right) \text{dB}.$$

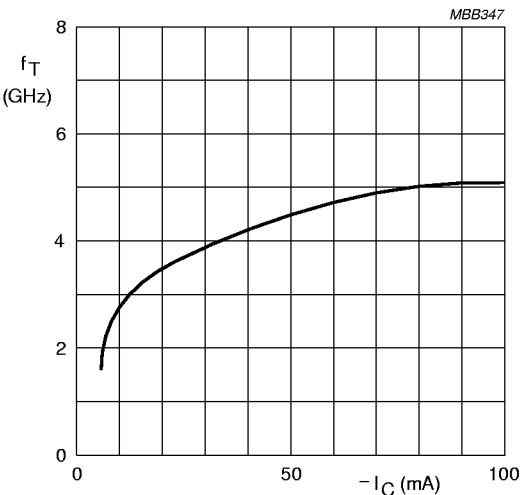
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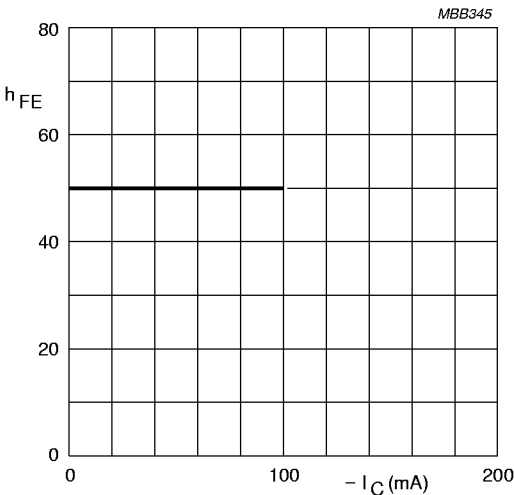
$I_E = 0$; $f = 1$ MHz; $T_j = 25$ °C.

Fig.2 Collector capacitance as a function of collector-base voltage.



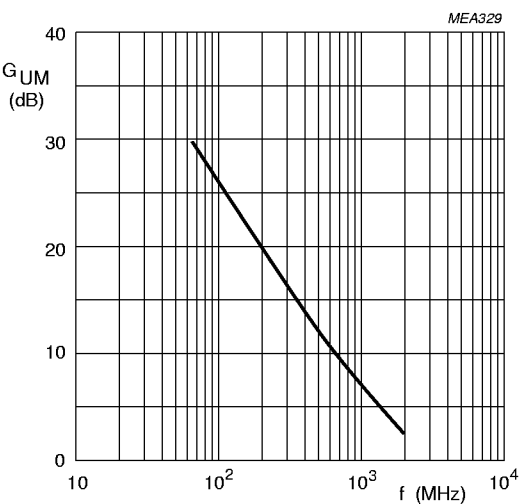
$V_{CE} = -10$ V; $f = 500$ MHz; $T_{amb} = 25$ °C.

Fig.3 Transition frequency as a function of collector current.



$V_{CE} = -10$ V; $T_j = 25$ °C.

Fig.4 DC current gain as a function of collector current.



$I_c = -50$ mA; $V_{CE} = -10$ V; $T_{amb} = 25$ °C.

Fig.5 Maximum unilateral power gain as a function of frequency.

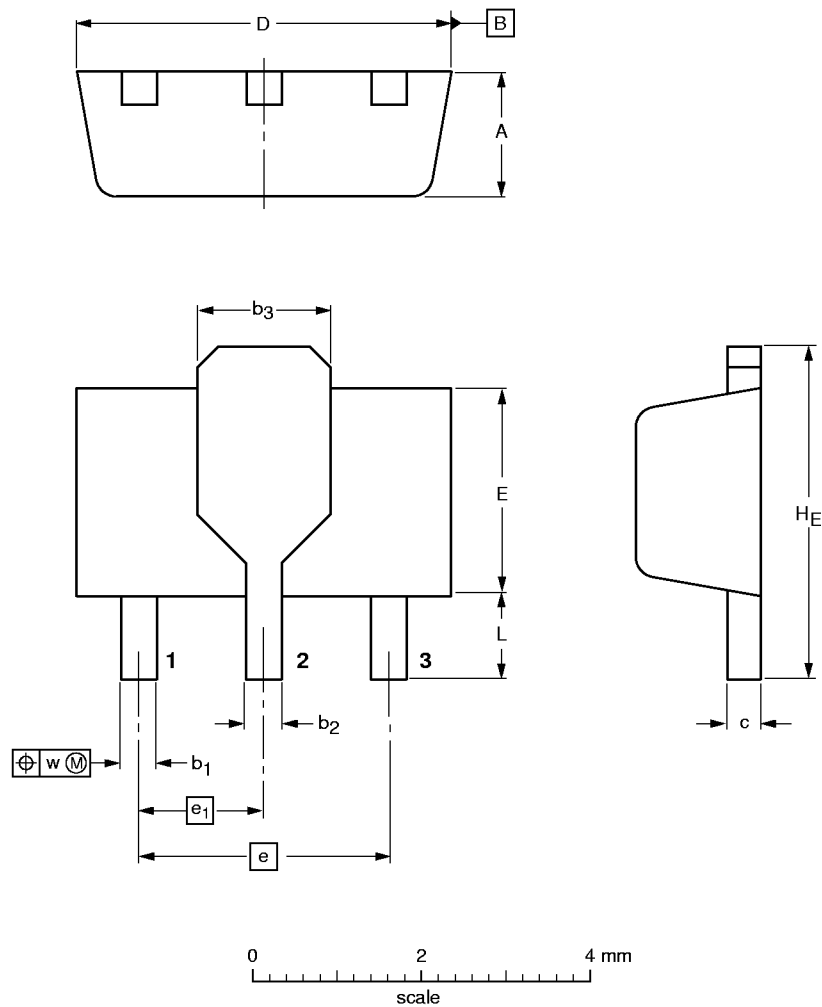
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PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



DIMENSIONS (mm are the original dimensions)

UNIT	A	b ₁	b ₂	b ₃	c	D	E	e	e ₁	H _E	L min.	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.37	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	0.8	0.13

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT89						97-02-28