

BT06 T/D/S/A / BTB06 T/D/S/A

Fig.5 : Relative variation of thermal impedance versus pulse duration.

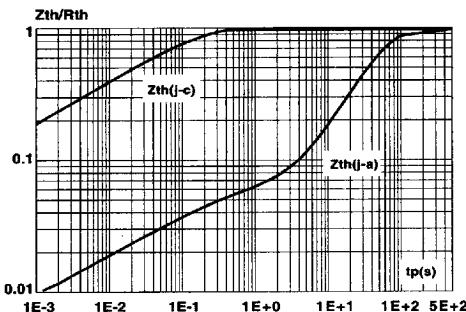


Fig.7 : Non Repetitive surge peak on-state current versus number of cycles.

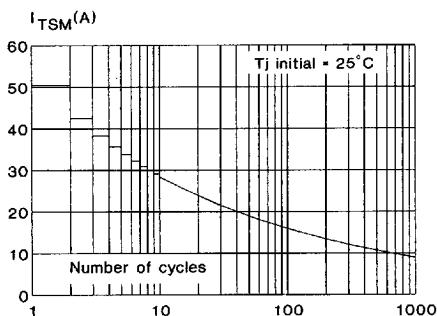


Fig.9 : On-state characteristics (maximum values).

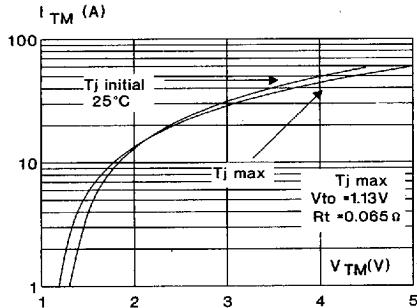


Fig.6 : Relative variation of gate trigger current and holding current versus junction temperature.

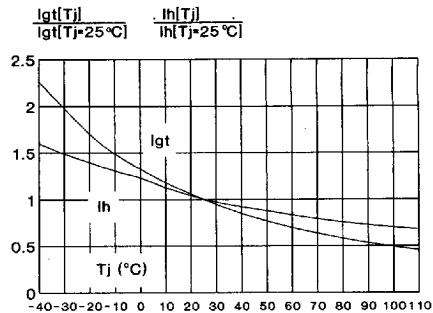


Fig.8 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : $t \leq 10ms$, and corresponding value of I^2t .

