

TP0610 SERIES

P-Channel Enhancement-Mode MOS Transistors

T-37-25

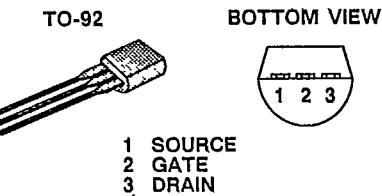
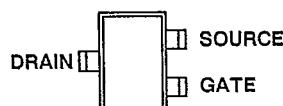
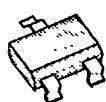
PRODUCT SUMMARY

PART NUMBER	$V_{(BR)DSS}$ (V)	$r_{DS(ON)}$ (Ω)	I_D (A)	PACKAGE
TP0610E	-60	10	-0.25	TO-206AC
TP0610L	-60	10	-0.18	TO-92
TP0610T	-60	10	-0.12	SOT-23

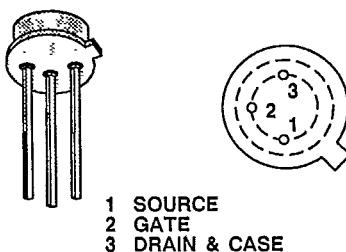
Performance Curves: VPDS06 (See Section 7)

SOT-23

TOP VIEW



TO-206AC (TO-52) BOTTOM VIEW

**ABSOLUTE MAXIMUM RATINGS (TA = 25°C unless otherwise noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	TP0610E ²	TP0610L	TP0610T	UNITS
Drain-Source Voltage	V_{DS}	-60	-60	-60	V
Gate-Source Voltage	V_{GS}	± 20	± 30	± 30	
Continuous Drain Current	I_D	-0.25	-0.18	-0.12	A
		-0.15	-0.11	-0.07	
Pulsed Drain Current ¹	I_{DM}	-1	-0.8	-0.4	
Power Dissipation	P_D	1.5	0.80	0.36	W
		0.60	0.32	0.14	
Operating Junction and Storage Temperature	T_J, T_{stg}	-55 to 150			°C
Lead Temperature (1/16" from case for 10 seconds)	T_L	300			

THERMAL RESISTANCE

THERMAL RESISTANCE	SYMBOL	TP0610E	TP0610L	TP0610T	UNITS
Junction-to-Ambient	R_{thJA}	400	156	350	°C/W

¹Pulse width limited by maximum junction temperature²Reference T_C for all temperature testing



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T-37-25

PARAMETER	SYMBOL	TEST CONDITIONS	TYP ²	LIMITS		TYP ²		UNIT
				TP0610E	TP0610L	MIN	MAX	
STATIC								
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = -10 μA	-70	-60		-60		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -1 mA	-1.7	-1	-2.4	-1	-2.4	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V V _{GS} = ±20 V	±1		±10		±10	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -48 V V _{GS} = 0 V	±5		±50		±50	
On-State Drain Current ³	I _{D(ON)}	T _J = 125°C	-0.02	-1	-1	-200	-200	μA
On-State Drain Current ³	I _{D(ON)}	V _{DS} = -10 V, V _{GS} = -4.5 V	-0.2	-80	-50	-50		mA
Drain-Source On-Resistance ³	r _{DSON}	V _{GS} = -4.5 V, I _D = -25 mA	11		25		25	
Forward Transconductance ³	g _{FS}	V _{DS} = -10 V, I _D = -0.5 A	8	135	80	80		mS
Common Source Output Conductance ³	g _{OS}	V _{DS} = -10 V, I _D = -0.1 A	15	400				μS
DYNAMIC								
Input Capacitance	C _{iss}	V _{DS} = -25 V V _{GS} = 0 V f = 1 MHz	15		60		60	pF
Output Capacitance	C _{oss}		10		25		25	
Reverse Transfer Capacitance	C _{rss}		3		5		5	
SWITCHING								
Turn-On Time	t _{d(ON)}	V _{DD} = -25 V, R _L = 133 Ω I _D = -0.18 A, V _{GEN} = -10 V R _G = 25 Ω (Switching time is essentially independent of operating temperature)	6		10		10	ns
	t _r		10		15		15	
Turn-Off Time	t _{d(OFF)}		7		15		15	
	t _f		8		20		20	

- NOTES: 1. T_A = 25 °C unless otherwise noted, T_C = 25 °C for TP0610E.
 2. For design aid only, not subject to production testing.
 3. Pulse test; PW = 300 μs, duty cycle ≤ 2%.

6

TP0610 SERIES

T-37-25

 Siliconix
incorporated

ELECTRICAL CHARACTERISTICS ¹			LIMITS			
PARAMETER	SYMBOL	TEST CONDITIONS	TYP ²	TP0610T		UNIT
				MIN	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = -10 μA	-70	-60		V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -1 mA	-1.7	-1	-2.4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V V _{GS} = ±20 V	±1		±10	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -48 V V _{GS} = 0 V	-0.02		-1	μA
On-State Drain Current ³	I _{D(ON)}	V _{DS} = -10 V, V _{GS} = -4.5 V	-80	-50		mA
Drain-Source On-Resistance ³	r _{DS(ON)}	V _{GS} = -4.5 V, I _D = -25 mA	11		25	Ω
		V _{GS} = -10 V I _D = -0.2 A	6		10	
Forward Transconductance ³	g _{FS}	V _{DS} = -10 V, I _D = -0.1 A	12		20	
Common Source Output Conductance ³	g _{OS}		90	60		mS
			400			μS
DYNAMIC						
Input Capacitance	C _{iss}	V _{DS} = -25 V V _{GS} = 0 V f = 1 MHz	15		60	pF
Output Capacitance	C _{oss}		10		25	
Reverse Transfer Capacitance	C _{rss}		3		5	
SWITCHING						
Turn-On Delay Time	t _{d(ON)}	V _{DD} = -25 V, R _L = 133 Ω I _D = -0.18 A, V _{GEN} = -10 V R _G = 25 Ω (Switching time is essentially independent of operating temperature)	6		10	ns
	t _r		10		15	
Turn-Off Delay Time	t _{d(OFF)}		7		15	
	t _f		8		20	

NOTES: 1. T_A = 25 °C unless otherwise noted.2. For design aid only, not subject to production testing.
3. Pulse test; PW = 300 μs, duty cycle ≤ 2%.