

T1/CEPT/ISDN-PRI TRANSFORMERS Reinforced Insulation, 3 KVRms

- UL 1950 RECOGNIZED
- DESIGNED TO MEET ITU-T G.703
- FOR T1/CEPT LINE INTERFACES
- MATCHED TO LEADING TRANSCEIVER ICS
- CERTIFIED FOR REINFORCED INSULATION PER EN41003/EN60950

ELECTRICAL SPECIFICATIONS: @ 25°C-OPERATING TEMPERATURE 0°C TO 70°C

PART NUMBER	TURNS RATIO (±5%)	OCL (mH MIN)	C _{w/w} (pF MAX)	L _L (uH MAX)	DCR pri (W MAX)	DCR sec (W MAX)	PACKAGE/ SCHEMATIC	PRIMARY PINS
XF8006-11	1:1:1.58	.800	15	0.70	0.50	0.35	C	1-5
XF8006-12	1:1:2	.800	15	0.70	0.50	0.45	C	1-5
XF0013-21	1:1.36CT	1.20	35	0.60	0.70	0.90	D	10-6
XF0013-41	1CT:2CT	1.20	20	0.30-0.55	0.50	0.90	A	1-5
XF0013-11	1:1	1.20	20	0.50	0.50	0.50	B	1-5
XF0013-31	1CT:2CT	1.20	15	0.80	0.70	1.10	A	1-5
XF6006-11	1CT:3CT:1	.600	30	0.80	0.70	1.70	E	1-3
XF0013-51	1:1.08/1.36	1.50	20	0.60	0.70	0.90	D	10-6
XF0013-61	1:1.14CT	1.50	30	1.00	0.70	0.90	D	10-6
XF0013-71	1:1/1.26	1.50	35	0.60	0.70	1.10	D	10-6
XF0013-81	1:1.58/2	1.50	20	0.70	0.70	1.20	D	10-6
XF0013-91	1CT:1.41CT	1.20	20	0.80	0.60	0.80	A	10-6

MECHANICAL DIMENSIONS:

SCHEMATIC:

