

# **Hex Driver for High-Temperature Application**

#### **Description**

The hex driver IC includes 6 non-inverted and current limited output stages with an open collector.

Each output can sink 20 mA, parallel output operation is

possible. The digital inputs have Schmitt-trigger function with pull-up resistors to 5 V.

#### **Features**

- Six input comparators with Schmitt-trigger characteristic
- Input clamping current capability of  $\pm 10 \text{ mA}$
- Integrated protection cells (EMC, ESD, RF) dedicated to all input stages
- Reset with hysteresis at low voltage

- ESD protection according to human body model:
   ± 2000 V (C = 100 pF, R = 1.5 kΩ)
- Output stages:
- Short-circuit protected
- Load-dump protected @ 1 k $\Omega$
- No crosstalk on adjacent channels
- Jump start possible

Extended Type Number	Package	Remarks
U6807B-FP	SO14	

#### **Block Diagram**

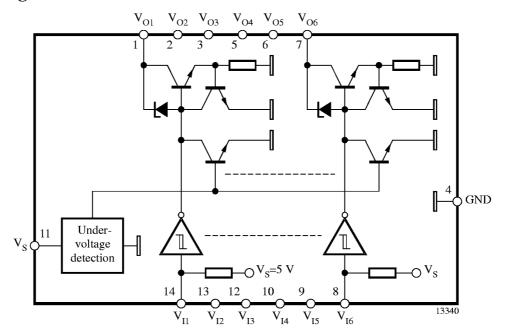


Figure 1.

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#### **Pin Description**

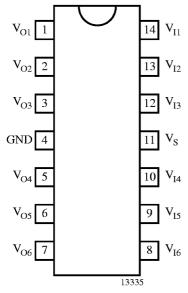


Figure 2. Pinning

Pin	Symbol	Function
1	$V_{O1}$	Output 1
2	$V_{O2}$	Output 2
3	$V_{O3}$	Output 3
4	GND	Ground
5	$V_{O4}$	Output 4
6	$V_{O5}$	Output 5
7	$V_{O6}$	Output 6
8	$V_{I6}$	Input 6
9	$V_{15}$	Input 5
10	$V_{I4}$	Input 4
11	$V_{S}$	Supply voltage, 5 V
12	$V_{I3}$	Input 3
13	$V_{I2}$	Input 2
14	$V_{I1}$	Input 1

#### **Basic Circuitry**

The integrated circuit U6805B requires a stabilized supply voltage ( $V_S = 5 \text{ V} \pm 5\%$ ) to comply with its electrical characteristics. An external buffer capacitor of C = 100 nF value is recommended. An integrated 14-V Z diode between  $V_S$  and ground protects the supply pin.

All input stages are provided with an integrated 250-k $\Omega$  pull-up resistor and can be directly connected to a microcontroller.

All output stages are open collectors – each capable of sinking 20 mA. Recommended external components:

Pull-up resistor,  $R = 1 \text{ k}\Omega$ 

Capacitor to GND, C = 470 pF, see figure KEIN MERKER

## **Functional Description**

#### General

ON state: Low level at the input stage activates the corresponding output stage.

OFF state: The internal pull-up resistor provides high level to the input comparator and deactivates the output stage.

7 VZ diodes between each input pin and GND are capable of  $\pm 10$  mA clamping currents without crosstalk on adjacent input stages.

A total clamping current of  $\pm 30$  mA should be observed with respect to the power dissipation.

# **Current Limitation of the Output Stages**

A temperature dependent current limitation in the range of 25 mA to 100 mA protects the stages during a short.

An appropriate heat sink assembly with  $R_{thJA}$  < 100 K/W and/or a further reduction of the maximum ambient temperature is recommended in order to avoid thermal overstress. Depending on the numbers of output stages which may be in short circuit condition these maximum ratings are (with  $R_{thJA}$  < 120 K/W):

 $T_{amb\;max} \leq 80^{\circ}\text{C} \otimes 2$  channels shorted simultaneously  $T_{amb\;max} \leq 65^{\circ}\text{C} \otimes 3$  channels shorted simultaneously Refer to hexdriver U6805B if thermal shut down is required.

## **Transients and Load Dump**

An integrated 28-V Z diode protects each output stage against transients and load dump (Schaffner pulses). With the help of external 1-k $\Omega$  resistor, the output transistor is capable to handle the corresponding current which flows during each of these conditions. Apart from that, outputs are short circuit and overload protected.

## **Low-Voltage Detection**

When the supply voltage is switched on, a power-on reset pulse is generated internally which disables all output stages until a defined supply voltage level is reached. The low-voltage detection is provided with a hysteresis of  $V_{hyst} = 0.5\ V$  typically.

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## **Absolute Maximum Ratings**

Parameters	Symbol	Value	Unit
Supply voltage	$V_S$	7.0	V
Ambient temperature range *	$T_{amb}$	-40 to +125	°C
Storage temperature range	$T_{stg}$	-50 to +150	°C
Maximum junction temperature	$T_j$	+150	°C

<sup>\*</sup> To be reduced if 2 or 3 output stages are shorted sumultaneously (see "Current Limitation of the Output Stages" on page 2).

#### **Thermal Resistance**

Parameters	Symbol		Unit
Junction ambient	$R_{thJA}$	120	K/W

#### **Electrical Characteristics**

 $V_S$  = 5 V ± 5%,  $T_{amb}$  = 27°C, reference point pin 4 (GND), unless otherwise specified, see figures KEIN MERKER and 3.

Parameters	Test Conditions / Pins	Symbol	Min.	Тур.	Max.	Unit
Supply Pin 11						
Supply voltage		Vs	4.75		5.25	V
Supply current	Inputs open Inputs closed to GND	$I_{ m S}$ $I_{ m S}$	0.8 7		4.2 14.5	mA mA
Low-voltage detection threshold	ON OFF	$egin{array}{c} V_{TH(ON)} \ V_{TH(OFF)} \end{array}$	3.7 3.0		4.6 3.8	V V
Low-voltage hysteresis		V <sub>hyst</sub>	0.55		1.05	V
Inputs Pins 8, 9, 10, 12, 13 and 14						
Z diode protection voltage	$I_{\rm I}$ = 10 mA	VI	6.7		8.5	V
Z diode clamping current		I <sub>I</sub>			±10	mA
Pull-up resistor		R <sub>I</sub>	170	250	305	kΩ
Switching threshold	OFF ON	$egin{array}{c} V_{\mathrm{I}} \ V_{\mathrm{I}} \end{array}$		3.3 1.8		V V
Hysteresis		V <sub>hyst</sub>		1.5		V
Outputs Pins 1, 2, 3, 5, 6 and 7						
Z diode protection voltage	$I_O = 10 \text{ mA}$	Vo	26.5			V
Integrated capacitor				5		pF
Leakage current		I <sub>Leak</sub>			2.5	μA
Saturation voltage	$I_O = 20 \text{ mA}$	V <sub>Sat</sub>			0.7	V
Current limitation		I <sub>limit</sub>	25		100	mA
Propagation delay	470 pF, 1 kΩ, 20 V	t <sub>d</sub>			5	μs

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# **Application Circuit**

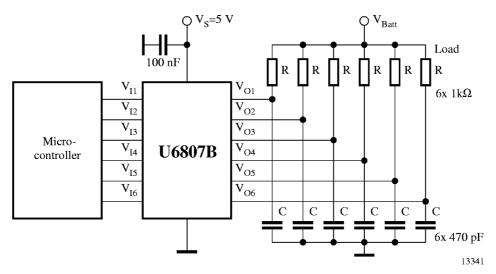
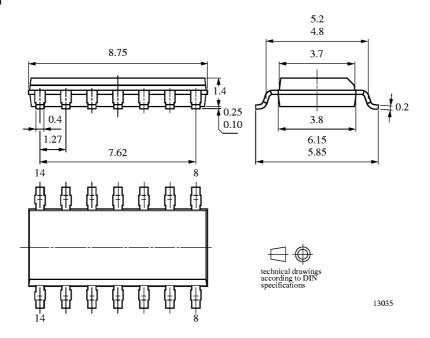


Figure 3. Application schematic

# **Package Information**

Package SO14
Dimensions in mm



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- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**TEMIC Semiconductor GmbH** has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

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