

### UC1707 UC2707 UC3707

## **Dual Channel Power Driver**

#### **FEATURES**

- Two Independent Drivers
- 1.5A Totem Pole Outputs
- Inverting and Non-Inverting Inputs
- 40ns Rise and Fall into 1000pF
- High-Speed, Power MOSFET Compatible
- Low Cross-Conduction Current Spike
- Analog Shutdown with Optional Latch
- Low Quiescent Current
- 5V to 40V Operation
- Thermal Shutdown Protection
- 16-Pin Dual-In-Line Package
- 20-Pin PLCC and CLCC Package

#### DESCRIPTION

The UC1707 family of power drivers is made with a high-speed Schottky process to interface between low-level control functions and high-power switching devices - particularly power MOSFETs. These devices contain two independent channels, each of which can be activated by either a high or low input logic level signal. Each output can source or sink up to 1.5A as long as power dissipation limits are not exceeded.

Although each output can be activated independently with its own inputs, it can be forced low in common through the action either of a digital high signal at the Shutdown terminal or a differential low-level analog signal. The Shutdown command from either source can either be latching or not, depending on the status of the Latch Disable pin.

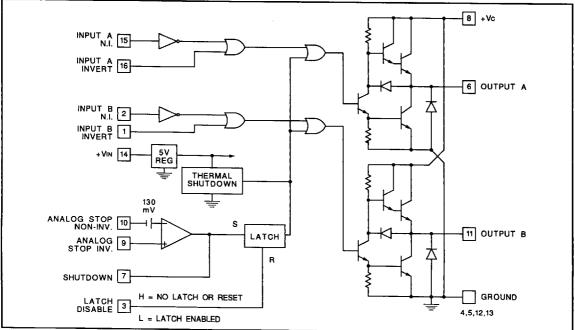
Supply voltage for both Vin and Vc can independently range from 5V to 40V.

These devices are available in two-watt plastic "bat-wing" DIP for operation over a 0°C to 70°C temperature range and, with reduced power, in a hermetically sealed cerdip for -55°C to +125°C operation. Also available in surface mount DW, Q, L packages.

#### TRUTH TABLE (Each Channel)

, , , , , , , , , , , , , , , , , , ,					
INV.	N.I	OUT			
н	Н	L			
L	Н	H			
H	L	L			
L	L	L			

#### **BLOCK DIAGRAM**



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#### **ABSOLUTE MAXIMUM RATINGS**

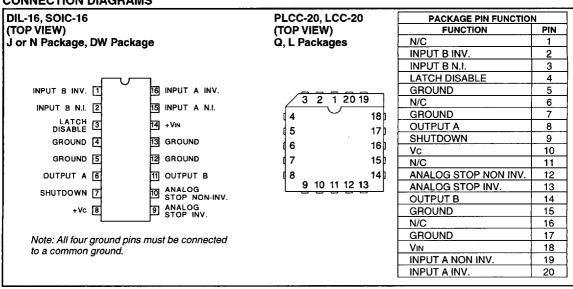
	N-Pkg	J-Pkg
Supply Voltage, Vin	40V	40V
Collector Supply Voltage, Vc	40V	40V
Output Current (Each Output, Source or Sink)		
Steady-State	±500mA	±500mA
Peak Transient	±1.5A	±1.0A
Capacitive Discharge Energy	20µJ	15μJ
Digital Inputs (See Note)	5.5V	5.5V
Analog Stop Inputs	Vin	VIN
Power Dissipation at TA = 25°C (See Note)	2W	
Power Dissipation at T (Leads/Case) = 25°C (See Note	e) 5W	2W
Operating Temperature Range	55°0	C to +125°C
Storage Temperature Range	65°0	C to +150°C
Lead Temperature (Soldering, 10 Seconds)		

Note: All voltages are with respect to the four ground pins which must be connected together. All currents are positive into, negative out of the specified terminal.

Digital Drive can exceed 5.5V if input current is limited to 10mA.

Consult Packaging section of Databook for thermal limitations and considerations of package.

#### **CONNECTION DIAGRAMS**



# **ELECTRICAL CHARACTERISTICS:** Unless otherwise stated, these specifications apply for TA = -55°C to +125°C for the UC1707, -25°C to +85°C for the UC2707 and 0°C to +70°C for the UC3707; VIN = VC = 20V. TA = TJ.

PARAMETERS	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Vin Supply Current	VIN = 40V		12	15	mA
Vc Supply Current	Vc = 40V, Outputs Low		5.2	7.5	mA
Vc Leakage Current	VIN = 0, VC = 30V, No Load		.05	0.1	mA
Digital Input Low Level				0.8	٧
Digital Input High Level		2.2			٧
Input Current	VI = 0		-0.6	-1.0	mA
Input Leakage	VI = 5V		.05	0.1	mA
Output High Sat., Vc-Vo	lo = -50mA			2.0	V
	lo = -500mA			2.5	V

## ELECTRICAL CHARACTERISTICS (cont.):

Unless otherwise stated, these specifications apply for Ta = -55°C to +125°C for the UC1707, -25°C to +85°C for the UC2707 and 0°C to +70°C for the UC3707; VIN = VC = 20V, TA = TJ.

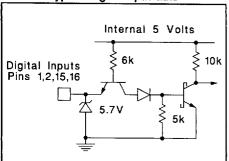
PARAMETERS	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Output Low Sat., Vo	Io = 50mA			0.4	V
	Io = 500mA			2.5	V
Analog Threshold	Vcm = 0 to 15V	100	130	150	mV
Input Bias Current	Vcm = 0		-10	-20	μА
Thermal Shutdown			155		°C
Shutdown Threshold	Pin 7 Input	0.4	1.0	2.2	V
Latch Disable Threshold	Pin 3 Input	0.8	1.2	2.2	l v

## TYPICAL SWITCHING CHARACTERISTICS: VIN = VC = 20V, TA = 25°C. Delays measured to 10% output change.

PARAMETERS From Inv. Input to Output:	TEST CONDITIONS	OU	OUTPUT CL =		
		open	1.0	2.2	UNIT nF
Rise Time Delay		40	50	60	ns
10% to 90% Rise		25	40	50	ns
Fall Time Delay		30	40	50	ns
90% to 10% Fall		25	40	50	ns
From N. 1. Input to Output:					1
Rise Time Delay		30	40	50	ns
10% to 90% Rise		25	40	50	ns
Fall Time Delay		45	55	65	ns
90% to 10% Fall		25	40	50	ns
Vc Cross-Conduction	Ouput Rise	25			ns
Current Spike Duration	Output Fall	0			ns
Analog Shutdown Delay	Stop Non-Inv. = 0V Stop Inv. = 0 to 0.5V	180			ns
Digital Shutdown Delay	2V Input on Pin 7	50			ns

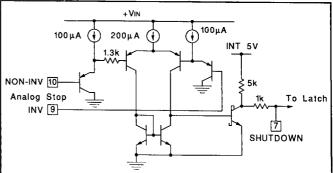
#### SIMPLIFIED INTERNAL CIRCUITRY

#### Typical Digital Input Gate



The input zener may be used to clamp input signal voltages higher than 5V as long as the zener current is limited to 10mA max. External pull-up resistors are not required.

#### **Analog Shutdown Comparator Circuit**



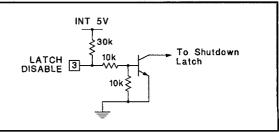
The input common-mode voltage range is from ground to (VIN-3V). When not used both inputs should be grounded. Activate time is a function of overdrive with a typical value of 180ns. Pin 7 serves both as a comparator output and as a common digital shutdown input. A high signal here will accomplish the fastest turn off of both outputs. Note that "OFF" is defined as the outputs low. Pulling shutdown low defeats the latch operation regardless of its status.

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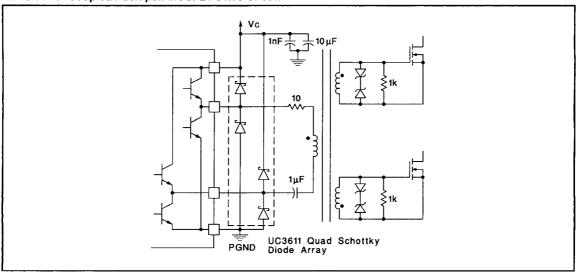
#### SIMPLIFIED INTERNAL CIRCUITRY (continued)

#### **Latch Disable**

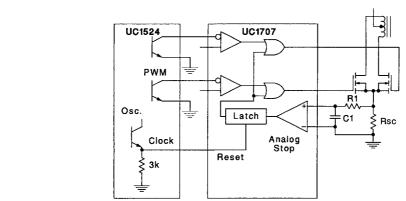
The Shutdown latch is disabled when pin 3 is open. An impedance of 4k or less from pin 3 to ground will allow a shutdown signal to set the latch which can then be reset by either recycling the VIN supply or by momentarily (>200ns) raising pin 3 high.



#### Transformer Coupled Push-pull MOSFET Drive Circuit

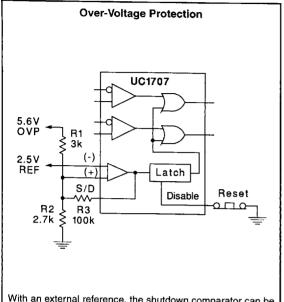


#### **Current Limiting**

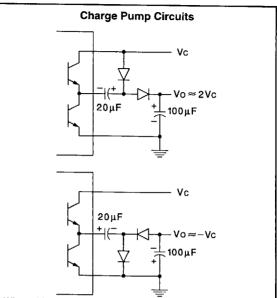


The Analog shutdown can give pulse-by-pulse current limiting with a reset pulse from the clock output of the UC1524. R1C1 is used to filter leading edge spikes.

### **APPLICATIONS** (continued)

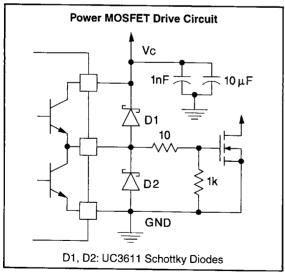


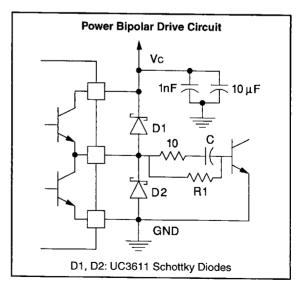
With an external reference, the shutdown comparator can be used for over-voltage protection. R1 and R2 set the shutdown level while R3 adds positive feedback for hysteresis.



When driven with a TTL square wave drive, the low output impedance of the UC1707 allows ready implementation of charge pump voltage converters.

#### **OUTPUT STAGE COUPLING**





#### TRANSFORMER COUPLING

