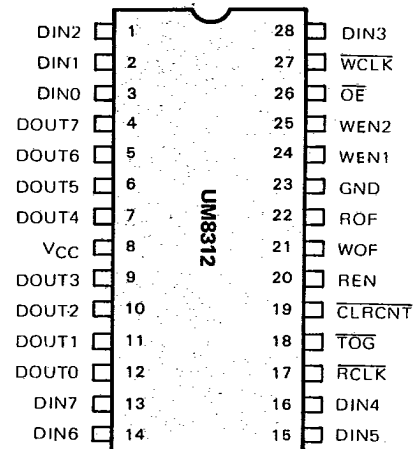


UM8312 Double Row Buffer (DRB)**FEATURES**

- Low Cost Solution to CRT Memory Contention Problem
- Provides Enhanced Processor Throughput for CRT Display Systems
- Replaces Shift Registers or Several RAM and Counter IC's in CRT Display System
- Permits Display of One Data Row While Next Data Row is Being Loaded
- Data May be Written into Buffer at Less Than the Video Painting Rate
- Double Data Row Buffer Permits Second Data Row to be Loaded Anytime during the Display of the Preceding Data Row
- Permits Active Video on All Scan Lines of Data Row
- Dynamically Variable Number of Characters per Data Row ...64, 80, 132,...up to a Maximum of 135
- Cascadable for Data Rows Greater than 135 Characters
- Stackable for "Invisible Attributes" or Character Widths of Greater than 8 Bits
- To 4 MHz R/W Data Rate and 5 Volt Only Power Supply

PIN CONFIGURATION**UM2674A/B/C*** Advanced CRT Controller (ACRTC)**FEATURES**

- 2.7, 4 and 5 MHz character rate
- 1 to 256 characters per row
- 1 to 16 raster lines per character row
- 1 to 128 character rows per frame
- Bit mapped graphics mode
- Programmable horizontal and vertical sync generators — RS170 compatible sync
- Interlaced or non-Interlaced operation
- Up to 64K RAM addressing for multiple page operation
- Readable, writable and incrementable cursor — Programmable cursor size and blink
- AC line lock
- Automatic wraparound of RAM
- Automatic split screen
- Automatic bidirectional soft scrolling — Programmable scan line increment
- Row table addressing mode
- Double height tops and bottoms
- Double width control output
- Selectable buffer interface modes
- Dynamic RAM refresh
- Completely TTL compatible
- Single +5 volt power supply
- Power on reset circuit

PIN CONFIGURATION