

Wide Bandwidth (Continued)

Type		V _{IO} Max. mV	I _I Max. nA	I ⁺ Max. Ma	Max. V ⁺ , V ⁻	AOL (Min.) dB	Unity Gain Bandwidth Typ. MHz	SR (Typ.) V/μs	Package No. of Pins ⁺
SINGLES	CA3010	5	12μA	2.5	±8	66	16	3	12T
	CA3010A	2	4μA	3.3	±8	66	16	3	12T
	CA3015	5	24μA	7.3	±16	72	60	7	12T
	CA3015A	2	6μA	7.3	±16	72	60	7	12T
	CA3029	5	12μA	2.5	±8	60	16	3	14E
	CA3029A	2	4μA	3.5	±8	60	16	3	14E
	CA3030	5	24μA	7.3	±16	72	60	7	14E
	CA3030A	2	6μA	7.3	±16	72	60	7	14E
	CA3037	5	12μA	2.5	±8	60	16	3	14D
	CA3037A	2	4μA	3.3	±8	60	16	3	14D
	CA3038	5	24μA	7.3	±16	72	60	7	14D
	CA3038A	2	6μA	7.3	±16	72	60	7	14D
	CA3100 •	5	2000	10.5	±18	56	38*	70	8E, S, T
	CA3130 •	15	50pA	15	±8	94	15	30	8E, M,
	CA3130A •	5	30pA	15	±8	94	15	30	S, T
	CA3140 •	15	50pA	6	±18	86	4.5*	9	8E, S,
	CA3140A •	5	30pA	6	±18	86	4.5*	9	T, M
	CA3160 •	15	50pA	15	±8	94	4*	10	8E, S,
	CA3160A •	5	30pA	15	±8	94	4*	10	T, M
	CA3450 •	15	130	35	±8.5	60	220	330	16E
DUALS	CA3240 •	15	50pA	12	±18	86	4.5*	9	8E, 8T,
	CA3240A •	5	40pA	12	±18	86	4.5*	9	8S, 14EI
	CA3260 •	15	50pA	15.5	±8	94	4*	10	8E, M,
	CA3260A •	5	30pA	15.5	±8	94	4*	10	T, S

+ See interpretation guide and packaging section

• BiMOS type

* ft.