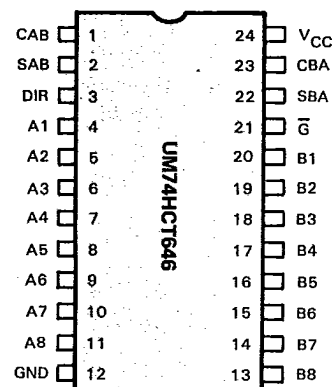# UM74HCT646 Octal Bus Transceivers and Register

FEATURES

- 8 Bi-directional Data Bus
- Transmits Directly Read-time Data or Stored Data in the Internal Register in Either Direction
- 3-state Outputs with Driving Capability for Direct Bus Interface
- Low Power Consumption with Function, Pin-out, Speed and Drive Compatibility with 74LS Logic Family and Interfaces Directly with TTL, NMOS and CMOS Dovices

PIN CONFIGURATION

Pin	Name		Pin	Name
1	CAB		24	V_{CC}
2	SAB		23	CBA
3	DIR		22	SBA
4	A1		21	$\overline{G}$
5	A2		20	B1
6	A3		19	B2
7	A4		18	B3
8	A5		17	B4
9	A6		16	B5
10	A7		15	B6
11	A8		14	B7
12	GND		13	B8

(UM74HCT646)

UM2332 4Kx8 Read Only Memory

FEATURES

- Access Time 200/300/450 ns (Max.)
- Single +5V ± 5% Power Supply
- TTL Compatible Inputs and Outputs
- Three-state Outputs
- Pin Compatible with 2532 EPROM
- Two Programmable Chip Selects for Output Control
- N-channel Silicon Gate Technology

PIN CONFIGURATION

Pin	Name		Pin	Name
1	A7		24	V_{CC}
2	A6		23	A8
3	A5		22	A9
4	A4		21	$CS2/\overline{CS2}$
5	A3		20	$CS1/\overline{CS1}$
6	A2		19	A10
7	A1		18	A11
8	A0		17	O7
9	O0		16	O6
10	O1		15	O5
11	O2		14	O4
12	GND		13	O3

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