

UM8250A Asynchronous Communication Element (ACE)

FEATURES

- Adds or Deletes Standard Asynchronous Communication Bits (Start, Stop, and Parity) to or from Serial Data Stream
- Full Double Buffering Eliminates Need for Precise Synchronization
- Independently Controlled Transmit, Receive, Line Status, and Data Set Interrupts
- Programmable Baud Rate Generator Allows Division of Any Input Clock by 1 to ($2^{16} - 1$) and Generates the Internal 16X Clock
- Independent Receiver Clock Input
- Modem Control Functions (CTS, RTS, DSR, DTR, RI, and Carrier Detect)
- Internal Diagnostic Capabilities
 - Loopback Controls for Communications Link Fault Isolation
 - Break, Parity, Overrun, Framing Error Simulation
- Single +5 Volt Power Supply
- Fully Programmable Serial Interface Characteristics
 - 5-, 6-, 7-, or 8-bit Characters
 - Even, Odd, or No-parity Bit Generation and Detection
 - 1-, 1½-, or 2-stop Bit Generation
 - Baud Rate Generation (DC to 56K Baud)
- INS 8250A Spec. Compatible
- False Start Bit Detection
- Complete Status Reporting Capabilities
- Easily Interfaces to Most Popular Micro Processors
- Line Break Generation and Detection
- Full Prioritized Interrupt System Controls.

PIN CONFIGURATION

