

UM82C212 Memory Controller

Features

- Page interleaved mode access
- Supports 100 ns DRAMs at 16MHz and 80 ns DRAM at 20 MHz
- Supports up to 8Mbytes of on board memory
- Supports LIM-EMS 4.0
- Shadow RAM feature for BIOS execution
- OS/2 optimization feature allows fast switching between protected and real mode
- Staggered refresh to reduce power supply noise
- 84 pin PLCC

Pin Configuration

