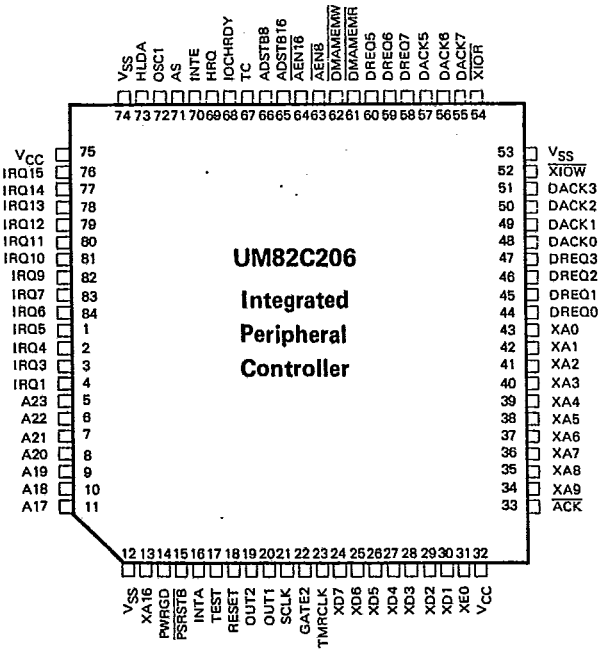
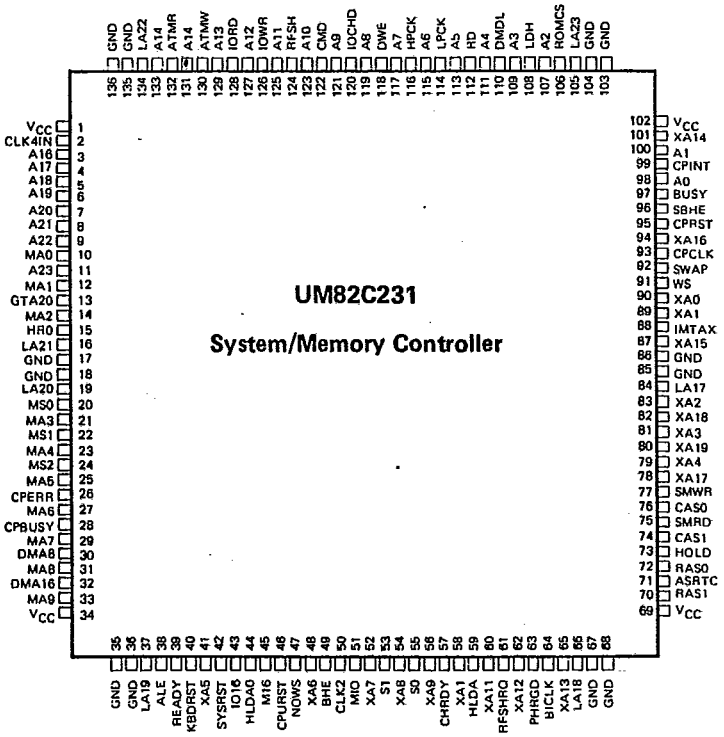
UM82C230 Series 286 PC/AT Chip Set

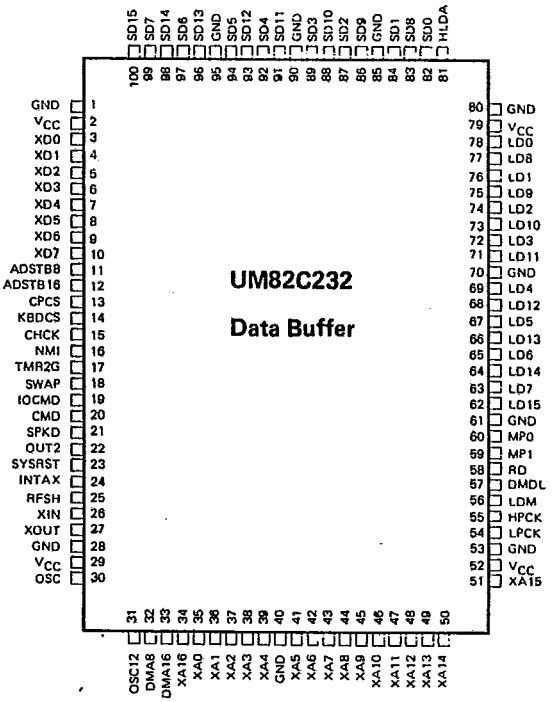
Features

- Fully PC/AT-Compatible for 10MHz and 12MHz systems
- Guaranteed zero wait state for 80 ns DRAMs at 12MHz and one wait state for 120 ns DRAMs at 12.5 MHz
- Supports up to 4MB local memory on board
- Remaps 384 KB to top of system local memory space
- Supports 1M/256K SIM Module
- Supports 12MHz operation with one wait state for 200 ns EPROM
- Commercially available BIOS (Phoenix/Award/AMI) applicable
- Easy design system board. Three VLSI chips and Eight TTLs configure all the logic
- Landmark speed = 15.9 MHz operating at 12MHz, 16.5MHz operating at 12.5MHz

Pin Configurations







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