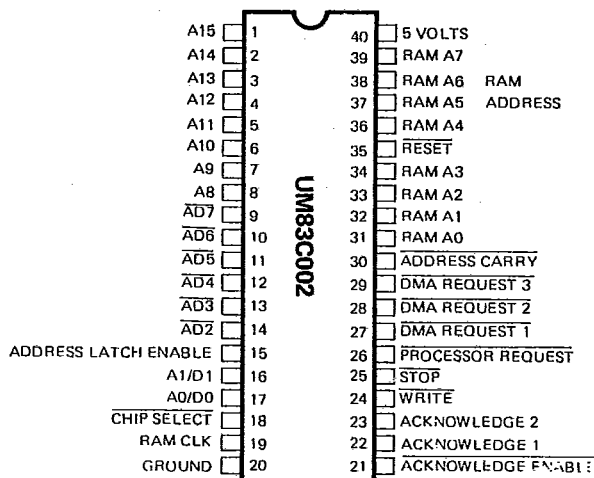


UM83C002 RAM Buffer Controller

Features

- 3 DMA channels
- Host processor port
- Refresh circuit for dynamic RAM built-in
- RAM access priority network
- Address multiplexing for dynamic RAM
- Multiplexed address and data lines from processor
- Pins arranged for easy integration into 8088/8086 systems
- No lost RAM cycles for DMA break-in
- DMA carry output permits DMA across 64K boundaries
- 24 bit timer or baud rate generator
- TTL compatible inputs and outputs. Outputs drive 8 LSTTL loads

Pin Configuration



UM83C003 Hard Disk Controller Interface

Features

- Interfaces to PC XT systems with Hard-Disk controller
- I/O channel ready signal generator (generates wait state)
- Clock generator
- Sector buffer RAM addressing and control
- Data bus drives directly to slot.
- 68-pin PLCC package.
- Supports MFM Disk Controller

Pin Configuration

