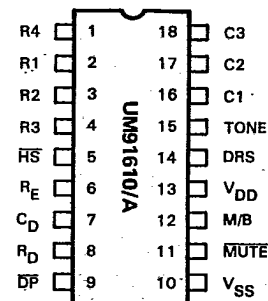# UM91610/A 10 Memory Pulse Dialer

FEATURES

- Ten 16 Digit Number Memories (Location 0-9 with Location 0 as Last Number Redial Memory)
- Low Voltage CMOS Process for Direct Operation from Telephone Line
- Inexpensive RC Oscillator Design with Accuracy Better than ±5% over Temperature and Unit-Unit Variations
- Independent Select Inputs for Variation of Dialing Rates (10pps/20pps), Make/Break Ratio (33 1/3 / 66 2/3 or 40-60)
- Can Interface with Inexpensive XY Matrix Keyboard
- Mute and Pulse Drivers On-Chip
- Inter-Digit Pause 800 ms
- Last Number Redial Can be Easily Transferred to any of the Other 9 Locations
- Tone Output for Valid Key Input

PIN CONFIGURATION

	UM91610/A	
R4 [1		18] C3
R1 [2		17] C2
R2 [3		16] C1
R3 [4		15] TONE
HS [5		14] DRS
R_E [6		13] V_DD
C_D [7		12] M/B
R_D [8		11] MUTE
DP [9		10] V_SS

UM91610C 10 Memory Pulse Dialer

FEATURES

- Ten 16 Digit Memories (Locations 0-9 with Location 0 as the Last Number Redial Memory)
- Low Voltage CMOS Process for Direct Operation from Telephone Line
- Connection to Inexpensive RC Oscillator with Accuracy Better than ±5% over Temperature and Unit-Unit Variations
- High-Speed Master Mode with Automatic Pause, * and # Key Inputs
- Independent Inputs for Selection of Pulse Dialing Rate/Inter-Digit Pause (10 pps/800 ms or 20 pps/400 ms) and Make/Break Ratio (33 1/3 / 66 2/3 or 40−60)
- Interface with Inexpensive XY Matrix Keyboard
- On-Chip Mute and Pulse Drivers
- Easy Transfer of Last Number Redial to any of the Other 9 Locations

PIN CONFIGURATION

	UM91610C	
R4 [1		18] C3
R1 [2		17] C2
R2 [3		16] C1
R3 [4		15] R5
HS [5		14] DRS
R_E [6		13] V_DD
C_D [7		12] M/B
R_D [8		11] MUTE
DP [9		10] V_SS

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