



POWER TRANSISTOR ARRAY

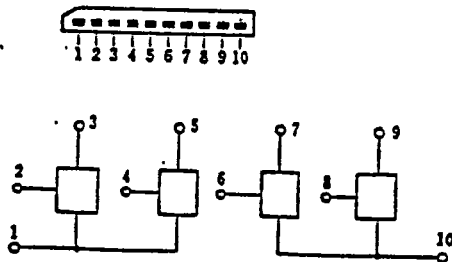
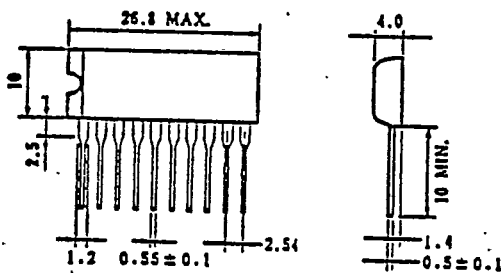
μ PA1426H

NPN SILICON EPITAXIAL POWER TRANSISTOR ARRAY
LOW SPEED SWITCHING (DARLINGTON)

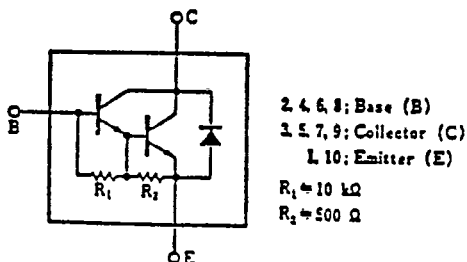
DESCRIPTION

The μ PA1426H is an array of four darlington power transistors. It is especially designed for applications demand for high peak current capability. It is suitable for driving actuators such as solenoids, motors, relays and lamps.

PACKAGE DIMENSIONS(Unit:mm)
AND INTERNAL CONNECTIONS



EQUIVALENT CIRCUIT(1 Unit)



FEATURES

- High hFE(Darlington)
- High peak current capability
- Easy to mount on plastic substrates
- Able to use with high-density mounting

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$).

Collector to Base Voltage	V_{CB0}	150V
Collector to Emitter Voltage	V_{CE0}	80V
Emitter to Base Voltage	V_{EB0}	7.0V
Collector Current(DC)	$I_C(\text{DC})$	$\pm 2.0\text{A/unit}$
Collector Current(pulse)	$I_C(\text{pulse})$	$\pm 4.0\text{A/unit}$
Base Current(DC)	$I_B(\text{DC})$	0.2A/unit
Total Power Dissipation	PT^{**}	3.5W
Total Power Dissipation	PT^{***}	28W
Junction Temperature	T_J	150 $^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +150 $^\circ\text{C}$

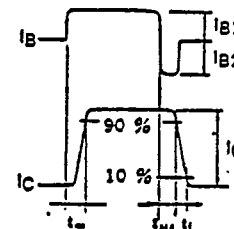
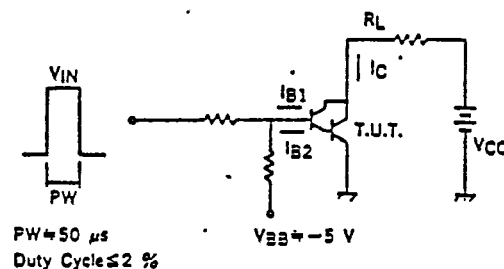
* $PW \leq 300 \mu\text{s}$, Duty Cycle $\leq 10\%$

** When all units are used, $T_a=25^\circ\text{C}$

*** When all units are used, $T_c=25^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

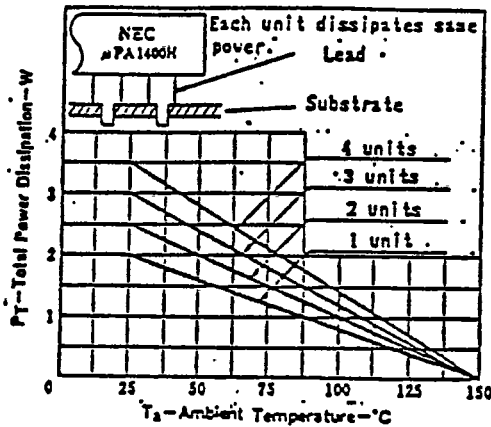
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	I_{CBO}			10	μA	$V_{CB} = 80\text{V}, I_E = 0$
Emitter Cutoff Current	I_{EBO}			1	mA	$V_{EB} = 5.0\text{V}, I_C = 0$
DC Current Gain	h_{FE1}^*	1000	6000		-	$V_{CE} = 2.0\text{V}, I_C = 0.5\text{A}$
DC Current Gain	h_{FE2}^*	2000	7000	30000	-	$V_{CE} = 2.0\text{V}, I_C = 1.0\text{A}$
Collector to Emitter Saturation Voltage	$V_{CE(sat)}^*$		1.0	1.5	V	$I_C = 1.0\text{A}, I_B = 1.0\text{mA}$
Base to Emitter Saturation Voltage	$V_{BE(sat)}^*$		1.6	2.0	V	$I_C = 1.0\text{A}, I_B = 1.0\text{mA}$
Turn-On Time	t_{on}		0.5		μs	$I_C = 1.0\text{A}$
Storage Time	t_{stg}		1.0		μs	$I_{B1} = -I_{B2} = 1.0\text{mA}$
Fall Time	t_f		1.0		μs	$R_L = 50\ \Omega, V_{CC} \approx 50\text{V}$ See Test Circuit.

* Pulsed/ $PW \leq 350\ \mu\text{s}$, Duty Cycle $\leq 2\%$ SWITCHING TIME (t_{on}, t_{stg}, t_f) TEST CIRCUIT

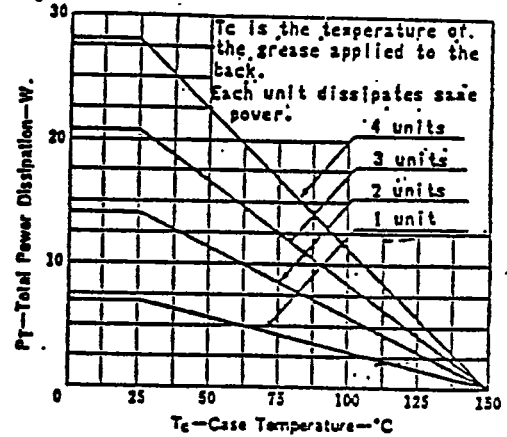
NEC ELECTRONIC DEVICE

TYPICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

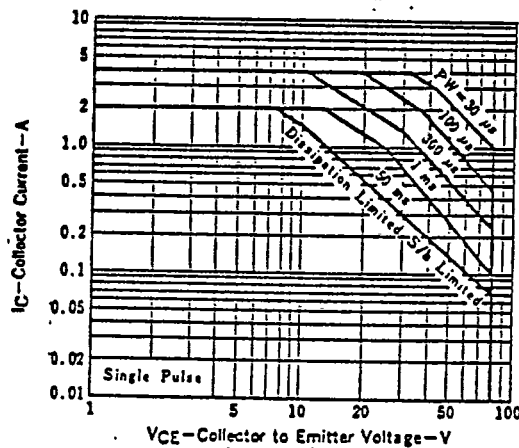
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



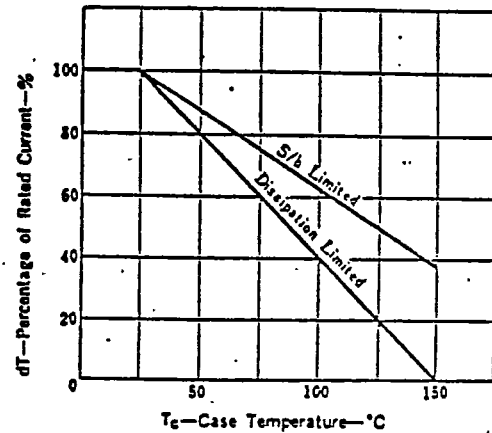
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



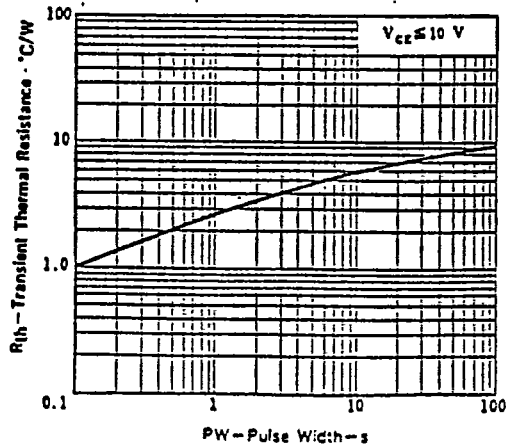
SAFE OPERATING AREA



DERATING CURVE OF SAFE OPERATING AREA



TRANSIENT THERMAL RESISTANCE



COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

