

6427525 N E C ELECTRONICS INC

81C 10161 D

T-79-05-20

NEC
NEC Electronics Inc.

μ PC4557
DUAL HIGH-PERFORMANCE
OPERATIONAL AMPLIFIER

Description

The μ PC4557 is a dual operational amplifier which features higher output drive current than that of the μ PC4558. This feature allows driving of headphone speakers directly. Other characteristics of this device are low noise and no crossover distortion, which make it the ideal choice for audio applications.

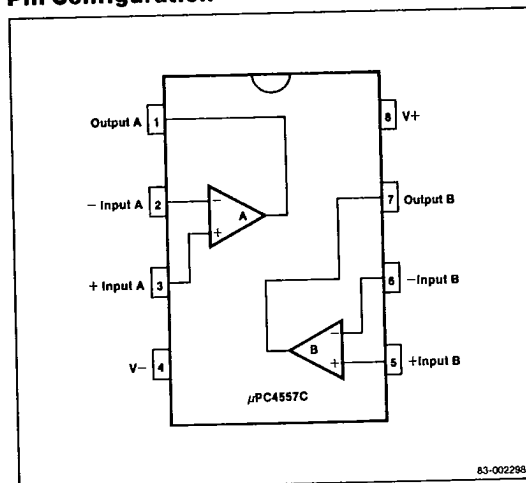
Features

- ☐ Internal frequency compensation
- ☐ Large common-mode and differential input voltage ranges
- ☐ No latch-up
- ☐ Low noise

Ordering Information

Part Number	Package	Operating Temperature Range
μ PC4557C	Plastic DIP	0°C to +70°C

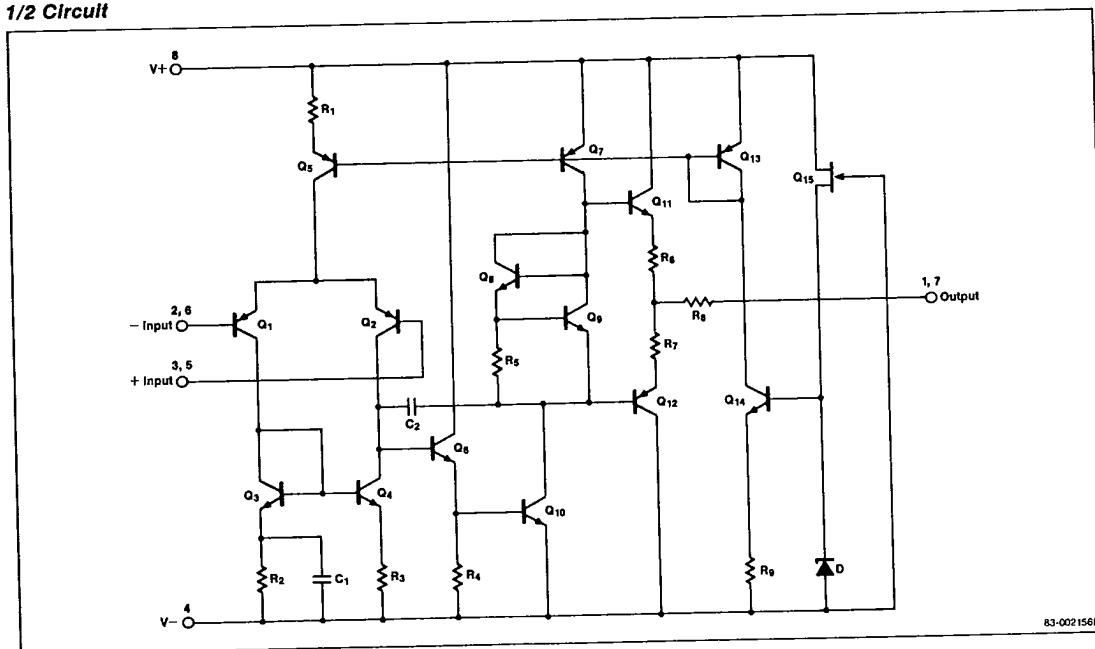
Pin Configuration



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Equivalent Circuit

1/2 Circuit



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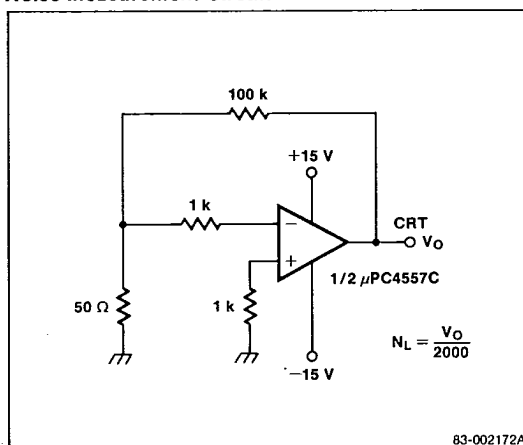
μPC4557**NEC****Absolute Maximum Ratings** $T_A = 25^\circ\text{C}$

Voltage Between V^+ and V^-	36 V
Power Dissipation (Note 1)	700 mW
Differential Input Voltage	± 30 V
Input Voltage (Note 2)	± 15 V
Output Short Circuit Duration	5 s
Operating Temperature Range	0 to $+70^\circ\text{C}$
Storage Temperature Range	-55 to $+125^\circ\text{C}$

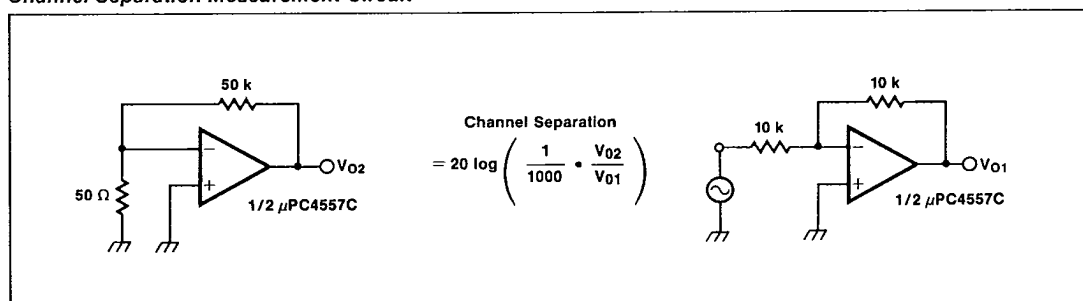
Notes: 1. When the ambient temperature is more than 25°C , derate linearly at $7\text{ mW}/^\circ\text{C}$, ($T_{J\text{max}} = 125^\circ$).

2. For supply voltages less than $\pm 15\text{ V}$, the absolute maximum input voltage is equal to the supply voltage.

Comment: Stress above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Noise Measurement Circuit**Electrical Characteristics** $T_A = 25^\circ\text{C}$, $V_{\pm} = \pm 15\text{ V}$

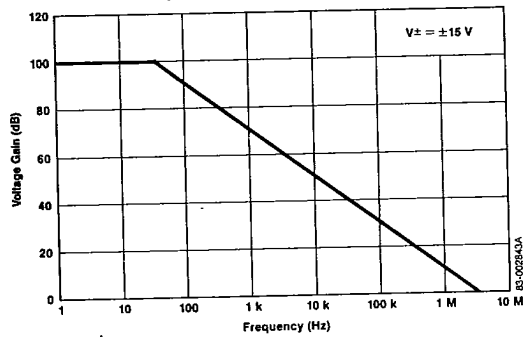
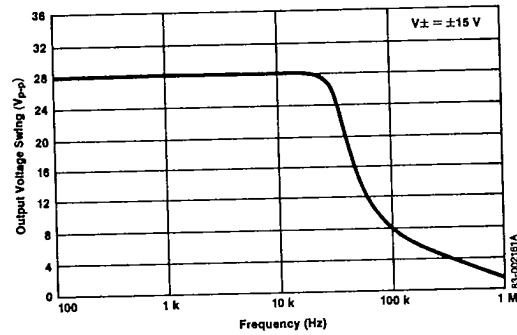
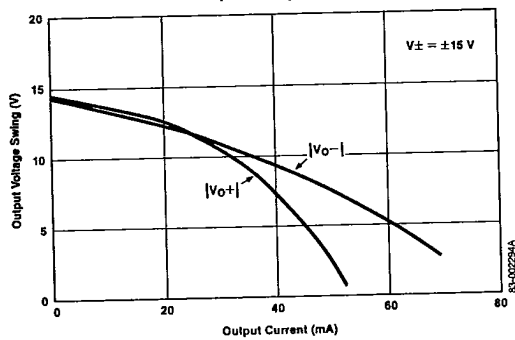
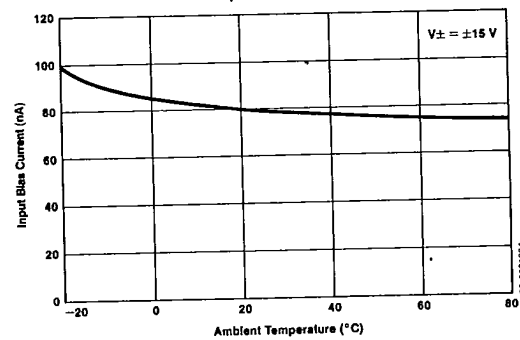
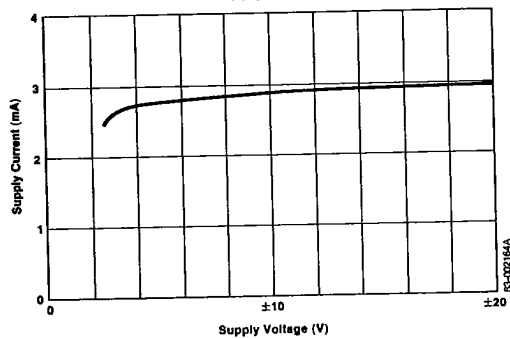
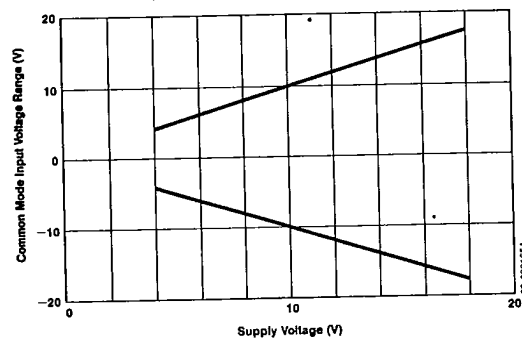
Parameter	Symbol	Limits			Unit	Test Conditions
		Min.	Typ.	Max.		
Input Offset Voltage	V_{IO}		0.5	6.0	mV	$R_S \leq 10\text{ k}\Omega$
Input Offset Current	I_{IO}		5	200	nA	
Input Bias Current	I_b		180	500	nA	
Large Signal Voltage Gain	A_{VOL}	86	100		dB	$R_L \geq 2\text{ k}\Omega$, $V_O = \pm 10\text{ V}$
Power Dissipation	P_D		90	170	mW	Both channels
Common Mode Rejection Ratio	CMRR	70	90		dB	$R_S \leq 10\text{ k}\Omega$
Supply Voltage Rejection Ratio	SVRR		30	150	$\mu\text{V/V}$	$R_S \leq 10\text{ k}\Omega$
Output Voltage Swing	V_{om}	± 12	± 14		V	$R_L \geq 2\text{ k}\Omega$
Output Voltage Swing	V_{om}	± 10	± 11.5		V	$I_O = \pm 25\text{ mA}$
Common Mode Input Voltage Range	V_{icm}	± 12	± 14		V	
Slew Rate	SR		1.0		V/ μs	$A_V = 1$
Input Noise Voltage	e_n		6		$\mu\text{Vp-p}$	$R_S = 1\text{ k}\Omega$, $f = 1\text{ Hz to } 1\text{ kHz}$
Channel Separation	CS		105		dB	$f = 1\text{ kHz}$

Channel Separation Measurement Circuit

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NEC **μ PC4557****Operating Characteristics** $T_A = 25^\circ\text{C}$ **Open Loop Frequency Response****Large Signal Frequency Response****Output Voltage Swing****Input Bias Current****Supply Current****Common Mode Input Voltage Range**

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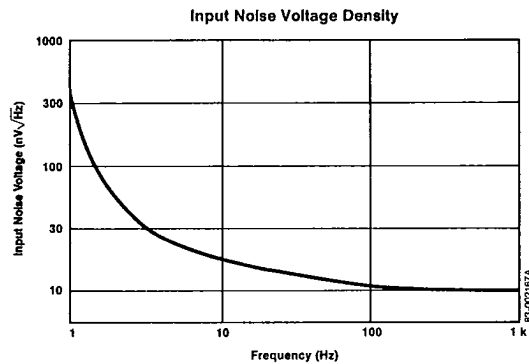
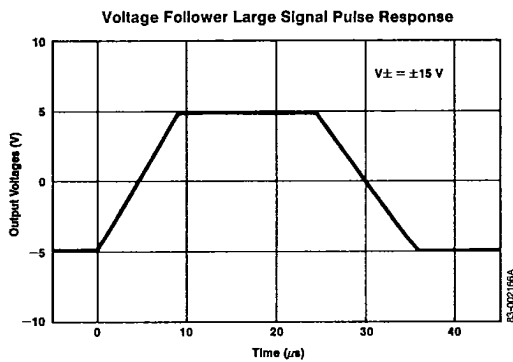
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 μ PC4557**NEC****Operating Characteristics (Cont.)** $T_A = 25^\circ\text{C}$ **Application Circuit****Head Phone AMP**