

### Description

The  $\mu$ PD29F032202/03/04AL is a 32 Mb flash memory designed to execute simultaneously a program or an erase operation in one bank and a read operation from the other bank, making the device suitable for systems that need to store both code and data.

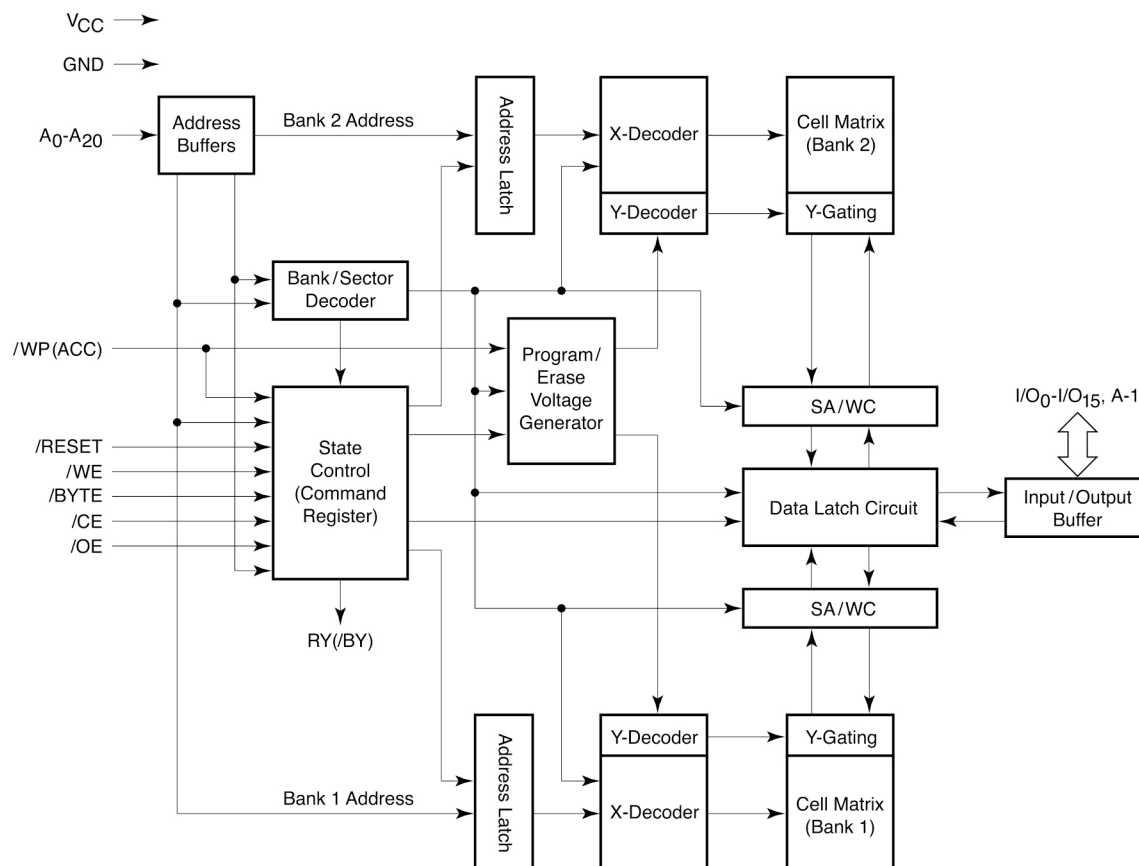
### Features

<b>Product Name</b>	$\mu$ PD29F032202/3/4
<b>Bit Organization</b>	4 Mb x 8 bits / 2 Mwords x 16 bits
<b>Sector Architecture</b>	8 KB x 8 sectors + 64 KB x 63 sectors (x8 mode) 4 Kwords x 8 sectors + 32 Kwords x 63 sectors (x16 mode)
<b>Bank Architecture</b>	$\mu$ PD29F032202: 4 Mb + 28 Mb $\mu$ PD29F032203: 8 Mb + 24 Mb $\mu$ PD29F032204AL: 16 Mb + 16 Mb
<b>Boot Location</b>	Top (-T) or bottom (-B) boot
<b>Power Supply</b>	$V_{CC} = 3.0V - 3.6V$ (-A) $V_{CC} = 2.7V - 3.3V$ (-B)
<b>Random Access Time</b>	85 ns
<b>Operating Temperature</b>	-25° C to +85° C (-X) -40° C to +85° C (-Y)
<b>Package</b>	48-pin TSOP Type I (12 mm x 20 mm) 63-ball tape FBGA (11 mm x 7 mm)

### Product Highlights

- Dual-operation architecture: reduced need for external buffer and system overhead; lower costs
- Three kinds of bank architecture: 4 Mb/28 Mb, 8 Mb/24 Mb or 16 Mb/16 Mb

## Block Diagram



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