



T-51-11

CD4529BM/CD4529BC Dual 4-Channel or Single 8-Channel Analog Data Selector

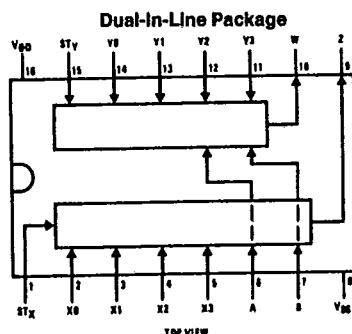
General Description

The CD4529B is a dual 4-channel or a single 8-channel analog data selector, implemented with complementary MOS (CMOS) circuits constructed with N- and P-channel enhancement mode transistors. Dual 4-channel or 8-channel mode operation is selected by proper input coding, with outputs Z and W tied together for the single 8-bit mode. The device is suitable for digital as well as analog applications, including various 1-of-4 and 1-of-8 data selector functions. Since the device is analog and bidirectional, it can also be used for dual binary to 1-of-4 or single 1-of-8 decoder applications.

Features

- Wide supply voltage range 3.0V to 15V
- High noise immunity 0.45 V_{DD} (typ.)
- Low quiescent power dissipation 0.005 μW /package (typ.)@5.0 V_{DC}
- 10 MHz frequency operation (typ.)
- Data paths are bidirectional
- Linear ON resistance [120 Ω (typ.)@15V]
- TRI-STATE® outputs (high impedance disable strobe)
- Plug-in replacement for MC14529B

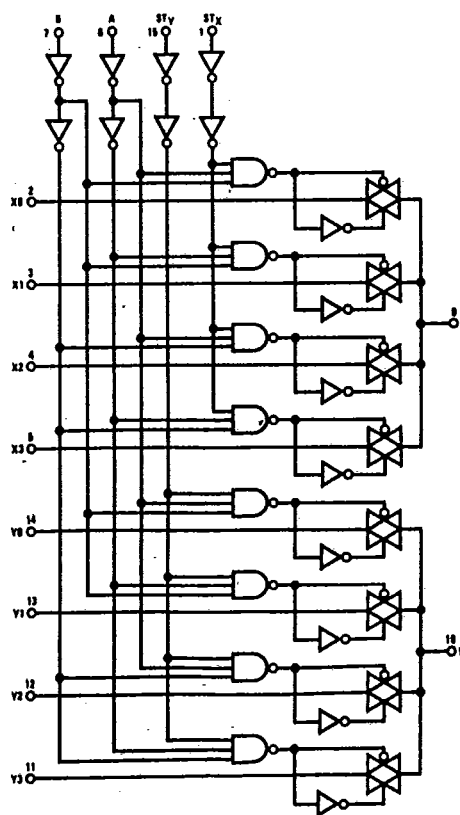
Connection Diagram



Order Number CD4529B*

*Please look into Section 8, Appendix D for availability of various package types.

Logic Diagram



TL/F/5099-1

Truth Table

ST _X	ST _Y	B	A	Z	W
1	1	0	0	X0	Y0
1	1	0	1	X1	Y1
1	1	1	0	X2	Y2
1	1	1	1	X3	Y3
1	0	0	0	X0	
1	0	0	1	X1	
1	0	1	0	X2	
1	0	1	1	X3	
0	1	0	0	Y0	
0	1	0	1	Y1	
0	1	1	0	Y2	
0	1	1	1	Y3	
0	0	X	X	High Impedance (TRI-STATE)	

Dual 4-Channel Mode
2 Outputs

Single 8-Channel Mode
1 Output (Z and W tied together)

X = Don't care

T-51-11

Absolute Maximum Ratings (Notes 1 & 2)

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

DC Supply Voltage (V_{DD}) -0.5V to +18VInput Voltage (V_{IN}) -0.5V to V_{DD} + 0.5VStorage Temperature Range (T_S) -65°C to +150°CPower Dissipation (P_D)

Dual-In-Line 700 mW

Small Outline 500 mW

Lead Temperature (T_L)

(Soldering, 10 seconds) 260°C

Recommended Operating**Conditions** (Note 2)DC Supply Voltage (V_{DD}) 3V to 15VInput Voltage (V_{IN}) 0 to V_{DD} Operating Temperature Range (T_A)

CD4529BM -55°C to +125°C

CD4529BC -40°C to +85°C

DC Electrical Characteristics CD4529BM (Note 2)

Symbol	Parameter	Conditions	-55°C		+25°C			+125°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I_{DD}	Quiescent Device Current	$V_{DD} = 5V$		1.0		0.001	1.0		60	μA
		$V_{DD} = 10V$		1.0		0.002	1.0		60	μA
		$V_{DD} = 15V$		2.0		0.003	2.0		120	μA
V_{OL}	Low Level Output Voltage	$V_{IL} = 0V, V_{IH} = V_{DD}, I_O < 1 \mu A$								
		$V_{DD} = 5V$		0.05		0	0.05		0.05	V
		$V_{DD} = 10V$		0.05		0	0.05		0.05	V
V_{OH}	High Level Output Voltage	$V_{IL} = 0V, V_{IH} = V_{DD}, I_O < 1 \mu A$								
		$V_{DD} = 5V$	4.95		4.95	5.0		4.95		V
		$V_{DD} = 10V$	9.95		9.95	10.0		9.95		V
V_{IL}	Low Level Input Voltage (Note 3)	$V_{DD} = 5V$		1.5		2.25	1.5		1.5	V
		$V_{DD} = 10V$		3.0		4.50	3.0		3.0	V
		$V_{DD} = 15V$		4.0		6.75	4.0		4.0	V
V_{IH}	High Level Input Voltage (Note 3)	$V_{DD} = 5V$	3.5		3.5	2.75		3.5		V
		$V_{DD} = 10V$	7.0		7.0	5.50		7.0		V
		$V_{DD} = 15V$	11.0		11.0	8.25		11.0		V
I_{IN}	Input Current	$V_{DD} = 15V$								
		$V_{IN} = 0V$		-0.1		-10 ⁻⁵	-0.1		-1.0	μA
		$V_{IN} = 15V$		0.1		10 ⁻⁵	0.1		1.0	μA
R_{ON}	ON Resistance	$V_{DD} = 5V, V_{SS} = -5V$								
		$V_{IN} = 5V$		400		165	480		640	Ω
		$V_{IN} = -5V$		400		100	480		640	Ω
		$V_{IN} = \pm 0.25V$		400		155	480		640	Ω
		$V_{DD} = 7.5V, V_{SS} = -7.5V$								
		$V_{IN} = 7.5V$		240		135	270		400	Ω
		$V_{IN} = -7.5V$		240		75	270		400	Ω
		$V_{IN} = \pm 0.25V$		240		100	270		400	Ω
		$V_{DD} = 10V, V_{SS} = 0V$								
		$V_{IN} = 10V$		400		165	480		640	Ω
		$V_{IN} = 0.25V$		400		100	480		640	Ω
		$V_{IN} = 5.6V$		400		160	480		640	Ω
		$V_{DD} = 15V, V_{SS} = 0V$								
		$V_{IN} = 15V$		250		135	270		400	Ω
		$V_{IN} = 0.25V$		250		75	270		400	Ω
		$V_{IN} = 9.3V$		250		110	270		400	Ω
I_{OFF}	Input to Output Leakage Current	$V_{SS} = -5V, V_{DD} = 5V, V_{IN} = 5V, V_{OUT} = -5V$		± 125		± 0.001	± 125		± 1250	nA
		$V_{SS} = -5V, V_{DD} = 5V, V_{IN} = -5V, V_{OUT} = 5V$		± 125		± 0.001	± 125		± 1250	nA
		$V_{SS} = -7.5V, V_{DD} = 7.5V, V_{IN} = 7.5V, V_{OUT} = -7.5V$		± 250		± 0.0015	± 250		± 2500	nA
		$V_{SS} = -7.5V, V_{DD} = 7.5V, V_{IN} = -7.5V, V_{OUT} = 7.5V$		± 250		± 0.0015	± 250		± 2500	nA

CD4529BM/CD4529BC

5

DC Electrical Characteristics CD4529BC (Note 2) (Continued) T-51-11

Symbol	Parameter	Conditions	-40°C		+25°C			+85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I _{DD}	Quiescent Device Current	V _{DD} = 5V		5.0		0.001	5.0		70	μA
		V _{DD} = 10V		5.0		0.002	5.0		70	μA
		V _{DD} = 15V		10.0		0.003	10.0		140	μA
V _{OL}	Low Level Output Voltage	V _{IL} = 0V, V _{IH} = V _{DD} , I _O < 1 μA								
		V _{DD} = 5V		0.05			0.05		0.05	V
		V _{DD} = 10V		0.05			0.05		0.05	V
		V _{DD} = 15V		0.05			0.05		0.05	V
V _{OH}	High Level Output Voltage	V _{IL} = 0V, V _{IH} = V _{DD} , I _O < 1 μA								
		V _{DD} = 5V	4.95		4.95	5.00		4.95		V
		V _{DD} = 10V	9.95		9.95	10.00		9.95		V
		V _{DD} = 15V	14.95		14.95	15.00		14.95		V
V _{IL}	Low Level Input Voltage (Note 3)	V _{DD} = 5V, V _{OUT} = 4.5 or 0.5 V _{DC}		1.5		2.25	1.5		1.5	V
		V _{DD} = 10V, V _{OUT} = 9.0 or 1.0 V _{DC}		3.0		4.50	3.0		3.0	V
		V _{DD} = 15V, V _{OUT} = 13.5 or 1.5 V _{DC}		4.0		6.75	4.0		4.0	V
V _{IH}	High Level Input Voltage (Note 3)	V _{DD} = 5V, V _{OUT} = 0.5 or 4.5 V _{DC}	3.5		3.5	2.75		3.5		V
		V _{DD} = 10V, V _{OUT} = 1.0 or 9.0 V _{DC}	7.0		7.0	5.50		7.0		V
		V _{DD} = 15V, V _{OUT} = 1.5 or 13.5 V _{DC}	11.0		11.0	8.25		11.0		V
I _{IN}	Input Current	V _{DD} = 15V								
		V _{IN} = 0V		-0.3		-10 ⁻⁵	-0.3		-1.0	μA
		V _{IN} = 15V		0.3		10 ⁻⁵	0.3		1.0	μA
R _{ON}	ON Resistance	V _{DD} = 5V, V _{SS} = -5V								
		V _{IN} = 5V		410		165	480		560	Ω
		V _{IN} = -5V		410		100	480		560	Ω
		V _{IN} = ±0.25V		410		155	480		560	Ω
		V _{DD} = 7.5V, V _{SS} = -7.5V								
		V _{IN} = 7.5V		250		135	270		350	Ω
		V _{IN} = -7.5V		250		75	270		350	Ω
		V _{IN} = ±0.25V		250		100	270		350	Ω
		V _{DD} = 10V, V _{SS} = 0V								
		V _{IN} = 10V		410		165	480		560	Ω
		V _{IN} = 0.25V		410		100	480		560	Ω
		V _{IN} = 5.6V		410		160	480		560	Ω
		V _{DD} = 15V, V _{SS} = 0V								
		V _{IN} = 15V		250		135	270		350	Ω
		V _{IN} = 0.25V		250		75	270		350	Ω
		V _{IN} = 9.3V		250		110	270		350	Ω
I _{OFF}	Input-Output Leakage Current	V _{SS} = -5V, V _{DD} = 5V								
		V _{IN} = 5V, V _{OUT} = -5V		±125		±0.001	±125		±500	nA
		V _{IN} = -5V, V _{OUT} = 5V		±125		±0.001	±125		±500	nA
		V _{SS} = -7.5V, V _{DD} = 7.5V								
		V _{IN} = 7.5V, V _{OUT} = -7.5V		±250		±0.0015	±250		±1000	nA
		V _{IN} = -7.5V, V _{OUT} = 7.5V		±250		±0.0015	±250		±1000	nA

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The tables of "Recommended Operating Conditions" and "Electrical Characteristics" provide conditions for actual device operation.

Note 2: V_{SS} = 0V unless otherwise specified.

Note 3: Switch OFF is defined as |I_O| ≤ 10 μA, switch ON as defined by R_{ON} specification.

AC Characteristics* CD4529BM/CD4539BCT_A = 25°C, R_L = 1 kΩ, t_r = t_f = 20 ns, unless otherwise specified.

T-51-11

CD4529BM/CD4539BC

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t _{PLH} , t _{PHL}	V _{IN} to V _{OUT} Propagation Delay	V _{SS} = 0V, C _L = 50 pF V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V		20 10 8	40 20 15	ns ns ns
t _{PLH} , t _{PHL}	Control to Output Propagation Delay	V _{IN} = V _{DD} or V _{SS} , C _L = 50 pF V _{IN} ≤ 10V V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V		200 80 50	400 160 120	ns ns ns
f _{MAX}	Maximum Control Input Pulse Frequency	V _{SS} = 0V, C _L = 50 pF V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V		5 10 12		MHz MHz MHz
	Crosstalk, Control to Output	R _{OUT} = 10 kΩ, C _L = 50 pF, V _{SS} = 0 V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V		5.0 5.0 5.0		mV mV mV
	Noise Voltage	f = 100 Hz, V _{SS} = 0V V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V		24 25 30		nV/√cycle nV/√cycle nV/√cycle
	Sine Wave (Distortion)	f = 100 kHz, V _{SS} = 0V V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V V _{IN} = 1.77Vrms Centered at 0V, R _L = 10 kΩ, f = 1 kHz, V _{SS} = -5V, V _{DD} = 5V		12 12 15 0.36		nV/√cycle nV/√cycle nV/√cycle %
I _{LOSS}	Insertion Loss, $I_{LOSS} = 20 \log_{10} \frac{V_{OUT}}{V_{IN}}$	V _{IN} = 177Vrms Centered at 0V, V _{SS} = -5V, V _{DD} = 5V R _L = 1 kΩ R _L = 10 kΩ R _L = 100 kΩ R _L = 1 MΩ		2.0 0.8 0.25 0.01		dB dB dB dB
BW	Bandwidth, -3dB	V _{IN} = 177Vrms Centered at 0 Vdc, V _{SS} = -5V, V _{DD} = 5V R _L = 1 kΩ R _L = 10 kΩ R _L = 100 kΩ R _L = 1 MΩ	35 28 27 26			MHz MHz MHz MHz
	Feedthrough and Crosstalk, $20 \log_{10} \frac{V_{OUT}}{V_{IN}} = -50 \text{ db}$	V _{SS} = -5V, V _{DD} = 5V R _L = 1 kΩ R _L = 10 kΩ R _L = 100 kΩ R _L = 1 MΩ	850 100 12 1.5			kHz kHz kHz Khz

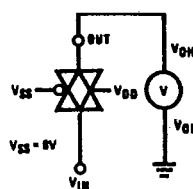
*AC Parameters are guaranteed by DC correlated testing.

5

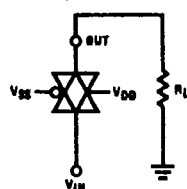
Test Circuits and Switching Time Waveforms

T-51-11

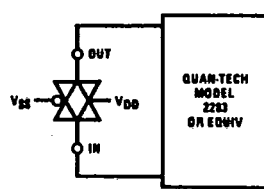
Output Voltage



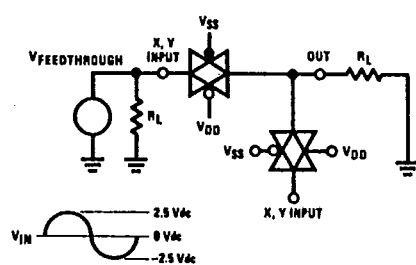
R_{ON} Characteristics



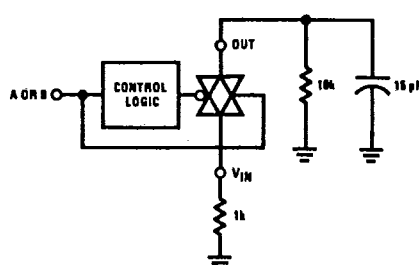
Noise Voltage



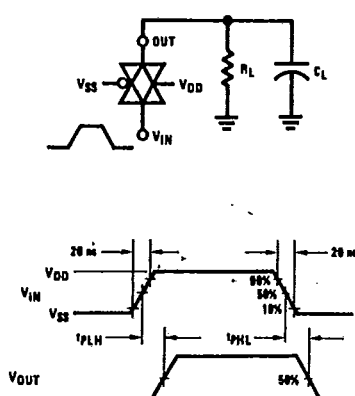
Frequency Response



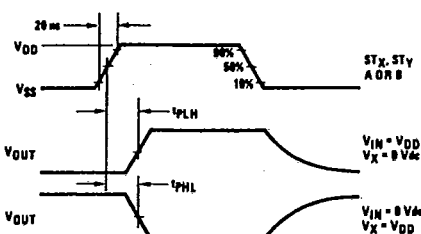
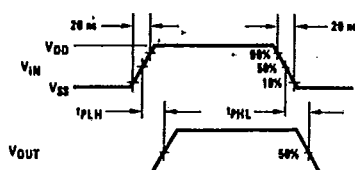
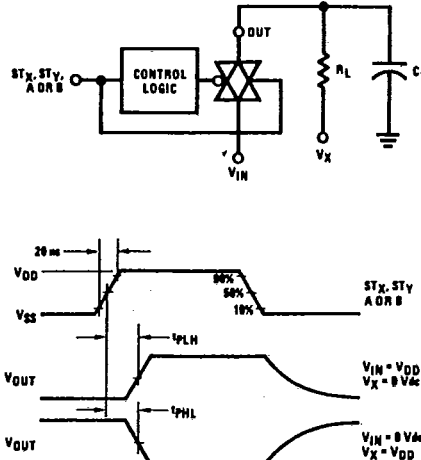
Crosstalk



Propagation Delay



Turn-ON Delay Time



TL/F/5899-2

