

# UT54ACS253/UT54ACTS253

## Radiation-Hardened

## Dual 4-Input Multiplexers, Three-State Outputs

### FEATURES

- Permits multiplexing from N lines to 1 line
- Performs parallel-to-serial conversion
- 1.2μ radiation-hardened CMOS
  - Latchup immune
- High speed
- Low power consumption
- Single 5 volt supply
- Available QML Q or V processes
- Flexible package
  - 16-pin DIP
  - 16-lead flatpack

### DESCRIPTION

The UT54ACS253 and the UT54ACTS253 are 1-line to 4-line multiplexers that contain drivers to supply full binary decoding. Separate output control inputs are provided for each of the two four-line sections.

Use the three-state outputs to drive data lines in bus-organized systems. With all but one of the common outputs disabled the low-impedance of the single enable output will drive the bus line to a high or low logic level. Each output has its own strobe (G).

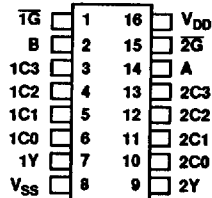
The devices are characterized over full military temperature range of -55°C to +125°C.

### FUNCTION TABLE

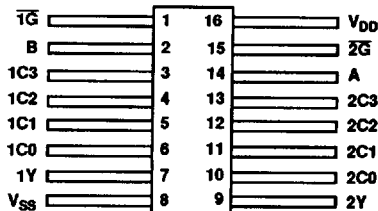
| SELECT INPUTS |   | DATA INPUTS |    |    |    | OUTPUT CONTROL | OUTPUT |
|---------------|---|-------------|----|----|----|----------------|--------|
| B             | A | C0          | C1 | C2 | C3 | $\bar{G}$      | Y      |
| X             | X | X           | X  | X  | X  | H              | Z      |
| L             | L | L           | X  | X  | X  | L              | L      |
| L             | L | H           | X  | X  | X  | L              | H      |
| L             | H | X           | L  | X  | X  | L              | L      |
| L             | H | X           | H  | X  | X  | L              | H      |
| H             | L | X           | X  | L  | X  | L              | L      |
| H             | L | X           | X  | H  | X  | L              | H      |
| H             | H | X           | X  | X  | L  | L              | L      |
| H             | H | X           | X  | X  | H  | L              | H      |

### PINOUTS

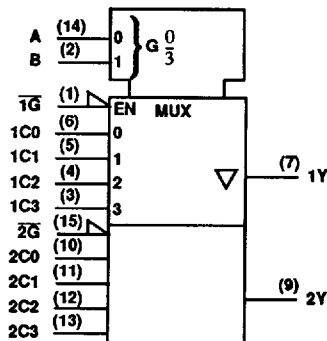
16-Pin DIP  
Top View



16-Lead Flatpack  
Top View



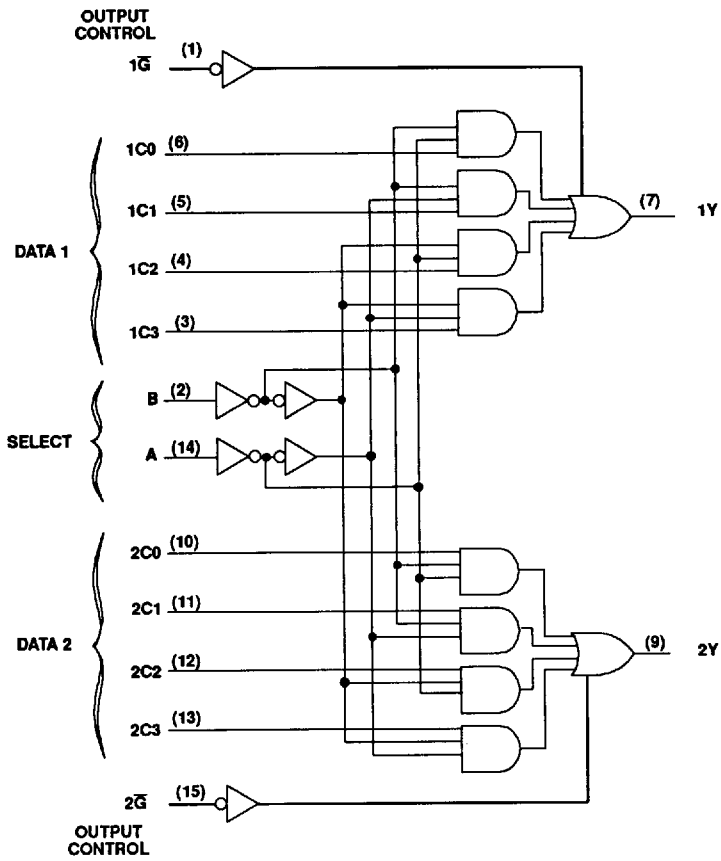
### LOGIC SYMBOL



Note:

1. These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

## LOGIC DIAGRAM



**RADIATION HARDNESS SPECIFICATIONS <sup>1</sup>**

| PARAMETER                        | LIMIT  | UNITS                   |
|----------------------------------|--------|-------------------------|
| Total Dose                       | 1.0E6  | rads(Si)                |
| SEU & SEL Threshold <sup>2</sup> | 80     | MeV-cm <sup>2</sup> /mg |
| Neutron Fluence                  | 1.0E14 | n/cm <sup>2</sup>       |

**Notes:**

1. Logic will not latchup during radiation exposure within the limits defined in the table.

2. Device storage elements are immune to SEU effects.

**ABSOLUTE MAXIMUM RATINGS**

| SYMBOL           | PARAMETER                              | LIMIT                      | UNITS |
|------------------|----------------------------------------|----------------------------|-------|
| V <sub>DD</sub>  | Supply voltage                         | -0.3 to 7.0                | V     |
| V <sub>I/O</sub> | Voltage any pin                        | -.3 to V <sub>DD</sub> +.3 | V     |
| T <sub>STG</sub> | Storage Temperature range              | -65 to +150                | °C    |
| T <sub>J</sub>   | Maximum junction temperature           | +175                       | °C    |
| T <sub>LS</sub>  | Lead temperature (soldering 5 seconds) | +300                       | °C    |
| Θ <sub>JC</sub>  | Thermal resistance junction to case    | 20                         | °C/W  |
| I <sub>I</sub>   | DC input current                       | ±10                        | mA    |
| P <sub>D</sub>   | Maximum power dissipation              | 1                          | W     |

**Note:**

1. Stresses outside the listed absolute maximum ratings may cause permanent damage to the device. This is a stress rating only, functional operation of the device at these or any other conditions beyond limits indicated in the operational sections is not recommended. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**RECOMMENDED OPERATING CONDITIONS**

| SYMBOL          | PARAMETER             | LIMIT                | UNITS |
|-----------------|-----------------------|----------------------|-------|
| V <sub>DD</sub> | Supply voltage        | 4.5 to 5.5           | V     |
| V <sub>IN</sub> | Input voltage any pin | 0 to V <sub>DD</sub> | V     |
| T <sub>C</sub>  | Temperature range     | -55 to +125          | °C    |

**DC ELECTRICAL CHARACTERISTICS <sup>7</sup>**(V<sub>DD</sub> = 5.0V ±10%; V<sub>SS</sub> = 0V <sup>6</sup>, -55°C < T<sub>C</sub> < +125°C)

| SYMBOL             | PARAMETER                                               | CONDITION                                            | MIN                                         | MAX                      | UNIT       |
|--------------------|---------------------------------------------------------|------------------------------------------------------|---------------------------------------------|--------------------------|------------|
| V <sub>IL</sub>    | Low-level input voltage <sup>1</sup><br>ACTS<br>ACS     |                                                      |                                             | 0.8<br>.3V <sub>DD</sub> | V          |
| V <sub>IH</sub>    | High-level input voltage <sup>1</sup><br>ACTS<br>ACS    |                                                      | .5V <sub>DD</sub><br>.7V <sub>DD</sub>      |                          | V          |
| I <sub>IN</sub>    | Input leakage current<br>ACTS/ACS                       | V <sub>IN</sub> = V <sub>DD</sub> or V <sub>SS</sub> | -1                                          | 1                        | μA         |
| V <sub>OL</sub>    | Low-level output voltage <sup>3</sup><br>ACTS<br>ACS    | I <sub>OL</sub> = 8.0mA<br>I <sub>OL</sub> = 100μA   |                                             | 0.40<br>0.25             | V          |
| V <sub>OH</sub>    | High-level output voltage <sup>3</sup><br>ACTS<br>ACS   | I <sub>OH</sub> = -8.0mA<br>I <sub>OH</sub> = -100μA | .7V <sub>DD</sub><br>V <sub>DD</sub> - 0.25 |                          | V          |
| I <sub>OZ</sub>    | Three-state output leakage current                      | V <sub>O</sub> = V <sub>DD</sub> and V <sub>SS</sub> | -20                                         | 20                       | μA         |
| I <sub>OS</sub>    | Short-circuit output current <sup>2,4</sup><br>ACTS/ACS | V <sub>O</sub> = V <sub>DD</sub> and V <sub>SS</sub> | -200                                        | 200                      | mA         |
| P <sub>total</sub> | Power dissipation <sup>8,9</sup>                        | C <sub>L</sub> = 50pF                                |                                             | 2.1                      | mW/<br>MHz |
| I <sub>DDQ</sub>   | Quiescent Supply Current                                | V <sub>DD</sub> = 5.5V                               |                                             | 10                       | μA         |
| C <sub>IN</sub>    | Input capacitance <sup>5</sup>                          | f = 1MHz @ 0V                                        |                                             | 15                       | pF         |
| C <sub>OUT</sub>   | Output capacitance <sup>5</sup>                         | f = 1MHz @ 0V                                        |                                             | 15                       | pF         |

**Notes:**

- Functional tests are conducted in accordance with MIL-STD-883 with the following input test conditions: V<sub>IH</sub> = V<sub>IH</sub>(min) + 20%, - 0%; V<sub>IL</sub> = V<sub>IL</sub>(max) + 0%, - 50%, as specified herein, for TTL, CMOS, or Schmitt compatible inputs. Devices may be tested using any input voltage within the above specified range, but are guaranteed to V<sub>IH</sub>(min) and V<sub>IL</sub>(max).
- Supplied as a design limit but not guaranteed or tested.
- Per MIL-M-38510, for current density ≤ 5.0E5 amps/cm<sup>2</sup>, the maximum product of load capacitance (per output buffer) times frequency should not exceed 3,765 pF MHz.
- Not more than one output may be shorted at a time for maximum duration of one second.
- Capacitance measured for initial qualification and when design changes may affect the value. Capacitance is measured between the designated terminal and V<sub>SS</sub> at frequency of 1MHz and a signal amplitude of 50mV rms maximum.
- Maximum allowable relative shift equals 50mV.
- All specifications valid for radiation dose ≤ 1E6 rads(Si).
- Power does not include power contribution of any TTL output sink current.
- Power dissipation specified per switching output.

**AC ELECTRICAL CHARACTERISTICS <sup>2</sup>**(V<sub>DD</sub> = 5.0V ±10%; V<sub>SS</sub> = 0V <sup>1</sup>, -55°C < T<sub>C</sub> < +125°C)

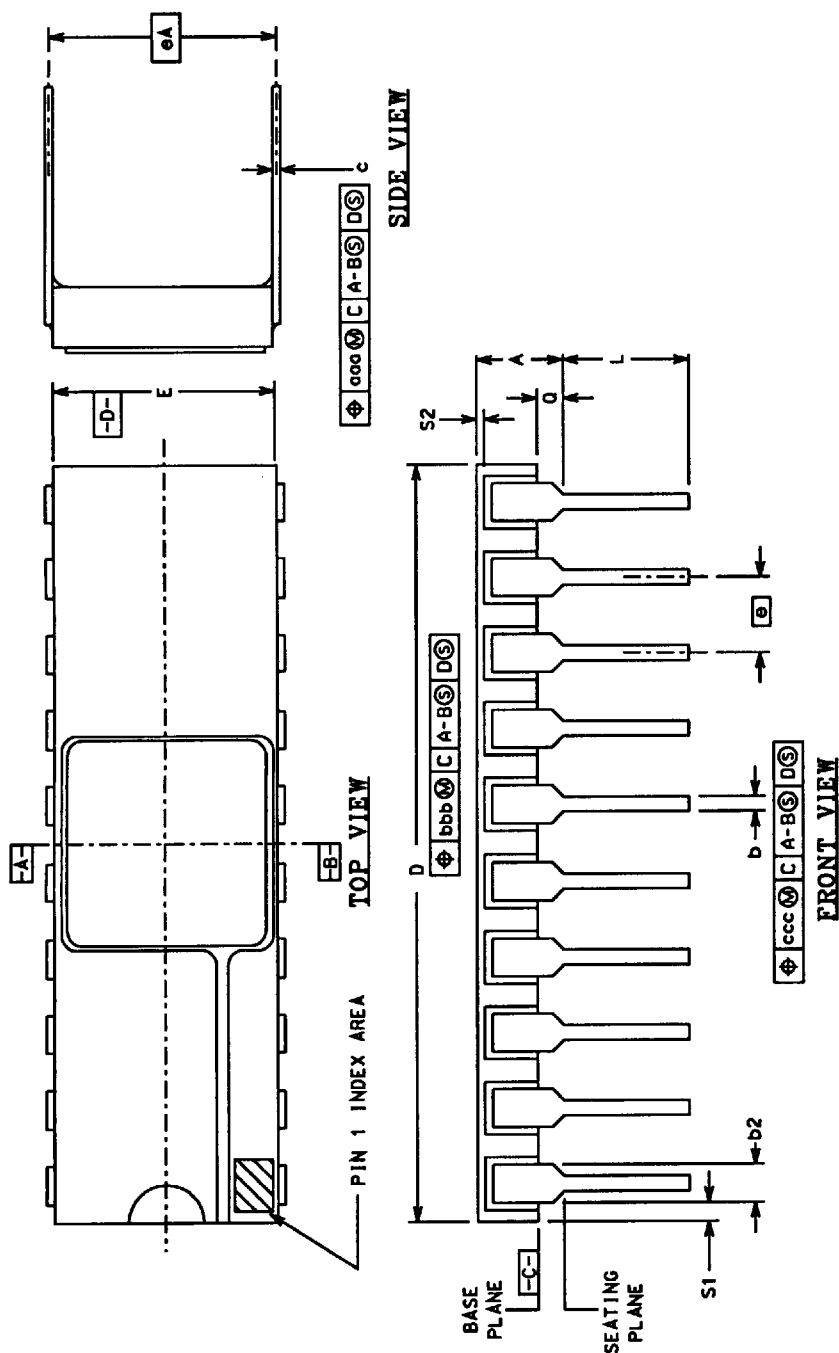
| SYMBOL           | PARAMETER                             | MINIMUM | MAXIMUM | UNIT |
|------------------|---------------------------------------|---------|---------|------|
| t <sub>PLH</sub> | Select to output Yn                   | 2       | 12      | ns   |
| t <sub>PHL</sub> | Select to output Yn                   | 2       | 16      | ns   |
| t <sub>PLH</sub> | Data to output Yn                     | 2       | 14      | ns   |
| t <sub>PHL</sub> | Data to output Yn                     | 2       | 16      | ns   |
| t <sub>PZH</sub> | $\overline{G}$ low to Yn active       | 2       | 12      | ns   |
| t <sub>PZL</sub> | $\overline{G}$ low to Yn active       | 1       | 12      | ns   |
| t <sub>PHZ</sub> | $\overline{G}$ high to Yn three-state | 2       | 11      | ns   |
| t <sub>PLZ</sub> | $\overline{G}$ high to Yn three-state | 2       | 10      | ns   |

**Notes:**

1. Maximum allowable relative shift equals 50mV.

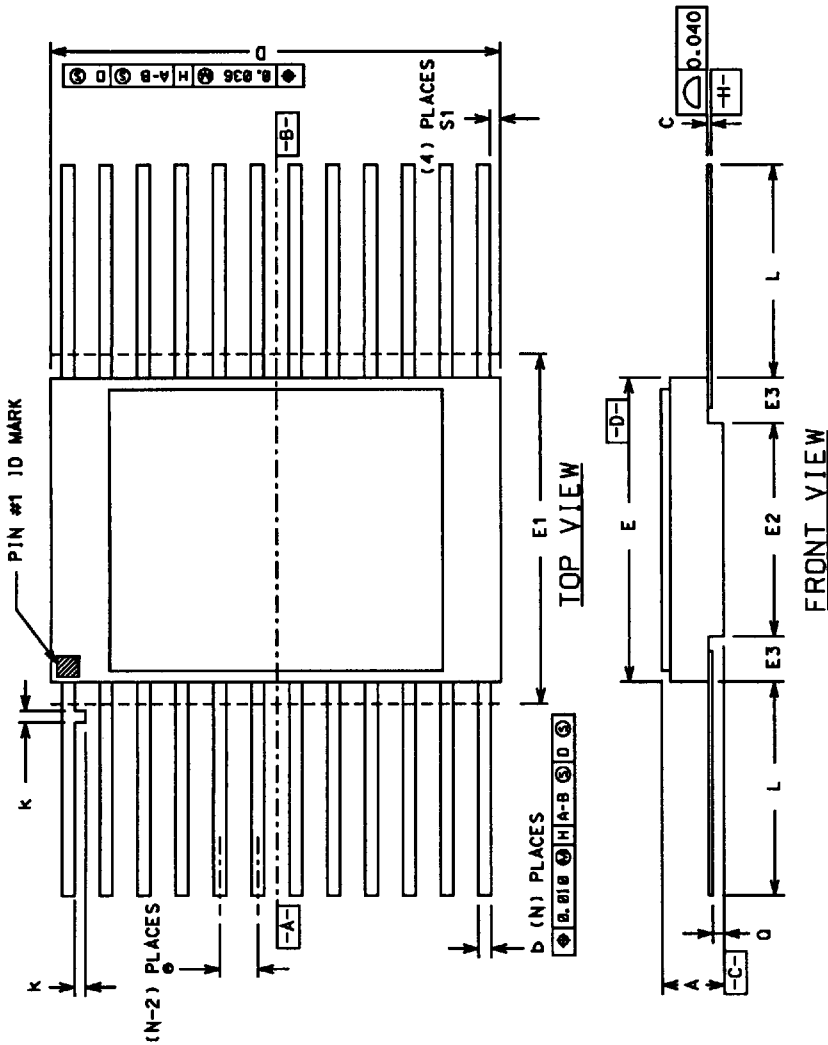
2. All specifications valid for radiation dose ≤ 1E6 rads(Si).

## Side-Brazed Packages



| PKG<br>CON# | IG  | LEAD<br>COUNT | MIL-STD-<br>1835 DWG<br>CONF C | DIMENSION SYMBOLS |       |       |       |       |       |       |       |       |       |       |       |       |      |     |
|-------------|-----|---------------|--------------------------------|-------------------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|------|-----|
|             |     |               |                                | A                 | B     | B2    | C     | D     | E     | e     | eA    | L     | Q     | S1    | S2    | 000   | 0000 | DOE |
| -01         | D-1 | 14            |                                | 0.200             | 0.026 | 0.065 | 0.018 | 0.785 | 0.310 | 0.100 | 0.300 | 0.200 | 0.060 | 0.015 | 0.030 | 0.010 |      |     |
| -02         | D-2 | 16            |                                | 0.200             | 0.026 | 0.065 | 0.018 | 0.840 | 0.310 | 0.100 | 0.300 | 0.200 | 0.060 | 0.015 | 0.030 | 0.010 |      |     |
| -03         | D-8 | 20            |                                | 0.200             | 0.026 | 0.065 | 0.018 | 1.060 | 0.310 | 0.100 | 0.300 | 0.200 | 0.070 | 0.015 | 0.030 | 0.010 |      |     |

## Flatpack Packages



| PKG<br>CONF IG | LEAD<br>COUNT | MIL-STD<br>1836<br>Dwg<br>CONF B | DIMENSION SYMBOLS |       |       |       |       |       |       |       |      |       |       |       |       |
|----------------|---------------|----------------------------------|-------------------|-------|-------|-------|-------|-------|-------|-------|------|-------|-------|-------|-------|
|                |               |                                  | A                 | b     | c     | D     | E     | E1    | E2    | E3    | e    | k     | L     | O     | S1    |
| -03            | 14            | F-2A                             | 0.115             | 0.022 | 0.009 | 0.390 | 0.260 | 0.290 | ----  | ----  | ---- | 0.050 | 0.370 | 0.045 | ----  |
|                |               |                                  | 0.045             | 0.015 | 0.004 | 0.235 | 0.235 | ----  | 0.130 | 0.030 | BSC  | 0.008 | 0.270 | 0.026 | 0.005 |
| -04            | 16            | F-5A                             | 0.115             | 0.022 | 0.009 | 0.440 | 0.285 | 0.315 | ----  | ----  | ---- | 0.050 | 0.370 | 0.045 | ----  |
|                |               |                                  | 0.045             | 0.015 | 0.004 | 0.245 | 0.245 | ----  | 0.130 | 0.030 | BSC  | 0.008 | 0.250 | 0.026 | 0.005 |
| -05            | 20            | F-9A                             | 0.115             | 0.022 | 0.009 | 0.540 | 0.300 | 0.330 | ----  | ----  | ---- | 0.050 | 0.370 | 0.045 | ----  |
|                |               |                                  | 0.045             | 0.015 | 0.004 | 0.245 | 0.245 | ----  | 0.130 | 0.030 | BSC  | 0.008 | 0.250 | 0.026 | 0.000 |