MICROTECH

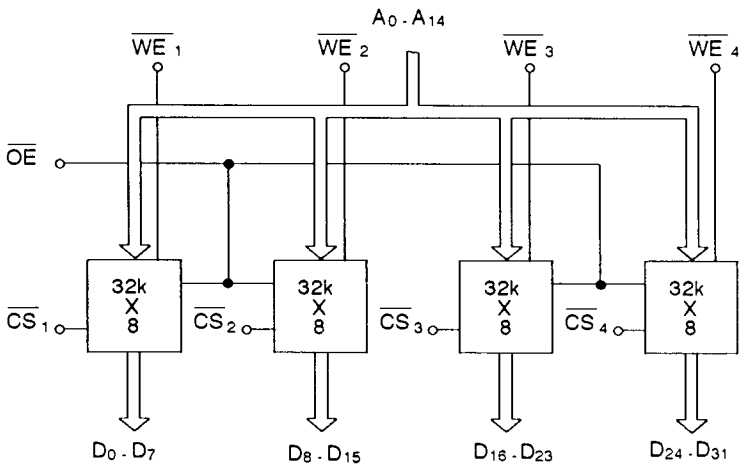
µT8S128

µT8 series 128k - Byte Static RAM module

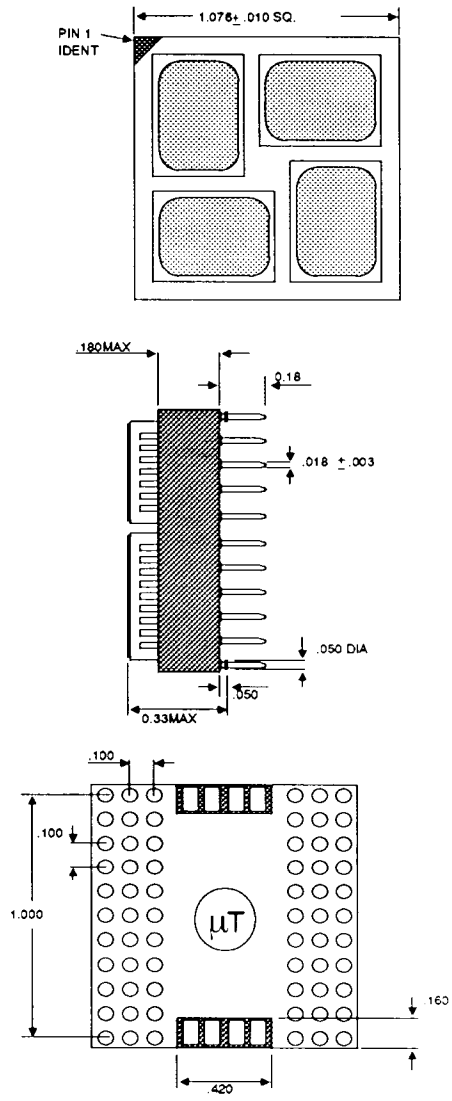
Features

- User selectable path width - 8,16 or 32 bits
- Single 5V power supply
- Very fast access times - 70/85/100ns
- Full military temperature range
- Battery back-up operation
- On-board decoupling capacitors
- BS9400 approval pending
- TTL compatible inputs and outputs

Block Diagram



Package Details
(Dimensions in inches)



Ordering Information

µT8 — S — 128 - 70

- µT8: Selectable word-width module
- S: Static RAM
- 128: 128k Bytes total capacity
- 70: Speed - 70ns / - 85ns / - 100ns

Mode Selection

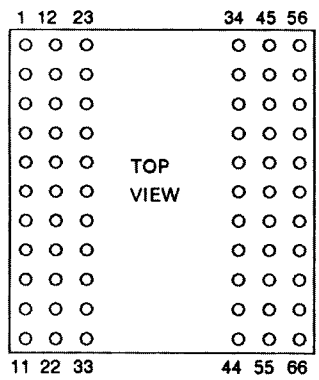
MODE	$\overline{CS}$	$\overline{OE}$	$\overline{WE}$	Vcc	OUTPUTS
Standby	V_{IH}	X	X	Vcc	High Z
Output Disable	V_{IL}	V_{IH}	V_{IH}	Vcc	High Z
Read	V_{IL}	V_{IL}	V_{IH}	Vcc	DOUT
Write	V_{IL}	X	V_{IL}	Vcc	DIN
Battery back-up	Vcc - 0.2	X	X	2.0V	High Z

Read Cycle (Over full temperature range)

(All units in ns)

Item	Symbol	-70		-85		-100	
		min.	max.	min.	max.	min.	max.
Read Cycle Time	t_{RC}	70	-	85	-	100	-
Address Access Time	t_{AA}	-	70	-	85	-	100
Chip Select Access Time	t_{ACS}	-	70	-	85	-	100
Output Enable to Output Valid	t_{OE}	-	35	-	45	-	50
Output Hold from Address Change	t_{OH}	5	-	5	-	10	-
Chip Selection to Output Low Z	t_{CLZ}	10	-	10	-	10	-
Output Enable to Output in Low Z	t_{CLZ}	5	-	5	-	5	-
Chip Deselection to Output in High Z	t_{CHZ}	0	30	0	30	0	35
Output Disable to Output in High Z	t_{OHZ}	0	30	0	30	0	35

Pin Assignments



Pin Names

A0 -A14	Address Inputs
D0-D31	Data
$\overline{CS}$1-4	Chip Selects
$\overline{OE}$	Output Enable
$\overline{WE}$1-4	Write Enables
VCC	Power (+5V)
GND	Ground

μT8 MODULE FLOW

LCC SRAM's released to BS9400 G0178

ASSEMBLE μT8S128

EXTERNAL VISUAL INSPECTION BS9450 1.2.10.2.2

HIGH TEMPERATURE STORAGE BS9400 1.2.6.3 150°C For 24 Hours

RAPID CHANGE OF TEMPERATURE BS9400 1.2.6.3 10 CYCLES -65°C TO +150°C

PRE BURN-IN ELECTRICAL TESTS

BURN-IN SCREEN BS9400 1.2.9.2 160 Hours min at 125°C

FINAL ELECTRICAL TESTS, AC,DC & Functional at 25°C

FINAL ELECTRICAL TESTS, AC,DC & Functional at Tmin/Tmax

SAMPLE MECHANICAL TESTS

Power Consumption (Operating average)

8 bit data - 80mA maximum	32 bit data - 280mA maximum
16 bit data - 145mA maximum	Standby mode - 8mA maximum

MICROTECH

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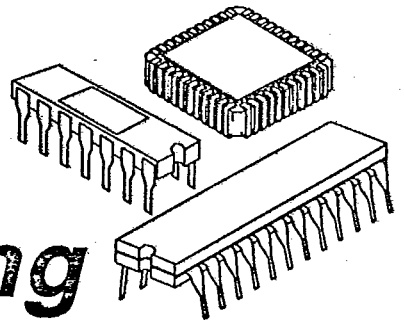

Custom Packaging

Microtech Semiconductors are leaders in Custom assembly, with our considerable expertise in production and test, we can satisfy the most complex requirement.

We are equipped to assemble virtually any hermetic package style - LCC, JLCC, Quad Pack, PGA & SOT 23 to name but a few. Using proven techniques, such as multichip or multicavity design, our expert engineering, with class 1000 cleanroom assembly and final test, assures quality results.

From commercial to space applications Microtech Semiconductors will support your assembly needs.

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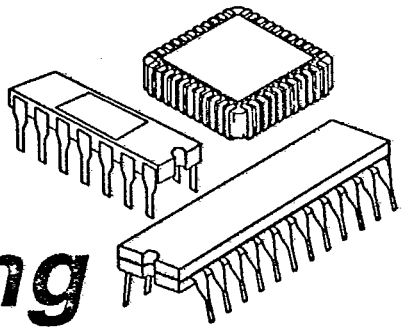

Custom Packaging

Approved to CECC and BS9000, hallmarks of European quality, MICROTECH Semiconductors is totally committed to total quality. Dedicated to supporting the high-reliability equipment manufacturer, our integrated circuits are produced in Class 1000 cleanroom conditions. Final optical inspection takes place under better than Class 100 laminar flow and our impressive range of environmental and electrical test equipment ensures that we release high performance components with no compromise on reliability.