

Features

- High speed
 - Maximum access times of 15, 20, 25, 30 and 35 ns
- Equal access and cycle times
- Fully static operation
 - No clock or refresh required
- Output Enable and two Chip Enable inputs for ease in application
- TTL compatible inputs and outputs
- Low Power Consumption
 - Standard Power (typ)
 - Active 400 mW
 - Standby 5 mW

Description

The V63C82 is a high speed, low power, 262,144-word by 4-bit CMOS static RAM fabricated using Vitelic's high performance CMOS technology. This high reliability process coupled with innovative circuit design techniques, yields access times as low as 15 ns maximum.

The \overline{OE} pin provides flexibility in system design. When \overline{OE} is high, the device assumes a low-power standby mode in which device power dissipation is reduced. Operation is from a single 5V ($\pm 10\%$) power supply.

The V63C82 is available in a space-saving 400 mil plastic SOJ. The pinout is compatible with the JEDEC standard 256K x 4 SRAMs.

Package	Symbol	Pin Count
Plastic DIP	S	28
Plastic SOJ	K	28

V	6	3	C	8	2			
Family		Device		Package	Speed	Power		

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Device Usage Chart

Operating Temperature Range	Package Outline		Access Time (ns)					Power		Temperature Mark
	S	K	15	20	25	30	35	Std.	Low	
0°C to 70°C	•	•	•	•	•	•	•	•	•	Blank

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DIP/SOJ
PIN CONFIGURATION
Top View

