

**VITELIC**

V63C83
128K x 8 BIT
FAST CMOS STATIC RAM

ADVANCED**Features**

- High speed
 - Maximum access times of 15, 20, 25, 30 and 35 ns
- Equal access and cycle times
- Fully static operation
 - No clock or refresh required
- Output Enable and two Chip Enable inputs for ease in application
- TTL compatible inputs and outputs
- Low Power Consumption
 - Standard Power (typ)
 - Active 800 mW
 - Standby 5 mW

Description

The V63C83 is a high speed, low power, 131,072-word by 8-bit CMOS static RAM fabricated using Vitellic's high performance CMOS technology. This high reliability process coupled with innovative circuit design techniques, yields access times as low as 15 ns maximum.

Two Chip Enable Inputs, \overline{CE}_1 and CE_2 provide flexibility in system design and make memory expansion easy. When \overline{CE}_1 is high, the device assumes a low-power standby mode in which device power dissipation is reduced. Operation is from a single 5V ($\pm 10\%$) power supply.

The V63C83 is available in a space-saving 400 mil plastic SOJ. The pinout is compatible with the JEDEC standard 128K x 8 SRAMs.

Package	Symbol	Pin Count
Plastic DIP	S	32
Plastic SOJ	K	32

V	6	3	C	8	3
Family		Device	Package	Speed	Power

6**Device Usage Chart**

Operating Temperature Range	Package Outline		Access Time (ns)					Power		Temperature Mark
	S	K	15	20	25	30	35	Std.	Low	
0°C to 70°C	•	•	•	•	•	•	•	•		Blank

V63C83 Rev. 00 6/90



DIP/SOJ
PIN CONFIGURATION
Top View

