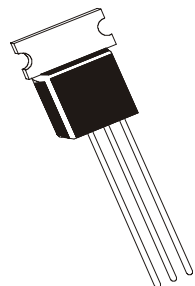


PNP SILICON EPITAXIAL TRANSISTOR
CIL9359
TO-237
BCE

PNP Vertical Retrace Switching Transistor
ABSOLUTE MAXIMUM RATINGS(Ta=25deg C)

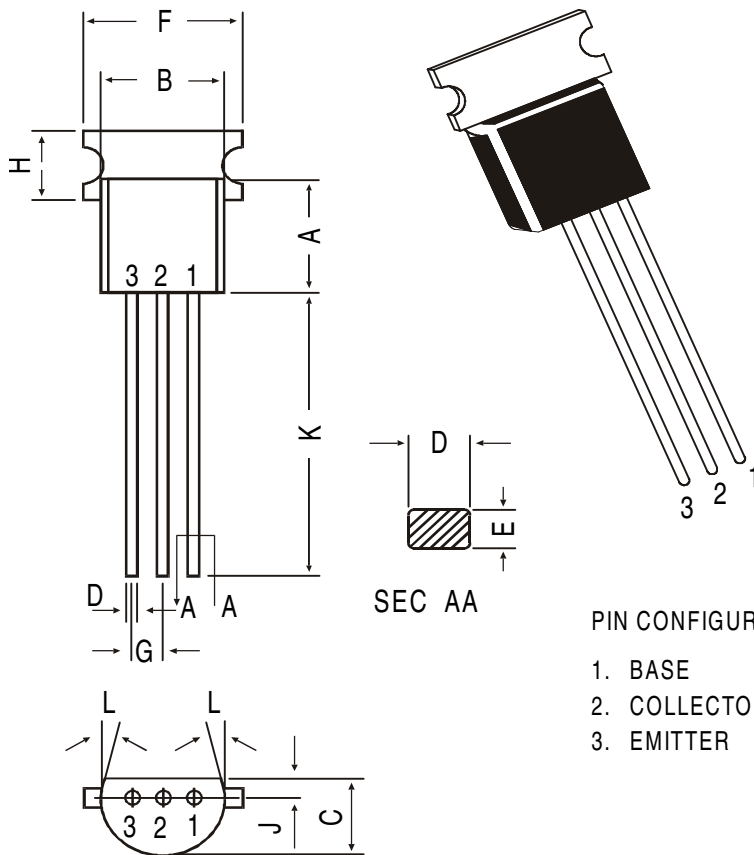
DESCRIPTION	SYMBOL	VALUE	UNIT
Collector -Base Voltage	VCBO	40	V
Collector -Emitter Voltage	VCEO	40	V
Emitter Base Voltage	VEBO	5.0	V
Collector Current (DC)	IC	2.0	A
Peak	IC	3.0	A
Power Dissipation	PD	900	mW
Junction Temperature Range	TJ	-65 to +150	deg C
Lead Soldering, 1/16" from body ,10sec	TL	280	deg C

ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Otherwise Specified)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector -Emitter Voltage	VCEO*	IC=10mA, IB=0	40	-	-	V
Emitter Base Voltage	VEBO	IE=10uA, IC=0	5.0	-	-	V
Collector Cut off Current	ICBO	VCB=40V, IE=0	-	-	50	nA
Emitter Cut off Current	IEBO	VEB=4V, IC=0	-	-	100	nA
DC Current Gain	hFE	IC=10mA,VCE=10V	100	-	400	
		IC=1.5A,VCE=10V*	40	-	-	
Collector Emitter Saturation Voltage	VCE(Sat) *	IC=1A,IB=100mA	-	-	1.0	V
Forward Emitter on Base Voltage	VEB(F) *	IC=1A, VCE=1V	-	-	1.2	V
Switching Time						
Turn on Time	ton	VCC=30V,VEB(off)= 3.8V, IC=500mA, IB1= 50mA	-	-	150	ns
Turn off Time	toff	VCC=30V, IC=500mA IB1=1B2=50mA	-	-	800	ns

***Pulse Test : Pulse Width =300us, Duty Cycle=2%**

TO-237 Plastic Package



All dimensions in mm.

DIM	MIN.	MAX.
A	4.32	5.33
B	4.45	5.20
C	3.18	4.19
D	0.41	0.55
E	0.35	0.50
F	—	5.40
G	1.14	1.40
H	—	2.54
K	12.70	—
L	5 DEG	
J	1.14	1.53

PIN CONFIGURATION

1. BASE
2. COLLECTOR
3. EMITTER

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-237 Bulk	1K/polybag	240 gm/1K pcs	3" x 7.5" x 7.5"	5.0K	17" x 15" x 13.5"	80.0K	26.2 kgs
TO-237 T&A	2K/ammo box	725 gm/2K pcs	12.5" x 8" x 1.8"	2.0K	17" x 15" x 13.5"	32.0K	13.8 kgs

Disclaimer

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