

DC/DC CONVERTERS
LOW-COST, 2:1 WIDE INPUT RANGE
FEATURES

- LOW-COST
- SMALL DIP PACKAGE SIZE
- FULL POWER TO +85°C
- EXTENDED TEMPERATURE RANGE:
-40°C TO +85°C
- INDUSTRY STANDARD PINOUTS

DESCRIPTION

The WP02R Series is a family of low-cost, high performance DC/DC converters that offer regulated outputs over a wide temperature range of -40°C to +85°C without any power derating. No external heatsink is required.

A self-oscillating design with isolated feedback is used to give stability over the wide input range and continuous short circuit protection. A rugged MOSPOWER transistor is used in a flyback topology to provide enhanced reliability.

For units with inputs of 15VDC or less no external components are required, although they are recommended for enhanced performance on both the input and outputs. For units of 24VDC or 48VDC inputs a capacitor of at least 10µF must be used across the input.

ABSOLUTE MAXIMUM RATINGS

Short Circuit Protection	Continuous
Internal Power Dissipation	1.5W
Lead Temperature (soldering 10seconds, max)	+300°C

APPLICATIONS

- TELECOMMUNICATION APPLICATIONS
- BATTERY POWERED SYSTEMS
- PORTABLE INSTRUMENTS
- PROCESS CONTROL EQUIPMENT
- TRANSPORTATION EQUIPMENT
- DISTRIBUTED POWER SYSTEMS

By not including these capacitors internally in the DC/DC converter, the unit cost is significantly reduced. It also allows the customers to incur the additional costs only when necessary. The capacitors to be chosen externally are usually physically larger and much less expensive than those mandated by internal design considerations. Because most customers add external decoupling capacitors, the total system cost is lower if duplication is eliminated by not including them internally. The customer specifies and pays only for his external needs.

The plastic package of the WP02R Series is rated UL94V-0. The plastic eliminates the layout precautions required by metal enclosed devices. The encapsulant material is rated UL94V-0 for flammability and offers excellent heat transfer characteristics.

ORDERING INFORMATION

WP02R		xx	yy	zz	E	/H
Device Family _____						
Indicates Wide Input Power 2 Watt Regulated Unit						
Model Number _____						
Selected from Table of Electrical Characteristics						
Where:						
xx = Input Voltage						
yy = Number of Outputs (Single "S", Dual "D")						
zz = Output Voltage						
Package Option _____						
Choose from E or L						
Screening Option _____						

ELECTRICAL SPECIFICATIONS

Specifications typical at $T_A = +25^{\circ}\text{C}$, nominal input voltage, rated output current unless otherwise specified.

MODEL	NOMINAL INPUT VOLTAGE (VDC)	RATED OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT		INPUT CURRENT		EFFICIENCY (%)
			MIN LOAD (mA)	RATED LOAD (mA)	MIN LOAD (mA)	RATED LOAD (mA)	
WP02R05S05 WP02R05D05 WP02R05D15	5 5 5	5 ± 5 ± 15	100 ± 50 ± 20	400 ± 200 ± 67	150 150 195	615 580 570	65 69 70
WP02R12S05 WP02R12D05 WP02R12D12 WP02R12D15	12 12 12 12	5 ± 5 ± 12 ± 15	100 ± 50 ± 21 ± 20	400 ± 200 ± 83 ± 67	65 65 85 85	230 225 220 220	73 75 76 76
WP02R15S05 WP02R15D05 WP02R15D15	15 15 15	5 ± 5 ± 15	100 ± 50 ± 20	400 ± 200 ± 67	50 50 65	180 175 175	75 76 77
WP02R24S05 WP02R24D05 WP02R24D15	24 24 24	5 ± 5 ± 15	100 ± 50 ± 20	400 ± 200 ± 67	40 40 40	120 120 120	69 69 70
WP02R48S05 WP02R48D05 WP02R48D15	48 48 48	5 ± 5 ± 15	100 ± 50 ± 20	400 ± 200 ± 67	20 20 20	60 60 60	69 69 70

NOTE: Other input to output voltages may be available. Please consult factory.

COMMON SPECIFICATIONS

Specifications typical at $T_A = +25^{\circ}\text{C}$, nominal input voltage, rated output current unless otherwise specified.

Parameter	Conditions	Min	Typ	Max	Units
INPUT					
Voltage Range		4 7 10 18 36	5 12 15 24 48	8 15 20 36 72	VDC VDC VDC VDC VDC
Reflected Ripple Current	5, 12, 15VDC Input Models With 100 μF cap. across input Without 100 μF cap. across input		130 200	200 300	mAp-p mAp-p
Reflected Ripple Current	24, 48VDC Input Models With 10 μF cap. across input		130	200	mAp-p
ISOLATION					
Rated Voltage		500			VDC
Test Voltage	60 Hz, 10 Seconds	500			VpR
Resistance			10		G Ω
Capacitance			80		pF
Leakage Current	$V_{\text{iso}} = 240\text{VAC}$, 60Hz		30		μA_{rms}
OUTPUT					
Rated Power			2		W
Voltage Setpoint Accuracy			± 3	± 5	%
Temperature Coefficient			± 0.02		%/ $^{\circ}\text{C}$
Line Regulation	Low Line to High Line			0.5	%
Load Regulation					
(Single Output Models)	Min Load to Rated Load			1.0	%
(Positive Output, Duals)	Min Load to Rated Load			1.0	%
(Negative Output, Duals)	Min Load to Rated Load			1.5	%
Ripple & Noise	With 100 μF cap. across output BW = DC to 100 MHz BW = 20 Hz to 300 KHz		100 5	150 10	mVp-p mVrms
Ripple & Noise	Without 100 μF cap. across output BW = DC to 10 MHz BW = 20 Hz to 300 KHz		1.7 60	3.5 175	Vp-p mVrms
GENERAL					
Switching Frequency			200		kHz
MTTF per MIL-HDBK-217, Rev. E*	Circuit Stress Method $T_A = +25^{\circ}\text{C}$ $T_A = +85^{\circ}\text{C}$		650 155		kHr kHr
Package Weight			12		g
TEMPERATURE					
Specification		-40		+85	$^{\circ}\text{C}$
Operation		-40		+100	$^{\circ}\text{C}$
Storage		-55		+110	$^{\circ}\text{C}$

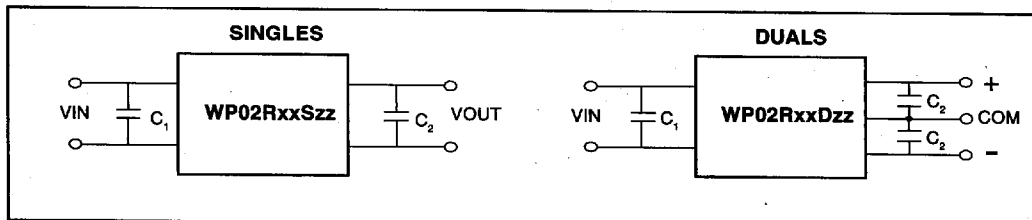
* For demonstrated MTTF results reference Power Convertibles' Reliability Report WP02R

APPLICATION NOTES

For ease of design, performance data has been included with and without the optional external capacitors. For models with 5, 12, or 15VDC inputs, performance is described with and without any external capacitors. For models with 24 and

48VDC inputs, performance is described with and without external **output** capacitors. See also Power Convertibles' application note on Noise and Filtering of DC/DC Converters, AN162.

RECOMMENDED EXTERNAL COMPONENTS



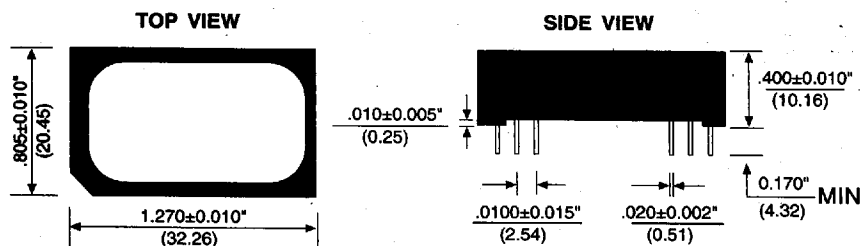
C₁: For 5, 12, 15VDC Input Models = Sprague 515D107M025AA6A, 100μF 25V

For 24, 48VDC Input Models = Sprague 515D106M100AA6A, 10μF 100V

C₂: Sprague 515D107M025AA6A, 100μF 25V

MECHANICAL

Package/Pinout "E"



MATERIAL: Units are encapsulated in Iso-ThermoFlex™, a low thermal resistance molding compound which has excellent chemical resistance, wide operating temperature range, and good electrical properties even in high humidity environments. The encapsulant and outer shell of the unit have UL94V-0 ratings. Lead material is brass with a solder plated surface to allow ease of solderability.

PIN CONNECTIONS

PIN #	SINGLES	DUALS
1	+VIN	+VIN
2	NC	-VOUT
3	NC	Common
10	-VOUT	Common
11	+VOUT	+VOUT
12	-VIN	-VIN
13	-VIN	-VIN
14	+VOUT	+VOUT
15	-VOUT	Common
22	NC	Common
23	NC	-VOUT
24	+VIN	+VIN

NC = No Internal Connection.

Duplicate pin functions are internally connected.

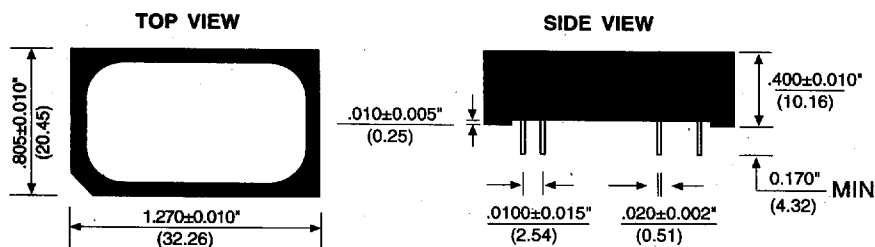
All dimensions are in inches (millimeters).

GRID: 0.100 inches (2.54 millimeters)

PIN PLACEMENT TOLERANCE: ± 0.015"

Product Marked with: specific model ordered, date code, and job code.

Package/Pinout "L"



MATERIAL: Units are encapsulated in Iso-ThermoFlex™, a low thermal resistance molding compound which has excellent chemical resistance, wide operating temperature range, and good electrical properties even in high humidity environments. The encapsulant and outer shell of the unit have UL94V-0 ratings. Lead material is brass with a solder plated surface to allow ease of solderability.

PIN #	SINGLES	DUALS
2	-VIN	-VIN
3	-VIN	-VIN
9	NU	Common
11	NC	-VOUT
14	+VOUT	+VOUT
16	-VOUT	Common
22	+VIN	+VIN
23	+VIN	+VIN

NU = Do not use.

NC = No Internal Connection.

Duplicate pin functions are internally connected.

All dimensions are in inches (millimeters).

GRID: 0.100 inches (2.54 millimeters)

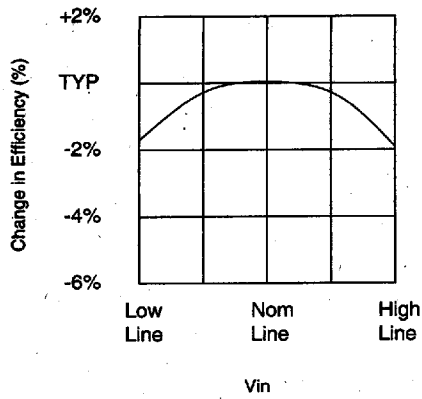
PIN PLACEMENT TOLERANCE: ± 0.015"

Product Marked with: specific model ordered, date code, and job code.

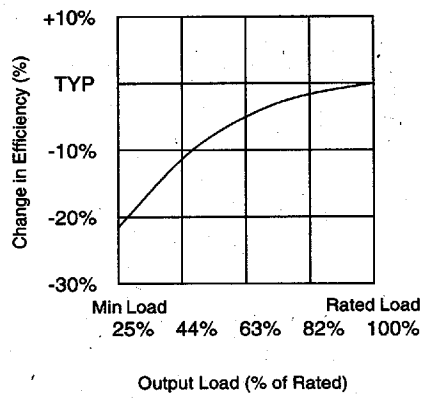
TYPICAL PERFORMANCE CURVES

$T_A = +25^\circ\text{C}$, nominal input voltage, rated load, recommended external components applied, unless otherwise specified.

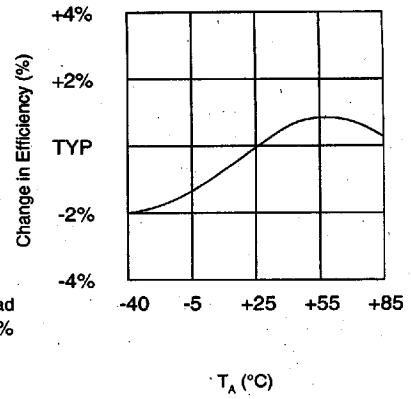
EFFICIENCY vs INPUT VOLTAGE



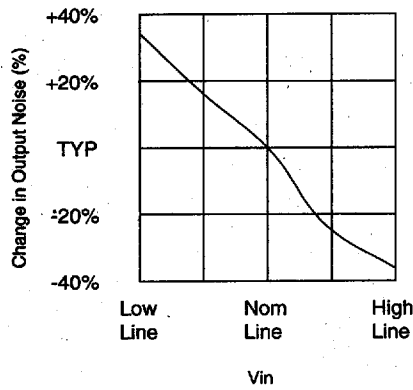
EFFICIENCY vs OUTPUT LOAD



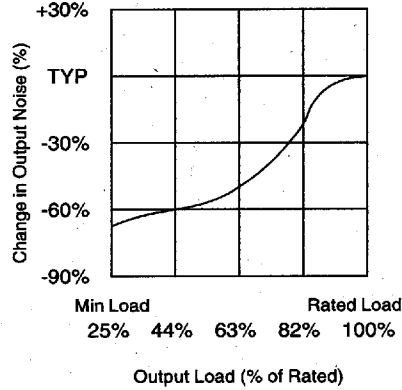
EFFICIENCY vs TEMPERATURE



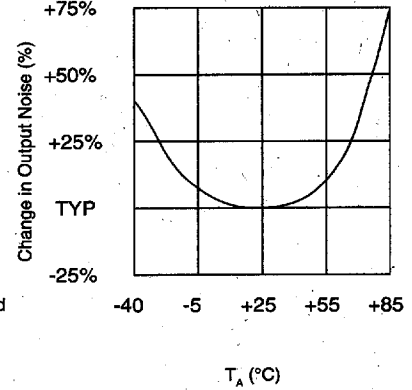
OUTPUT NOISE vs INPUT VOLTAGE



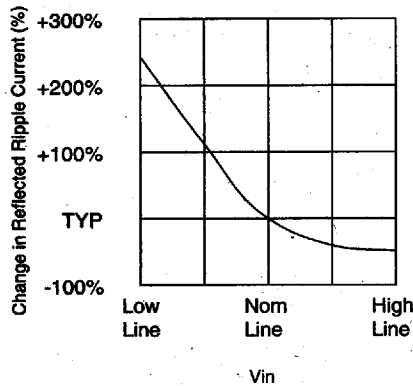
OUTPUT NOISE vs OUTPUT LOAD



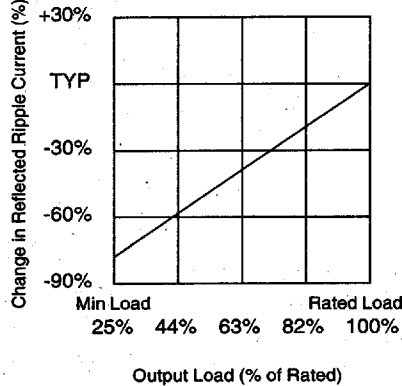
OUTPUT NOISE vs TEMPERATURE



REFLECTED RIPPLE CURRENT vs INPUT VOLTAGE



REFLECTED RIPPLE CURRENT vs OUTPUT LOAD



REFLECTED RIPPLE CURRENT vs TEMPERATURE

