

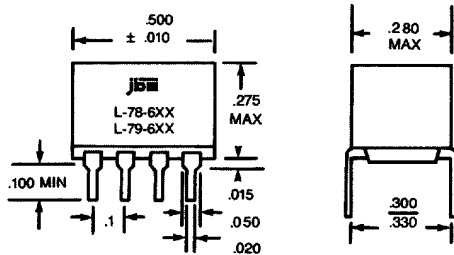
T-47-13

L-78-600 SERIES (8-pin)

Mini Single

L-79-600 SERIES (8-pin)

Mini 3 DLS

**INPUT CONDITIONS**

(See Page 2)

Operating Temp. 0°C to +70°C

Storage Temp. -55°C to +125°C

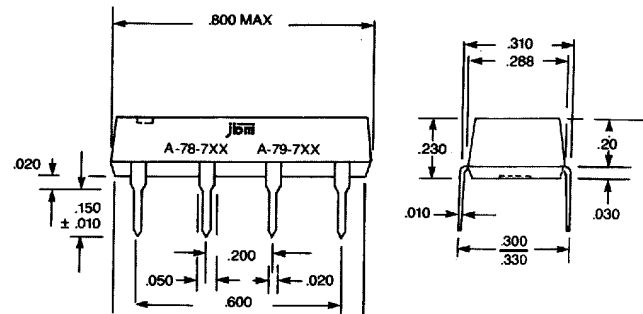
(All lines internally terminated)

 I_{IH} Logic "1" Input Current . . . 50 μ A Max. I_{IL} Logic "0" Input Current . . . 2mA Max. V_{OH} Logic "1" Voltage Out . . . 2.7V Min. V_{OL} Logic "0" Voltage Out . . . 0.5V Max.**A-78-700 SERIES (14-pin)**

Single

A-79-700 SERIES (14-pin)

3 DLS

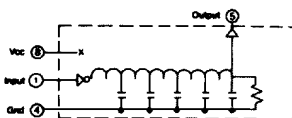
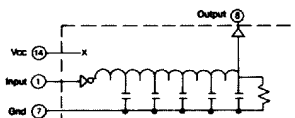
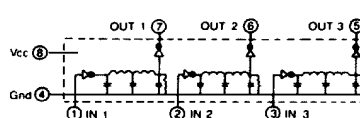
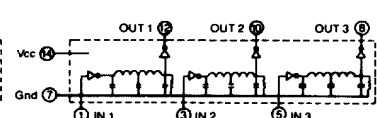
**DRIVE CAPABILITIES**

(TTL LOADS)

Logic "0" 10 Loads/Tap Max.
20 Loads/Unit Max.

Logic "1" 20 Loads/Unit Max.

Voltage Range: 4.75V to 5.25V

**8 PIN
L-78-6XX****14 PIN
A-78-7XX****8 PIN
L-79-6XX****14 PIN
A-79-7XX****SINGLE OUTPUT DELAY LINES**

8 PIN Part Number	14 PIN Part Number	TOTAL (ns) Delay Time
L-78-605	A-78-705	5.0 ± 1 ns
L-78-610	A-78-710	10.0 ± 2 ns
L-78-615	A-78-715	15.0 ± 2 ns
L-78-620	A-78-720	20.0 ± 2 ns
L-78-625	A-78-725	25.0 ± 2 ns
L-78-630	A-78-730	30.0 ± 2
L-78-640	A-78-740	40.0 ± 2
L-78-650	A-78-750	50.0 ± 2.5
L-78-652	A-78-752	60.0 ± 3
L-78-656	A-78-756	75.0 ± 4
L-78-659	A-78-759	100 ± 5
L-78-661	A-78-761	150 ± 7.5
L-78-663	A-78-763	200 ± 10
L-78-665	A-78-765	250 ± 12.5
L-78-666	A-78-766	300 ± 15
L-78-668	A-78-768	400 ± 20
L-78-670	A-78-770	500 ± 25

**3 INDEPENDENT
DELAY LINES**

8 PIN Part Number	14 PIN Part Number	Total (ns) Delay
L-79-605	A-79-705	5.0 ± 1
L-79-606	A-79-706	6.0 ± 1
L-79-607	A-79-707	7.0 ± 1.5
L-79-608	A-79-708	8.0 ± 1.5
L-79-609	A-79-709	9.0 ± 2
L-79-610	A-79-710	10.0 ± 2
L-79-615	A-79-715	15.0 ± 2
L-79-620	A-79-720	20.0 ± 2
L-79-625	A-79-725	25.0 ± 2.5
L-79-630	A-79-730	30.0 ± 3
L-79-640	A-79-740	40.0 ± 3
L-79-650	A-79-750	50.0 ± 3
—	A-79-752	60.0 ± 3
—	A-79-756	70.0 ± 3.5
—	A-79-760	75.0 ± 4
—	A-79-766	100 ± 5