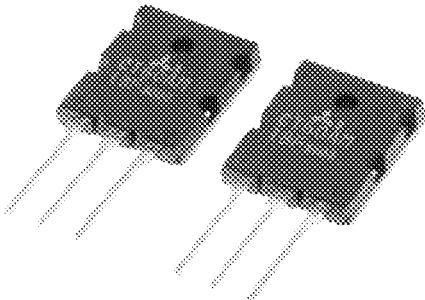


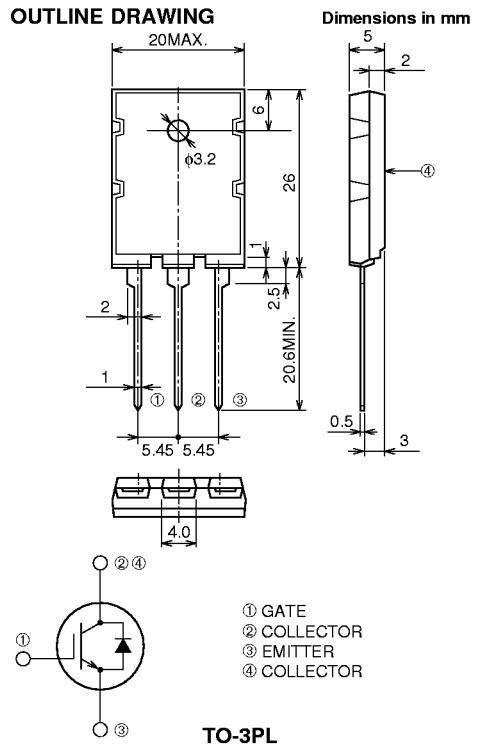
**PRELIMINARY**  
Notice: This is not a final specification.  
Some parametric limits are subject to change.

**CT60AM-18F**

INSULATED GATE BIPOLAR TRANSISTOR

**CT60AM-18F**

- V<sub>CES</sub> ..... 900V
- I<sub>C</sub> ..... 60A
- Simple drive
- Integrated Fast-recovery diode
- Small tail loss
- Low V<sub>CE</sub> Saturation Voltage

**OUTLINE DRAWING****APPLICATION**

Microwave oven, Electromagnetic cooking devices,  
Rice-cookers, Voltage-resonant inverter circuit  
electric appliances

**MAXIMUM RATINGS** ( $T_c = 25^\circ\text{C}$ )

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	$V_{GE} = 0\text{V}$	900	V
V <sub>GES</sub>	Gate-Emitter Voltage		$\pm 25$	V
V <sub>GEM</sub>	Peak Gate-Emitter Voltage		$\pm 30$	V
I <sub>C</sub>	Collector Current		60	A
I <sub>CM</sub>	Collector Current (Pulse)		120	A
I <sub>E</sub>	Emitter Current		40	A
P <sub>C</sub>	Maximum Power Dissipation		180	W
T <sub>j</sub>	Junction Temperature		-40 ~ +150	$^\circ\text{C}$
T <sub>stg</sub>	Storage Temperature		-40 ~ +150	$^\circ\text{C}$

Sep.1998

**PRELIMINARY**  
Notice: This is not a final specification.  
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ELECTRICAL CHARACTERISTICS ( $T_{ch} = 25^\circ C$ )

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$I_{CES}$	Collector cutoff current	$V_{CE} = 900V, V_{GE} = 0V$	—	—	1.0	mA
$I_{GES}$	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	—	—	0.5	$\mu A$
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_C = 6mA$	2.0	4.0	6.0	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$I_C = 60A, V_{CE} = 15V$	—	2.1	2.7	V
$C_{ies}$	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	—	4400	—	pF
$C_{oes}$	Output capacitance		—	115	—	pF
$C_{res}$	Reverse transfer capacitance		—	75	—	pF
$t_{d(on)}$	Turn-on deray time	$V_{CC} = 300V, I_C = 60A, V_{GE} = 15V, R_G = 10\Omega$	—	0.05	—	$\mu s$
$t_r$	Turn-on rise time		—	0.1	—	$\mu s$
$t_{d(off)}$	Turn-off delay time		—	0.2	—	$\mu s$
$t_f$	Turn-off fall time		—	0.2	—	$\mu s$
$E_{tail}$	Tail loss	$I_{CP} = 60A, T_j = 125^\circ C, dv/dt = 200V/\mu s$	—	0.6	1.0	mJ/pls
$I_{tail}$	Tail current		—	8	12	A
$V_{EC}$	Emitter-collector voltage	$I_E = 60A, V_{GE} = 0V$	—	2.2	3.0	V
$t_{rr}$	Diode reverse recovery time	$I_E = 60A, dI/dt = -20A/\mu s$	—	0.5	2.0	$\mu s$
$R_{th(j-c)}$	Thermal resistance (IGBT)	Junction to case	—	—	0.69	$^\circ C/W$
$R_{th(j-c)}$	Thermal resistance (Diode)	Junction to case	—	—	4.0	$^\circ C/W$