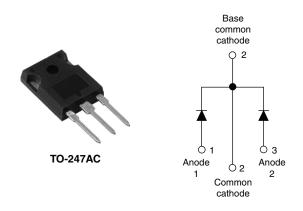
Vishay Semiconductors

Hyperfast Rectifier, 2 x 40 A FRED Pt®



PRODUCT SUMMARY					
Package	TO-247AC				
I _{F(AV)}	2 x 40 A				
V _R	200 V				
V _F at I _F	1.02 V				
t _{rr} (typ.)	34 ns				
T _J max.	175 °C				
Diode variation	Common cathode				

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTIONS/APPLICATIONS

VS-80CPU02-F3 series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of welding, SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Repetitive peak reverse voltage	V _{RRM}		200	V	
Average rectified forward currentper leg		T _C = 145 °C	40		
total device	IF(AV)	1 _C = 143 0	80	А	
Non-repetitive peak surge current per leg	I _{FSM}	T _J = 25 °C	330		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
		I _F = 40 A	-	0.94	1.02		
	V	I _F = 40 A, T _J = 150 °C	-	0.80	0.90	V	
Forward voltage	V _F	I _F = 80 A	-	1.07	1.20		
		I _F = 80 A, T _J = 150 °C	-	0.97	1.08		
		$V_{R} = V_{R}$ rated	-	-	5		
Reverse leakage current	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA	
Junction capacitance	CT	V _R = 200 V	-	120	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	3.5	-	nH	



COMPLIANT

VS-80CPU02-F3

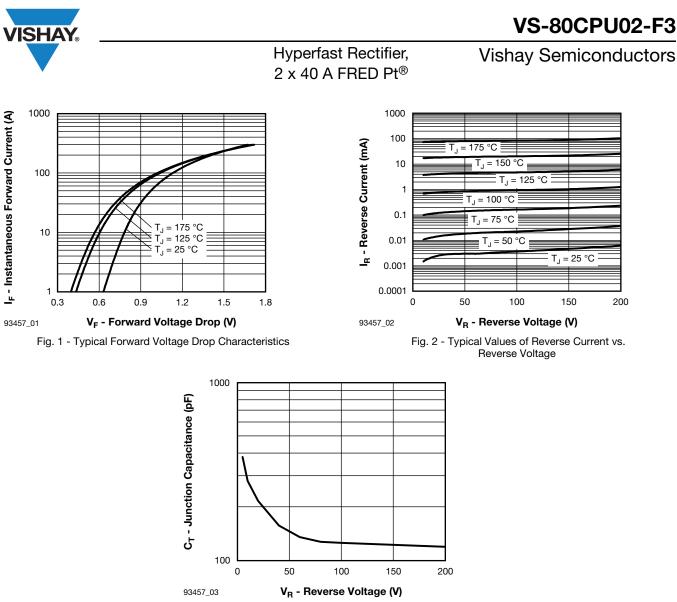
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 1.0 \text{ A}$	100 A/µs, V _R = 30 V	-	34	-		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	33	-	ns A	
		T _J = 125 °C	I _F = 40 A dI _F /dt = - 200 A/μs V _B = 200 V	-	54	-		
Deals receivers ourrent	I _{RRM}	T _J = 25 °C		-	3.4	-		
Peak recovery current		T _J = 125 °C		-	8	-		
	T _J = 25 °C		-	56	-	nC		
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	216	-	10	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	0.46	0.70		
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.3	-		
Weight			-	6.0	-	g	
weight			-	0.21	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-247AC		80CI	PU02		





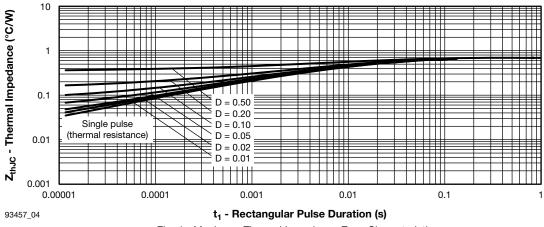
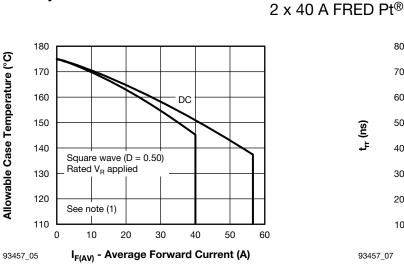
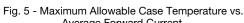


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

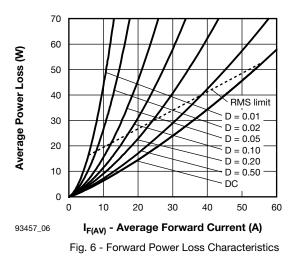
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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

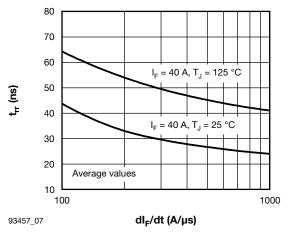


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

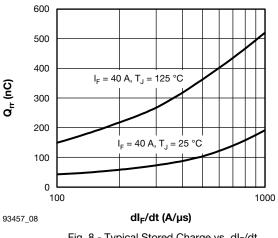


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Hyperfast Rectifier,

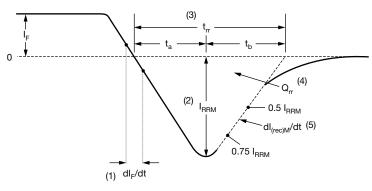


Hyperfast Rectifier, 2 x 40 A FRED $Pt^{\mathbb{R}}$

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$V_{R} = 200 V$ $L = 70 \mu H$ D.U.T. dI_{F}/dt adjust G IRFP250S

Fig. 9 - Reverse Recovery Parameter Test Circuit



(1) dl_F/dt - rate of change of current through zero crossing

(4) $\rm Q_{rr}$ - area under curve defined by $\rm t_{rr}$ and $\rm I_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(2) ${\rm I}_{\rm RRM}$ - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

-80CPI 102-E3 VS

VS-80CPU02-F3								
Vishay Semiconducto	ors		•••	ast Re A FRE				
ORDERING INFORMATION	TABLE							
Device code	VS-	80	с	Р	U	02	-F3	
	1 2 3 4 5 6 7	- Cur - Circ - C = - P = - U = - Vol	TO-247 Ultrafa tage rat	st rectifie ing (02 = ntal digit	= 80 A) n: ode er = 200 V)	7)	

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-80CPU02-F3	25	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95223					
Part marking	www.vishay.com/doc?95007				

SHA



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