FEATURES

- •2.488 Gbps small Factor transceiver
- •RJ-45 style LCTM connector system
- •850 nm Vertical Cavity Surface Emitting Laser (VCSEL) Source Technology
- •Data Link up to 300 Meters min in 50/125 MMF and 270 Meters min in 62.5/270 MMF
- •Single + 3.3V Power Supply and PECL Logic Interface
- •TTL Signal detection output
- •Class 1 FDA and IEC laser safety compliant (Laser Class 1 Product)

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Reference
Storage temperature	Ts	-40		85	¢ J	
Lead soldering temperature	T _{SOLD}			260	¢ J	
Lead soldering time	t _{SOID}			10	sec.	
Supply voltage	Vcc	0		6	V	

Recommended Operating Conditions:

Parameter	Symbol	Min.	Тур.	Max.	Unit	Reference
Ambient Operating Temperature	$T_{\mathbf{A}}$	0		70	¢ J	
Supply voltage	Vcc	3.1		3.5	V	
Transmitter Differential Input Voltage	$V_{\mathbf{D}}$	0.3		1.6	V	
Data Output Load	R _D		50		£ [

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Transmitter Electro-Optical Performance Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Unit	Reference
Supply current	Icc			140	mA	
Launched power(avg.)	P_{O}	-10		-4	dBm	
Optical extinction ratio		8			dB	
Center wavelength	λс	830	850	860	nm	
Spectral width(RMS)	σ			0.85	nm rms	
Optical risetime	$t_{\rm r}$			0.175	ns	Note(1)
Optical falltime	t_{f}			0.175	ns	Note(1)
Relative Intensity Noise	RIN			-117	DB/Hz	

Note(1). These are unfiltered 20-80% values.

Receiver Electro-Optical Performance Specifications:

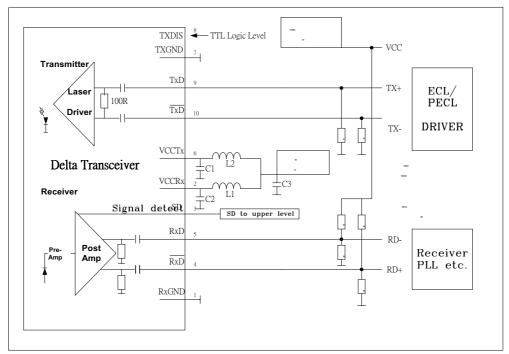
Parameter	Symbol	Min.	Typ.	Max.	Unit	Reference
Supply current	Icc			130	mA	
Data output differential voltage	V_{D}	0.5	0.7	1.23	V	
Optical input sensitivity(avg.)	P_{IN}			-17	dBm	Note(1)
Optical input saturation(avg.)	P _{SAT}	-3			dBm	Note(1)
Optical wavelength	λ		850		nm	
Signal detect- Assert	P_A			-17	dBm	
Signal detect- Deassert	P_D	-30			dBm	
Signal detect- Hysteresis	$P_A - P_D$	0.5			dB	

Note(1). With BER better than or equal to 1x10⁻¹², measured in the center of the eye opening with 2²³-1 NRZ PRBS

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Recommended Circuit Schematic

Small Form Factor Multimode 850nm Fibre Channel 2x5 Transceiver, 3.3V Transceiver Version



Input Level (LV-TTL)	TX Function
Low	ON
High	OFF
NIC	ON

Table 1 (Disable Pin Truth Table)

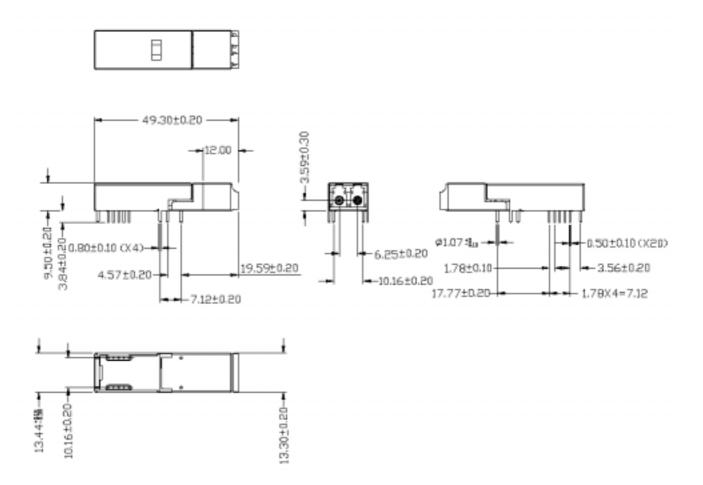
C1/2/3 = 4.7 uFL1/2 = 1 uH

Value of R5/6/9/10/11/12 may very as long as proper 50 $\,\Omega$ termination to VEE. For good EMI performance the power supplyfilter is require. Use short tracks from the inductor L1/2 to the module VccTx/VccRx . This recommend Circuit assume Gigabit Ethernet / Fiber Channel using 3.3V power supply.

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Mechanical Dimensions

Unit: mm



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Pin Assignments

Pin	FUNCTION	LOGIC FAMILY
1	RX GOUND	0 Vdc
2	RX VCC	3.3 Vdc
3	SD(RX SIGNLE DETECT)	LV-TTL
4	RX-	LV-PECL
5	RX+	LV-PECL
6	TX VCC	3.3 Vdc
7	TX GND	0 Vdc
8	TX DISABLE	Note (1)
9	TX+	LV-PECL
10	TX-	LV-PECL

•NOTE (1):

Input Level (LV-TTL)	TX Function
Low	ON
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Regulatory Compliance

Test Item	Reference	Qty'	Evaluation
(#1) Electromagnetic Interference EMC	FCC Class B EN 55022 Class B CISPR 22	5	
(#2) Immunity : Radio Frequency Electromagnetic Field	EN 61000-4-3 IEC 1000-4-3	5	
(#3) Immunity : Electrostatic Discharge to the Duplex SC Receptacle	EN 61000-4-2 IEC 1000-4-2 IEC 801.2	5	(1) Satisfied with electrical characteristics of product
	MIL-STD-883C Method 3015.4 EIAJ#1988.3.2B Version 2, Machine model	5	spec. (2) No physical damage

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