



±15kV ESD-Protected, Slew-Rate-Limited, Low-Power, RS-485/RS-422 Transceivers

General Description

The MAX481E, MAX483E, MAX485E, MAX487E-MAX491E, and MAX1487E are low-power transceivers for RS-485 and RS-422 communications in harsh environments. Each driver output and receiver input is protected against ±15kV electrostatic discharge (ESD) shocks, without latchup. These parts contain one driver and one receiver. The MAX483E, MAX487E, MAX488E, and MAX489E feature reduced slew-rate drivers that minimize EMI and reduce reflections caused by improperly terminated cables, thus allowing error-free data transmission up to 250kbps. The driver slew rates of the MAX481E, MAX485E, MAX490E, MAX491E, and MAX1487E are not limited, allowing them to transmit up to 2.5Mbps.

These transceivers draw as little as 120µA supply current when unloaded or when fully loaded with disabled drivers (see *Selection Table*). Additionally, the MAX481E, MAX483E, and MAX487E have a low-current shutdown mode in which they consume only 0.1µA. All parts operate from a single +5V supply.

Drivers are short-circuit current limited, and are protected against excessive power dissipation by thermal shutdown circuitry that places their outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a logic-high output if the input is open circuit.

The MAX487E and MAX1487E feature quarter-unit-load receiver input impedance, allowing up to 128 transceivers on the bus. The MAX488E-MAX491E are designed for full-duplex communications, while the MAX481E, MAX483E, MAX485E, MAX487E, and MAX1487E are designed for half-duplex applications. For applications in a less demanding environment, see the pin- and function-compatible MAX481, MAX483, MAX485, MAX487-MAX491, and MAX1487.

Applications

Low-Power RS-485 Transceivers
Low-Power RS-422 Transceivers
Level Translators
Transceivers for EMI-Sensitive Applications
Industrial-Control Local Area Networks

Features

- ♦ ESD Protection: ±15kV—Human Body Model
- ♦ Available in µMAX Package (the smallest 8-pin SO)
- ♦ Slew-Rate Limited for Error-Free Data Transmission (MAX483E/487E/488E/489E)
- ♦ Low Quiescent Current:
 - 120µA (MAX483E/487E/488E/489E)
 - 230µA (MAX1487E)
 - 300µA (MAX481E/485E/490E/491E)
- ♦ -7V to +12V Common-Mode Input Voltage Range
- ♦ Three-State Outputs
- ♦ 30ns Propagation Delays, 5ns Skew (MAX481E/485E/490E/491E/1487E)
- ♦ Full-Duplex and Half-Duplex Versions Available
- ♦ Allows up to 128 Transceivers on the Bus (MAX487E/MAX1487E)
- ♦ Current Limiting and Thermal Shutdown for Driver Overload Protection

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX481ECPA	0°C to +70°C	8 Plastic DIP
MAX481ECSA	0°C to +70°C	8 SO
MAX481ECUA	0°C to +70°C	8 µMAX
MAX481EEPA	-40°C to +85°C	8 Plastic DIP
MAX481EESA	-40°C to +85°C	8 SO

Ordering Information continued on last page.

Selection Table

PART NUMBER	HALF/FULL DUPLEX	DATA RATE (Mbps)	SLEW-RATE LIMITED	LOW-POWER SHUTDOWN	RECEIVER/ DRIVER ENABLE	QUIESCENT CURRENT (µA)	NUMBER OF TRANSMITTERS ON BUS	PIN COUNT
MAX481E	Half	2.5	No	Yes	Yes	300	32	8
MAX483E	Half	0.25	Yes	Yes	Yes	120	32	8
MAX485E	Half	2.5	No	No	Yes	300	32	8
MAX487E	Half	0.25	Yes	Yes	Yes	120	128	8
MAX488E	Full	0.25	Yes	No	No	120	32	8
MAX489E	Full	0.25	Yes	No	Yes	120	32	14
MAX490E	Full	2.5	No	No	No	300	32	8
MAX491E	Full	2.5	No	No	Yes	300	32	14
MAX1487E	Half	2.5	No	No	Yes	230	128	8

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MAX481E/MAX483E/MAX485E/MAX487E-MAX491E/MAX1487E

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ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V _{CC})	12V
Control Input Voltage (RE, DE)	-0.5V to (V _{CC} + 0.5V)
Driver Input Voltage (DI)	-0.5V to (V _{CC} + 0.5V)
Driver Output Voltage (A, B)	-8V to +12.5V
Receiver Input Voltage (A, B)	-8V to +12.5V
Receiver Output Voltage (RO)	-0.5V to (V _{CC} + 0.5V)
Continuous Power Dissipation (T _A = +70°C)	
8-Pin Plastic DIP (derate 9.09mW/°C above +70°C)	727mW
14-Pin Plastic DIP (derate 10.00mW/°C above +70°C)	800mW

8-Pin SO (derate 5.88mW/°C above +70°C)	471mW
14-Pin SO (derate 8.33mW/°C above +70°C)	667mW
8-Pin µMAX (derate 4.1mW/°C above +70°C)	330mW
Operating Temperature Ranges	
MAX4_ _C_ _/MAX1487EC_ A	0°C to +70°C
MAX4_ _E_ _/MAX1487EE_ A	-40°C to +85°C
Storage Temperature Range	-65°C to +160°C
Lead Temperature (soldering, 10sec)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

(V_{CC} = 5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Differential Driver Output (no load)	V _{OD1}				5	V
Differential Driver Output (with load)	V _{OD2}	R = 50Ω (RS-422)	2			V
		R = 27Ω (RS-485), Figure 8	1.5		5	
Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	ΔV _{OD}	R = 27Ω or 50Ω, Figure 8			0.2	V
Driver Common-Mode Output Voltage	V _{OC}	R = 27Ω or 50Ω, Figure 8			3	V
Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States	ΔV _{OC}	R = 27Ω or 50Ω, Figure 8			0.2	V
Input High Voltage	V _{IH}	DE, DI, RE	2.0			V
Input Low Voltage	V _{IL}	DE, DI, RE			0.8	V
Input Current	I _{IN1}	DE, DI, RE			±2	µA
Input Current (A, B)	I _{IN2}	DE = 0V; V _{CC} = 0V or 5.25V, all devices except MAX487E/MAX1487E	V _{IN} = +12V		1.0	mA
			V _{IN} = -7V		-0.8	
		MAX487E/MAX1487E, DE = 0V, V _{CC} = 0V or 5.25V	V _{IN} = +12V		0.25	mA
			V _{IN} = -7V		-0.2	
Receiver Differential Threshold Voltage	V _{TH}	-7V ≤ V _{CM} ≤ 12V	-0.2		0.2	V
Receiver Input Hysteresis	ΔV _{TH}	V _{CM} = 0V		70		mV
Receiver Output High Voltage	V _{OH}	I _O = -4mA, V _{ID} = 200mV	3.5			V
Receiver Output Low Voltage	V _{OL}	I _O = 4mA, V _{ID} = -200mV			0.4	V
Three-State (high impedance) Output Current at Receiver	I _{OZR}	0.4V ≤ V _O ≤ 2.4V			±1	µA
Receiver Input Resistance	R _{IN}	-7V ≤ V _{CM} ≤ 12V, all devices except MAX487E/MAX1487E	12			kΩ
		-7V ≤ V _{CM} ≤ 12V, MAX487E/MAX1487E	48			kΩ

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DC ELECTRICAL CHARACTERISTICS (continued)

(V_{CC} = 5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
No-Load Supply Current (Note 3)	I _{CC}	MAX488E/MAX489E, DE, DI, RE = 0V or V _{CC}		120	250	μA
		MAX490E/MAX491E, DE, DI, RE = 0V or V _{CC}		300	500	
		MAX481E/MAX485E, RE = 0V or V _{CC}	DE = V _{CC}	500	900	
			DE = 0V	300	500	
		MAX1487E, RE = 0V or V _{CC}	DE = V _{CC}	300	500	
			DE = 0V	230	400	
		MAX483E/MAX487E, RE = 0V or V _{CC}	DE = V _{CC}	350	650	
			MAX483E	250	400	
			DE = 0V	120	250	
Supply Current in Shutdown	I _{SHDN}	MAX481E/483E/487E, DE = 0V, RE = V _{CC}		0.1	10	μA
Driver Short-Circuit Current, V _O = High	I _{OSD1}	-7V ≤ V _O ≤ 12V (Note 4)	35		250	mA
Driver Short-Circuit Current, V _O = Low	I _{OSD2}	-7V ≤ V _O ≤ 12V (Note 4)	35		250	mA
Receiver Short-Circuit Current	I _{OSR}	0V ≤ V _O ≤ V _{CC}	7		95	mA
ESD Protection		A, B, Y and Z pins, tested using Human Body Model		±15		kV

SWITCHING CHARACTERISTICS—MAX481E/MAX485E, MAX490E/MAX491E, MAX1487E

(V_{CC} = 5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Driver Input to Output	tPLH	Figures 10 and 12, R _{DIFF} = 54Ω, CL1 = CL2 = 100pF	10		60	ns	
	tPHL		10		60		
Driver Output Skew to Output	tSKEW	Figures 10 and 12, R _{DIFF} = 54Ω, CL1 = CL2 = 100pF			10	ns	
Driver Rise or Fall Time	tR, tF	Figures 10 and 12, R _{DIFF} = 54Ω, CL1 = CL2 = 100pF	MAX481E, MAX485E, MAX1487E	3	40	ns	
			MAX490EC/E, MAX491EC/E	5	25		
Driver Enable to Output High	tZH	Figures 11 and 13, CL = 100pF, S2 closed			70	ns	
Driver Enable to Output Low	tZL	Figures 11 and 13, CL = 100pF, S1 closed			70	ns	
Driver Disable Time from Low	tLZ	Figures 11 and 13, CL = 15pF, S1 closed			70	ns	
Driver Disable Time from High	tHZ	Figures 11 and 13, CL = 15pF, S2 closed			70	ns	
Receiver Input to Output	tPLH, tPHL	Figures 10 and 14, R _{DIFF} = 54Ω, CL1 = CL2 = 100pF	MAX481E, MAX485E, MAX1487E	20	60	200	ns
			MAX490EC/E, MAX491EC/E	20	60	150	
tPLH - tPHL Differential Receiver Skew	tSKD	Figures 10 and 14, R _{DIFF} = 54Ω, CL1 = CL2 = 100pF		5		ns	
Receiver Enable to Output Low	tZL	Figures 9 and 15, C _{RL} = 15pF, S1 closed		20	50	ns	
Receiver Enable to Output High	tZH	Figures 9 and 15, C _{RL} = 15pF, S2 closed		20	50	ns	
Receiver Disable Time from Low	tLZ	Figures 9 and 15, C _{RL} = 15pF, S1 closed		20	50	ns	
Receiver Disable Time from High	tHZ	Figures 9 and 15, C _{RL} = 15pF, S2 closed		20	50	ns	
Maximum Data Rate	fMAX		2.5			Mbps	
Time to Shutdown	tSHDN	MAX481 (Note 5)	50	200	600	ns	

MAX481E/MAX483E/MAX485E/MAX487E-MAX491E/MAX1487E

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SWITCHING CHARACTERISTICS—MAX481E/MAX485E, MAX490E/MAX491E, MAX1487E (continued)

(V_{CC} = 5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Enable from Shutdown to Output High (MAX481E)	t _{ZH} (SHDN)	Figures 11 and 13, C _L = 100pF, S2 closed			100	ns
Driver Enable from Shutdown to Output Low (MAX481E)	t _{ZL} (SHDN)	Figures 11 and 13, C _L = 100pF, S1 closed			100	ns
Receiver Enable from Shutdown to Output High (MAX481E)	t _{ZH} (SHDN)	Figures 9 and 15, C _L = 15pF, S2 closed, A - B = 2V			1000	ns
Receiver Enable from Shutdown to Output Low (MAX481E)	t _{ZL} (SHDN)	Figures 9 and 15, C _L = 15pF, S1 closed, B - A = 2V			1000	ns

SWITCHING CHARACTERISTICS—MAX483E, MAX487E/MAX488E/MAX489E

(V_{CC} = 5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	t _{PLH}	Figures 10 and 12, R _{DIFF} = 54Ω, C _{L1} = C _{L2} = 100pF	250		2000	ns
	t _{PHL}		250		2000	
Driver Output Skew to Output	t _{SKEW}	Figures 10 and 12, R _{DIFF} = 54Ω, C _{L1} = C _{L2} = 100pF			800	ns
Driver Rise or Fall Time	t _R , t _F	Figures 10 and 12, R _{DIFF} = 54Ω, C _{L1} = C _{L2} = 100pF	250		2000	ns
Driver Enable to Output High	t _{ZH}	Figures 11 and 13, C _L = 100pF, S2 closed	250		2000	ns
Driver Enable to Output Low	t _{ZL}	Figures 11 and 13, C _L = 100pF, S1 closed	250		2000	ns
Driver Disable Time from Low	t _{LZ}	Figures 11 and 13, C _L = 15pF, S1 closed	300		3000	ns
Driver Disable Time from High	t _{HZ}	Figures 11 and 13, C _L = 15pF, S2 closed	300		3000	ns
Receiver Input to Output	t _{PLH}	Figures 10 and 14, R _{DIFF} = 54Ω, C _{L1} = C _{L2} = 100pF	250		2000	ns
	t _{PHL}		250		2000	
t _{PLH} - t _{PHL} Differential Receiver Skew	t _{SKD}	Figures 10 and 14, R _{DIFF} = 54Ω, C _{L1} = C _{L2} = 100pF		100		ns
Receiver Enable to Output Low	t _{ZL}	Figures 9 and 15, C _{RL} = 15pF, S1 closed			50	ns
Receiver Enable to Output High	t _{ZH}	Figures 9 and 15, C _{RL} = 15pF, S2 closed			50	ns
Receiver Disable Time from Low	t _{LZ}	Figures 9 and 15, C _{RL} = 15pF, S1 closed			50	ns
Receiver Disable Time from High	t _{HZ}	Figures 9 and 15, C _{RL} = 15pF, S2 closed			50	ns
Maximum Data Rate	f _{MAX}	t _{PLH} , t _{PHL} < 50% of data period	250			kbps
Time to Shutdown	t _{SHDN}	MAX483E/MAX487E (Note 5)	50		600	ns
Driver Enable from Shutdown to Output High	t _{ZH} (SHDN)	MAX483E/MAX487E, Figures 11 and 13, C _L = 100pF, S2 closed			2000	ns
Driver Enable from Shutdown to Output Low	t _{ZL} (SHDN)	MAX483E/MAX487E, Figures 11 and 13, C _L = 100pF, S1 closed			2000	ns
Receiver Enable from Shutdown to Output High	t _{ZH} (SHDN)	MAX483E/MAX487E, Figures 9 and 15, C _L = 15pF, S2 closed			2500	ns
Receiver Enable from Shutdown to Output Low	t _{ZL} (SHDN)	MAX483E/MAX487E, Figures 9 and 15, C _L = 15pF, S1 closed			2500	ns

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NOTES FOR ELECTRICAL/SWITCHING CHARACTERISTICS

- Note 1:** All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.
- Note 2:** All typical specifications are given for $V_{CC} = 5V$ and $T_A = +25^{\circ}C$.
- Note 3:** Supply current specification is valid for loaded transmitters when $DE = 0V$.
- Note 4:** Applies to peak current. See *Typical Operating Characteristics*.
- Note 5:** The MAX481E/MAX483E/MAX487E are put into shutdown by bringing \overline{RE} high and DE low. If the inputs are in this state for less than 50ns, the parts are guaranteed not to enter shutdown. If the inputs are in this state for at least 600ns, the parts are guaranteed to have entered shutdown. See *Low-Power Shutdown Mode* section.

Pin Description

PIN					NAME	FUNCTION
MAX481E/MAX483E MAX485E/MAX487E MAX1487E	μMAX	MAX488E MAX490E	μMAX	MAX489E MAX491E		
DIP/SO	μMAX	DIP/SO	μMAX	DIP/SO		
1	3	2	4	2	RO	Receiver Output: If $A > B$ by 200mV, RO will be high; If $A < B$ by 200mV, RO will be low.
2	4	—	—	3	\overline{RE}	Receiver Output Enable. RO is enabled when \overline{RE} is low; RO is high impedance when \overline{RE} is high.
3	5	—	—	4	DE	Driver Output Enable. The driver outputs, Y and Z, are enabled by bringing DE high. They are high impedance when DE is low. If the driver outputs are enabled, the parts function as line drivers. While they are high impedance, they function as line receivers if \overline{RE} is low.
4	6	3	5	5	DI	Driver Input. A low on DI forces output Y low and output Z high. Similarly, a high on DI forces output Y high and output Z low.
5	7	4	6	6, 7	GND	Ground
—	—	5	7	9	Y	Noninverting Driver Output
—	—	6	8	10	Z	Inverting Driver Output
6	8	—	—	—	A	Noninverting Receiver Input and Noninverting Driver Output
—	—	8	2	12	A	Noninverting Receiver Input
7	1	—	—	—	B	Inverting Receiver Input and Inverting Driver Output
—	—	7	1	11	B	Inverting Receiver Input
8	2	1	3	14	V _{CC}	Positive Supply: $4.75V \leq V_{CC} \leq 5.25V$
—	—	—	—	1, 8, 13	N.C.	No Connect—not internally connected

MAX481E/MAX483E/MAX485E/MAX487E-MAX491E/MAX1487E

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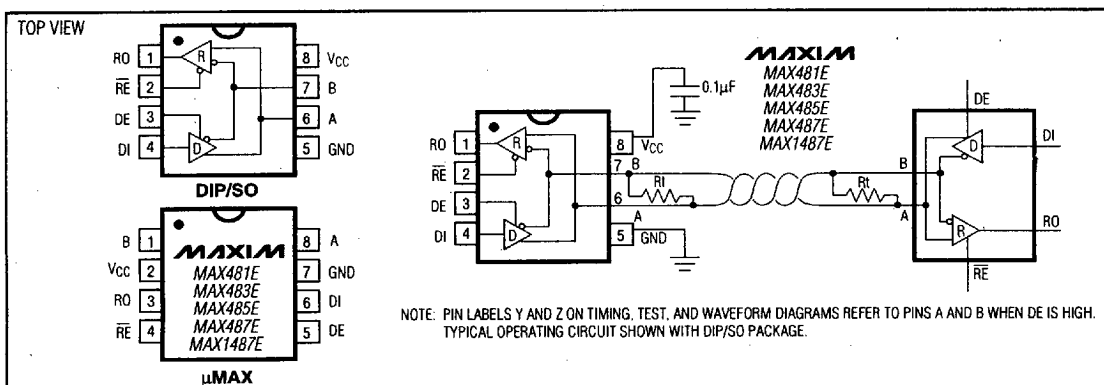


Figure 1. MAX481E/MAX483E/MAX485E/MAX1487E Pin Configuration and Typical Operating Circuit

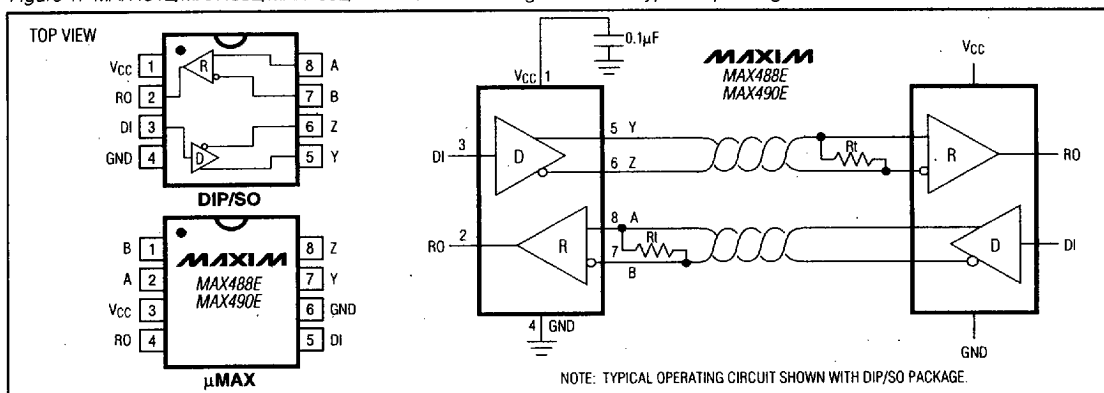


Figure 2. MAX488E/MAX490E Pin Configuration and Typical Operating Circuit

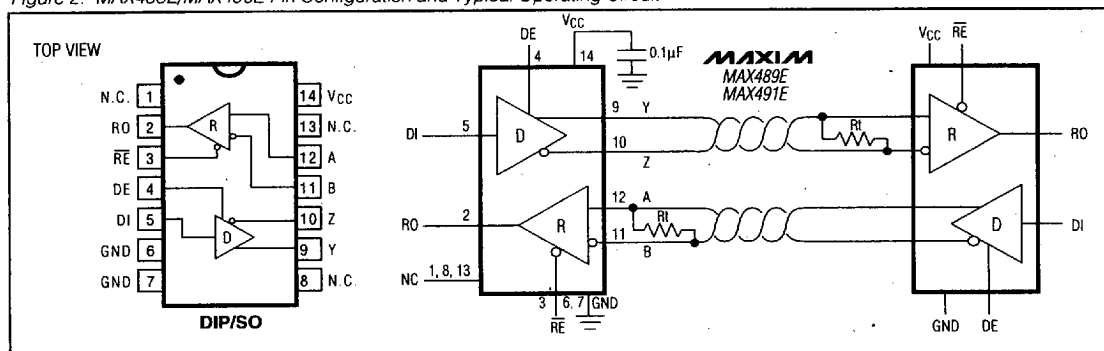


Figure 3. MAX489E/MAX491E Pin Configuration and Typical Operating Circuit

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Function Tables (MAX481E/MAX483E/MAX485E/MAX487E/MAX1487E)

Table 1. Transmitting

INPUTS			OUTPUTS	
RE	DE	DI	Z	Y
X	1	1	0	1
X	1	0	1	0
0	0	X	High-Z	High-Z
1	0	X	High-Z*	High-Z*

X = Don't care

High-Z = High impedance

* Shutdown mode for MAX481E/MAX483E/MAX487E

Table 2. Receiving

INPUTS			OUTPUT
RE	DE	A-B	RO
0	0	$\geq +0.2V$	1
0	0	$\leq -0.2V$	0
0	0	Inputs open	1
1	0	X	High-Z*

X = Don't care

High-Z = High impedance

* Shutdown mode for MAX481E/MAX483E/MAX487E

Applications Information

The MAX481E/MAX483E/MAX485E/MAX487E-MAX491E and MAX1487E are low-power transceivers for RS-485 and RS-422 communications. These "E" versions of the MAX481, MAX483, MAX485, MAX487-MAX491, and MAX1487 provide extra protection against ESD. The rugged MAX481E, MAX483E, MAX485E, MAX497E-MAX491E, and MAX1487E are intended for harsh environments where high-speed communication is important. These devices eliminate the need for transient suppressor diodes and the associated high capacitance loading. The standard (non-"E") MAX481, MAX483, MAX485, MAX487-MAX491, and MAX1487 are recommended for applications where cost is critical.

The MAX481E, MAX485E, MAX490E, MAX491E, and MAX1487E can transmit and receive at data rates up to 2.5Mbps, while the MAX483E, MAX487E, MAX488E, and MAX489E are specified for data rates up to 250kbps. The MAX488E-MAX491E are full-duplex transceivers, while the MAX481E, MAX483E, MAX487E, and MAX1487E are half-duplex. In addition, driver enable (DE) and receiver enable (RE) pins are included on the MAX481E, MAX483E, MAX485E, MAX487E, MAX489E, MAX491E, and MAX1487E. When disabled, the driver and receiver outputs are high impedance.

±15kV ESD Protection

As with all Maxim devices, ESD-protection structures are incorporated on all pins to protect against electrostatic discharges encountered during handling and assembly. The driver outputs and receiver inputs have extra protection against static electricity. Maxim's engi-

neers developed state-of-the-art structures to protect these pins against ESD of ±15kV without damage. The ESD structures withstand high ESD in all states: normal operation, shutdown, and powered down. After an ESD event, Maxim's MAX481E, MAX483E, MAX485E, MAX487E-MAX491E, and MAX1487E keep working without latchup.

ESD protection can be tested in various ways; the transmitter outputs and receiver inputs of this product family are characterized for protection to ±15kV using the Human Body Model.

Other ESD test methodologies include IEC10004-2 contact discharge and IEC1000-4-2 air-gap discharge (formerly IEC801-2).

ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test set-up, test methodology, and test results.

Human Body Model

Figure 4 shows the Human Body Model, and Figure 5 shows the current waveform it generates when discharged into a low impedance. This model consists of a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a 1.5kΩ resistor.

IEC1000-4-2

The IEC1000-4-2 standard covers ESD testing and performance of finished equipment; it does not specifically refer to integrated circuits (Figure 6).

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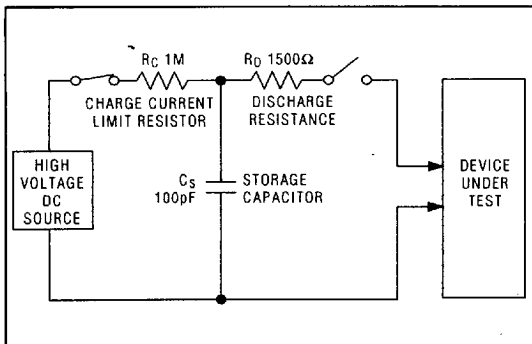


Figure 4. Human Body ESD Test Model

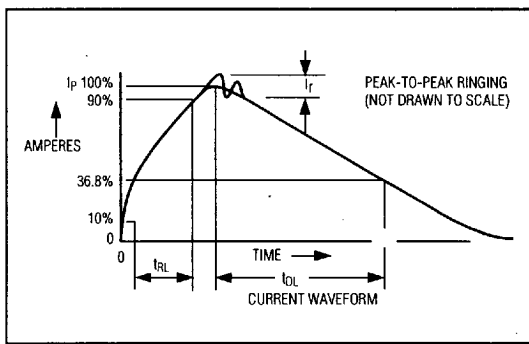


Figure 5. Human Body Model Current Waveform

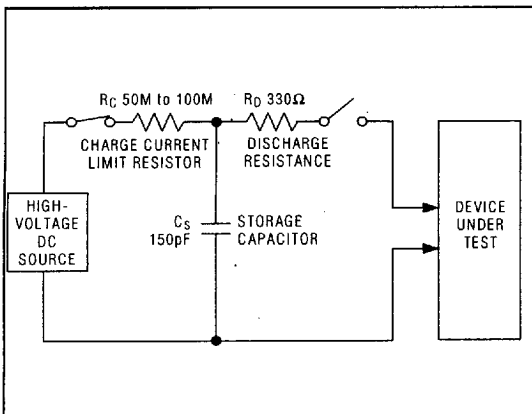


Figure 6. IEC1000-4-2 ESD Test Model

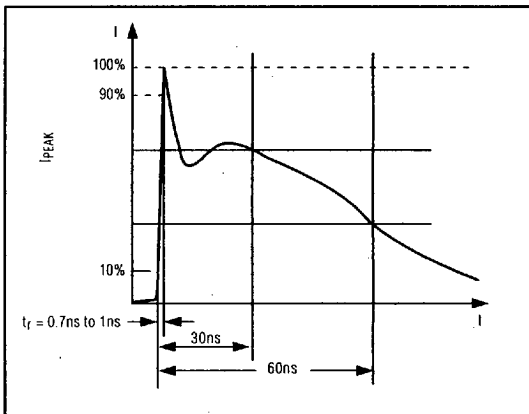


Figure 7. IEC1000-4-2 ESD Generator Current Waveform

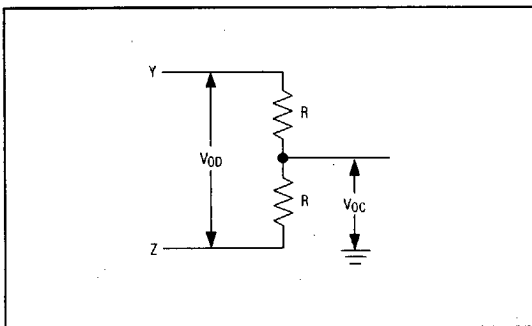


Figure 8. Driver DC Test Load

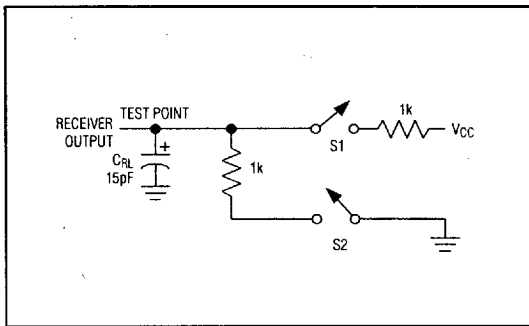


Figure 9. Receiver Timing Test Load

MAX481E/MAX483E/MAX485E/MAX487E-MAX491E/MAX1487E

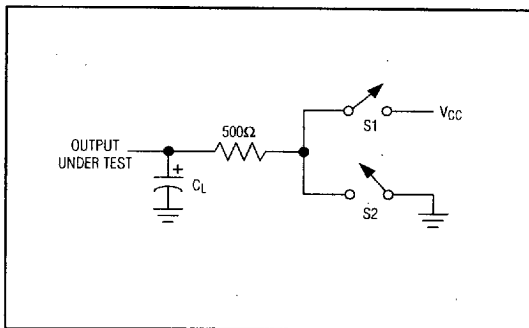


Figure 11. Driver Timing Test Load

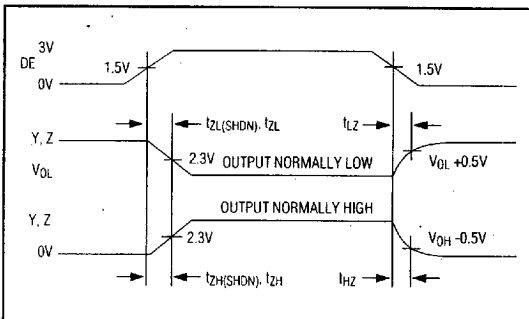


Figure 13. Driver Enable and Disable Times (except MAX488E and MAX490E)

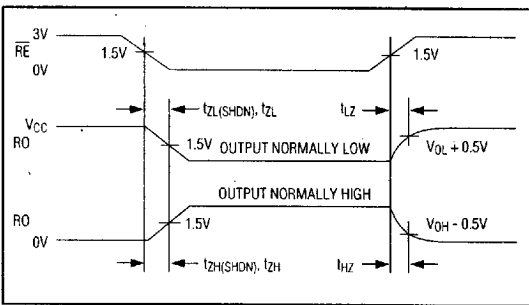


Figure 15. Receiver Enable and Disable Times (except MAX488E and MAX490E)

MAX481E/MAX483E/MAX485E/MAX487E-MAX491E/MAX1487E

The air-gap test involves approaching the device with a charged probe. The contact-discharge method connects the probe to the device before the probe is energized.

The Machine Model for ESD tests all pins using a 200pF storage capacitor and zero discharge resistance. Its objective is to emulate the stress caused by contact that occurs with handling and assembly during manufacturing. Of course, all pins require this protection during manufacturing—not just inputs and outputs. Therefore, after PC board assembly, the Machine Model is less relevant to I/O ports.

The 48k Ω , 1/4-unit-load receiver input impedance of the MAX487E and MAX1487E allows up to 128 transceivers on a bus, compared to the 1-unit load (12k Ω input impedance) of standard RS-485 drivers (32 transceivers maximum). Any combination of MAX487E/MAX1487E and other RS-485 transceivers with a total of 32 unit loads or less can be put on the bus. The MAX481E, MAX483E, MAX485E, and MAX488E-MAX491E have standard 12k Ω receiver input impedance.

The MAX483E and MAX487E–MAX489E are slow-rate limited, minimizing EMI and reducing reflections caused by improperly terminated cables.

A low-power shutdown mode is initiated by bringing both RE high and DE low. The devices will not shut down unless both the driver and receiver are disabled. In shutdown, the devices typically draw only 0.1µA of supply current.

For the MAX481E, MAX483E, and MAX487E, the t_{ZH} and t_{ZL} enable times assume the part was not in the low-power shutdown state (the MAX485E, MAX488E–MAX491E, and MAX1487E can not be shut down). The t_{ZH}(SHDN) and t_{ZL}(SHDN) enable times assume the parts were shut down (see *Electrical Characteristics*).

Driver Output Protection

Excessive output current and power dissipation caused by faults or by bus contention are prevented by two mechanisms. A foldback current limit on the output stage provides immediate protection against short circuits over the whole common-mode voltage range (see *Typical Operating Characteristics*). In addition, a thermal shutdown circuit forces the driver outputs into a high-impedance state if the die temperature rises excessively.

The MAX481E, MAX483E, MAX485E, MAX487E, MAX491E, and MAX1487E transceivers are designed for bidirectional data communications on multipoint bus transmission lines. Figures 16 and 17 show typical network application circuits. These parts can also be used as line repeaters, with cable lengths longer than 4000 feet.

To minimize reflections, the line should be terminated at both ends in its characteristic impedance, and stub lengths off the main line should be kept as short as possible. The slew-rate-limited MAX483E and MAX487E-MAX489E are more tolerant of imperfect termination. Bypass the VCC pin with 0.1µF.

For isolated RS-485 applications, see the MAX253 and MAX1480 data sheets.

$\pm 15\text{kV}$ ESD-Protected, Slew-Rate-Limited, Low-Power, RS-485/RS-422 Transceivers

MAX481E/MAX483E/MAX485E/MAX487E-MAX491E/MAX1487E

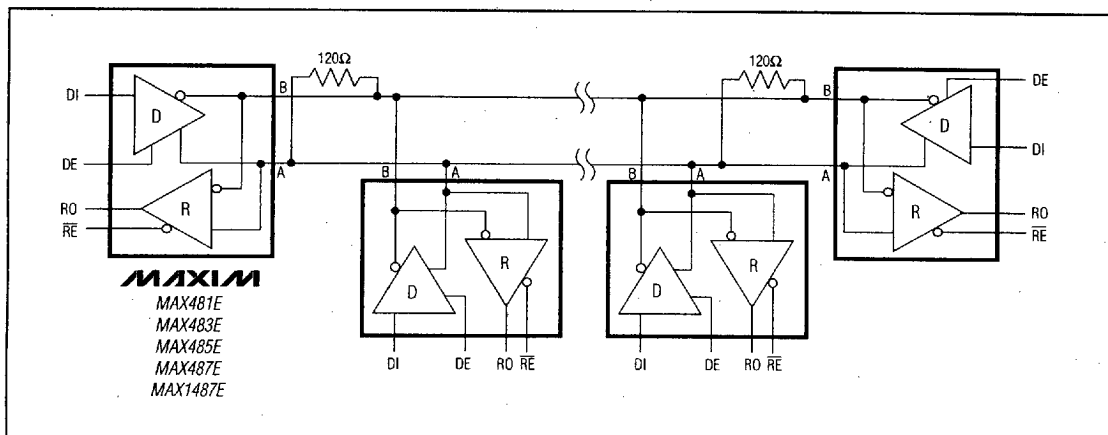


Figure 16. MAX481E/MAX483E/MAX485E/MAX487E/MAX1487E Typical Half-Duplex RS-485 Network

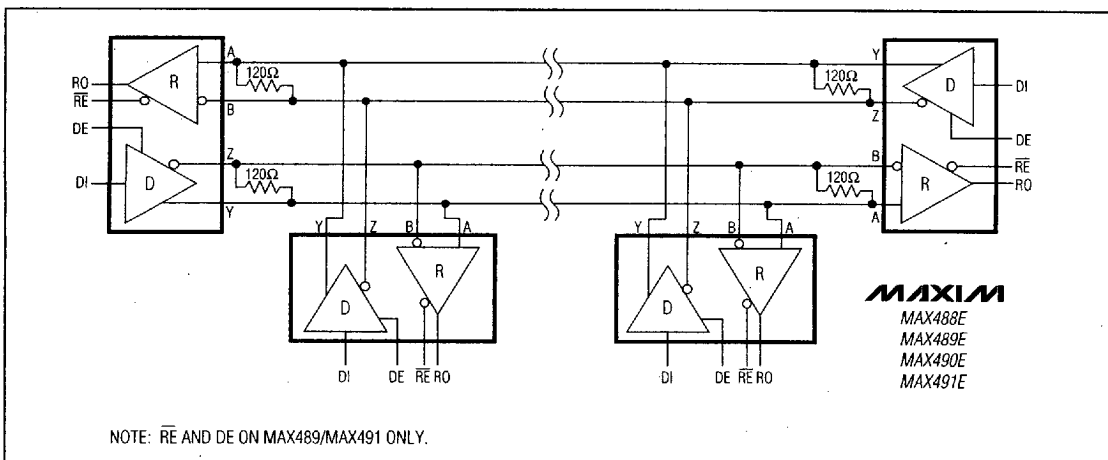


Figure 17. MAX488E-MAX491E Full-Duplex RS-485 Network

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±15kV ESD-Protected, Slew-Rate-Limited, Low-Power, RS-485/RS-422 Transceivers

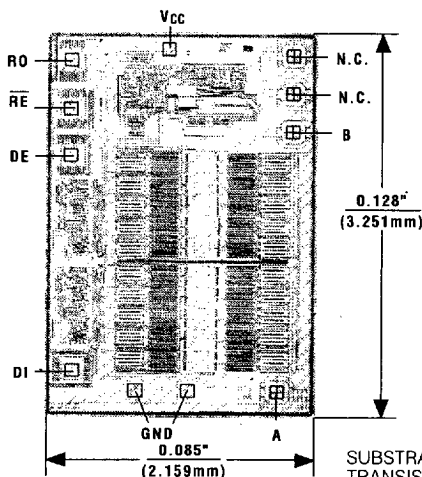
Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
MAX483ECPA	0°C to +70°C	8 Plastic DIP
MAX483ECSA	0°C to +70°C	8 SO
MAX483ECUA	0°C to +70°C	8 µMAX
MAX483EEPA	-40°C to +85°C	8 Plastic DIP
MAX483EESA	-40°C to +85°C	8 SO
MAX485ECPA	0°C to +70°C	8 Plastic DIP
MAX485ECSA	0°C to +70°C	8 SO
MAX485ECUA	0°C to +70°C	8 µMAX
MAX485EEPA	-40°C to +85°C	8 Plastic DIP
MAX485EESA	-40°C to +85°C	8 SO
MAX487ECPA	0°C to +70°C	8 Plastic DIP
MAX487ECSA	0°C to +70°C	8 SO
MAX487ECUA	0°C to +70°C	8 µMAX
MAX487EEPA	-40°C to +85°C	8 Plastic DIP
MAX487EESA	-40°C to +85°C	8 SO
MAX488ECPA	0°C to +70°C	8 Plastic DIP
MAX488ECSA	0°C to +70°C	8 SO
MAX488ECUA	0°C to +70°C	8 µMAX
MAX488EEPA	-40°C to +85°C	8 Plastic DIP
MAX488EESA	-40°C to +85°C	8 SO

PART	TEMP. RANGE	PIN-PACKAGE
MAX489ECPD	0°C to +70°C	14 Plastic DIP
MAX489ECSD	0°C to +70°C	14 SO
MAX489EEPD	-40°C to +85°C	14 Plastic DIP
MAX489EESD	-40°C to +85°C	14 SO
MAX490ECPA	0°C to +70°C	8 Plastic DIP
MAX490ECSA	0°C to +70°C	8 SO
MAX490ECUA	0°C to +70°C	8 µMAX
MAX490EEPA	-40°C to +85°C	8 Plastic DIP
MAX490EESA	-40°C to +85°C	8 SO
MAX491ECPD	0°C to +70°C	14 Plastic DIP
MAX491ECSD	0°C to +70°C	14 SO
MAX491EEPD	-40°C to +85°C	14 Plastic DIP
MAX491EESD	-40°C to +85°C	14 SO
MAX1487ECPA	0°C to +70°C	8 Plastic DIP
MAX1487ECSA	0°C to +70°C	8 SO
MAX1487ECUA	-40°C to +85°C	8 µMAX
MAX1487EEPA	-40°C to +85°C	8 Plastic DIP
MAX1487EESA	-55°C to +125°C	8 SO

Chip Topographies

MAX481E/483E/485E/487E/1487E



MAX488E/489E/490E/491E

