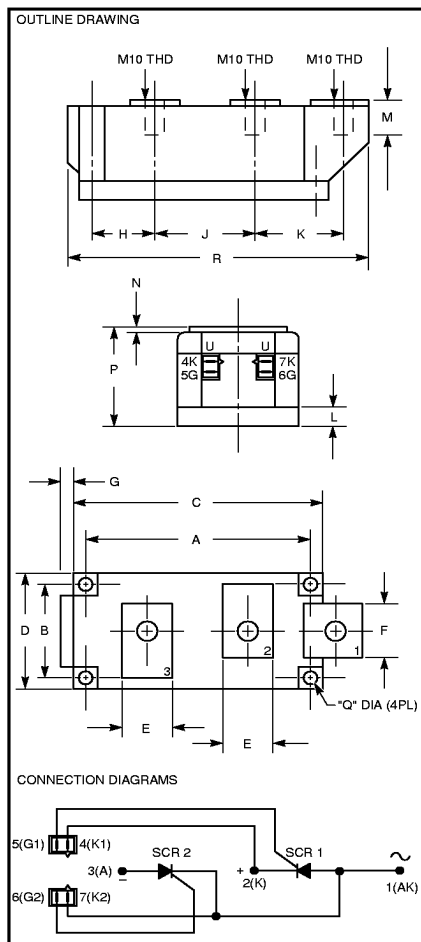


Dual SCR POW-R-BLOK™ Module 500 Amperes/600-1600 Volts



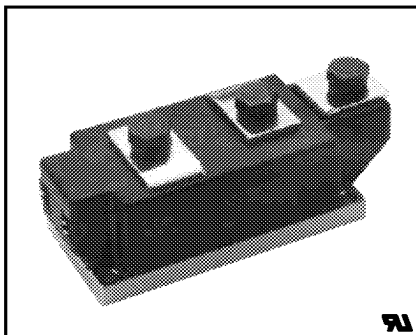
Outline Drawing

Ordering Information:

Select the complete eight digit module part number you desire from the table below.

Example: LD431650 is a 1600 Volt, 500 Ampere Dual SCR POW-R-BLOK™ Module.

Type	Voltage Volts (x100)	Current Rating Amperes (x10)
LD43	08 10 thru 16	50



LD43 _50
Dual SCR POW-R-BLOK™ Module
500 Amperes/600-1600 Volts

Dimension	Inches	Millimeters
A	4.41	112
B	1.89	48
C	4.88	124
D	2.36	60
E	0.98	25
F	1.10	28
G	0.24	6
H	1.22	31
J	1.97	50
K	1.73	44
L	0.39	10
M	0.71	18
N	0.12	3
P	2.05	52
Q	0.26	6.5
R	5.91	150
U	0.110 x 0.032	2.5 x 0.8

Description:

The LD43 is a pair of high current-phase control thyristors mounted in an electrically isolated module. The semiconductor elements are secured by compression bonding for excellent cycling performance. The LD43 features a self contained electrical isolation system. By using high thermal conductivity BeO ceramic isolators, excellent circuit-to-baseplate isolation (≥ 3000 volts RMS) has been achieved, while maintaining efficient cooling of the semiconductor elements. All LD Series SCRs use the "di/Namic" gate structure for ease of triggering and high di/dt capability.

Features:

- ☐ Low On-State Voltage
- ☐ High di/dt Capability
- ☐ High dv/dt Capability
- ☐ Excellent Surge and I²t Ratings
- ☐ Low Thermal Impedance
- ☐ UL Recognized 

Applications:

- ☐ Power Supplies
- ☐ Motor Control
- ☐ Large IGBT Circuit "Front Ends"

LD43_50

Dual SCR POW-R-BLOK™ Module

500 Amperes/600-1600 Volts

Absolute Maximum Ratings

Characteristics	Symbol	Conditions	LD43_50	Units
Repetitive Peak Forward Blocking Voltage	V_{DRM}	—	1600	Volts
Repetitive Peak Reverse Blocking Voltage	V_{RRM}	—	1600	Volts
Non-Repetitive Peak Forward Blocking Voltage	V_{DSM}	—	$V_{DRM} + 100$	Volts
Non-Repetitive Peak Reverse Blocking Voltage	V_{RSM}	—	$V_{RRM} + 100$	Volts
RMS Forward Current	$I_T(RMS)$	—	785	Amperes
Average Forward Current	$I_T(AV)$	180° Conduction, $T_C = 75^\circ C$	500	Amperes
Peak Half-Cycle Surge (Non-Repetitive) On-State Current	I_{TSM}	$t = 8.3ms, 100\%V_{RRM}$ Reapplied	13000	Amperes
		$t = 10ms, 100\%V_{RRM}$ Reapplied	12500	Amperes
I^2t (for Fusing) for One-Cycle	I^2t	$t = 8.3ms, 100\%V_{RRM}$ Reapplied	715,000	A^2sec
		$t = 10ms, 100\%V_{RRM}$ Reapplied	781,000	A^2sec
Maximum Rate-of-Rise of On-State Current (Non-Repetitive)*	di/dt	$I_{TM} = \pi I_T(AV), t_r < 0.5\mu s, t_p > 6\mu s$	120	Amperes/ μs
Storage Temperature	T_{STG}	—	-40 to 130	$^\circ C$
Operating Temperature	T_J	—	-40 to 125	$^\circ C$
Maximum Mounting Torque M6 Mounting Screw	—	—	6	Nm
Maximum Mounting Torque M10 Terminal Screw	—	—	12	Nm
Module Weight (Typical)	—	—	900	Grams
			2	lb.
V Isolation	V_{RMS}	—	3000	Volts

* $T_J = 25^\circ C, I_G = 500mA, V_D = 0.67 V_{DRM}$ (Rated)

LD43_50

Dual SCR POW-R-BLOK™ Module

500 Amperes/600-1600 Volts

Electrical and Thermal Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

			LD43_50			
Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Blocking State Maximums						
Forward Off-State Current, Peak	I _{DRM}	T _j = 125°C, V _D = V _{DRM}	—	—	80	mA
Reverse Off-State Current, Peak	I _{RRM}	T _j = 125°C, V _R = V _{RRM}	—	—	80	mA
Conducting State Maximums						
Peak On-State Voltage	V _{TM}	T _j = 125°C, I _{TM} = 1500A, Duty Cycle < 0.1%	—	—	1.36	Volts
Peak On-State Current Coefficients, Full Range	V _{TM}	T _j = 125°C, I = 15% I _{T(AV)} to I _{TSM} V _{TM} = A + B Ln I + C I + D Sqrt I	A = 0.4774 B = 0.0409 C = 0.00018 D = 0.00763			
Threshold Voltage, Low-Level	V _{(TO)1}	T _j = 125°C,	—	—	0.768	Volts
Slope Resistance, Low-Level	r _{T1}	I = 15% I _{T(AV)} to πI _{T(AV)}	—	—	0.361	mΩ
Threshold Voltage, High-Level	V _{(TO)2}	T _j = 125°C,	—	—	1.052	Volts
Slope Resistance, High-Level	r _{T2}	I = πI _{T(AV)} to I _{TSM}	—	—	0.234	mΩ
Switching Minimums						
Critical Rate-of-Rise of Off-State Voltage	dv/dt	T _j = 125°C, Gate Open, Exponential to V _{DRM}		1000		Volts/μs
Thermal Maximums						
Thermal Resistance, Junction-to-Case	R _{θ(J-C)}	Per Module, Both Conducting	—	—	0.035	°C/Watt
		Per SCR, Both Conducting	—	—	0.070	°C/Watt
Thermal Resistance, Case-to-Sink (Lubricated)	R _{θ(C-S)}	Per Module	—	—	0.01	°C/Watt
Gate Parameters Maximums						
Gate Current-to-Trigger	I _{GT}	T _j = 25°C, V _D = 6V	—	250	—	mA
Gate Voltage-to-Trigger	V _{GT}	T _j = 25°C, V _D = 6V	—	1.5	—	Volts
Non-Triggering Gate Voltage	V _{GDM}	T _j = 125°C, V _D = 1/2 V _{DRM}	—	0.20	—	Volts
Peak Forward Gate Current	I _{GTM}	—	—	4.0	—	Amperes
Peak Reverse Gate Voltage	V _{GRM}	—	—	5.0	—	Volts

WARNING:

Internal insulation used is Beryllium Oxide.

User should avoid grinding, crushing, or abrading these portions.

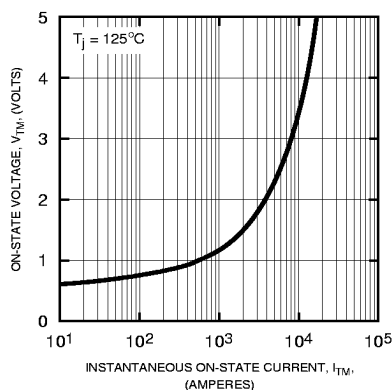
Care must be exercised in properly disposing of unwanted devices.

LD43__50

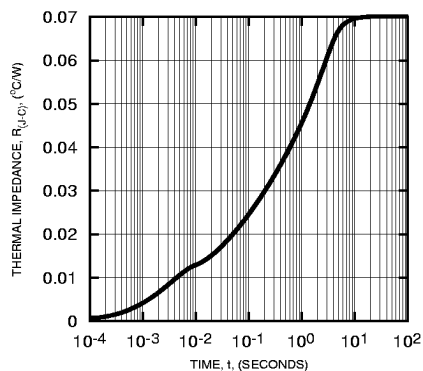
Dual SCR POW-R-BLOK™ Module

500 Amperes/600-1600 Volts

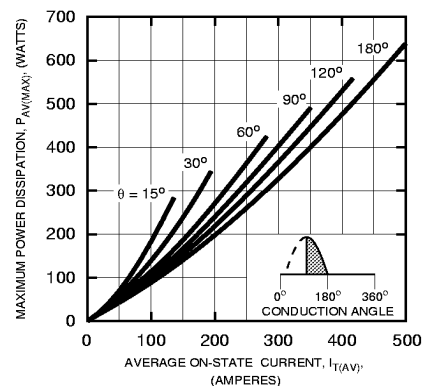
MAXIMUM ON-STATE FORWARD VOLTAGE DROP



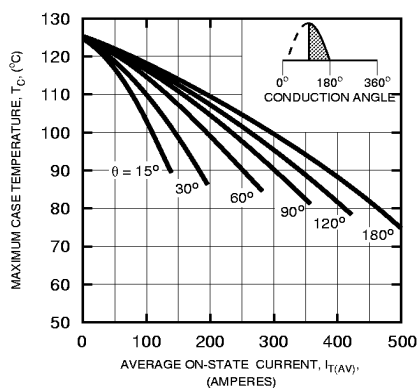
MAXIMUM TRANSIENT THERMAL IMPEDANCE (JUNCTION-TO-CASE) (PER SCR)



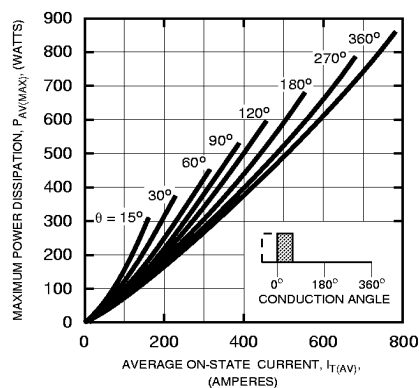
MAXIMUM ON-STATE POWER DISSIPATION (SINUSOIDAL WAVEFORM) (PER SCR)



MAXIMUM ALLOWABLE CASE TEMPERATURE (SINUSOIDAL WAVEFORM)



MAXIMUM ON-STATE POWER DISSIPATION (RECTANGULAR WAVEFORM) (PER SCR)



MAXIMUM ALLOWABLE CASE TEMPERATURE (RECTANGULAR WAVEFORM)

