

LH2110/LH2210/LH2310 **Dual Voltage Followers**

General Description

The LH2110 series of dual voltage followers are two LM110 type followers in a single hermetic package. Featuring all the same performance characteristics of the single, these duals offer in addition closer thermal tracking, lower weight, reduced insertion cost and smaller size than two singles. For additional information, see the LM110 data sheet and National's Linear Applications Handbook.

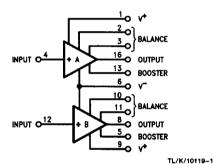
The LH2110 is specified for operation over the -55°C to +125°C military temperature range. The LH2210 is specified for operation over the -25°C to +85°C temperature range. The LH2310 is specified for operation over the 0°C to +70°C temperature range.

Features

- Low input current
- High input resistance
- High slew rate
- Wide bandwidth
- Wide operating supply range
- Output short circuit proof

- 1 nA $10^{10}\Omega$
- 30V/µs
 - 20 MHz \pm 5V to \pm 18V

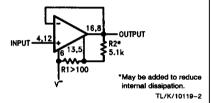
Connection Diagram



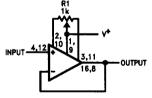
Order Number LH2110D, LH2210D or LH2310D See NS Package Number D16C

Auxiliary Circuits

Increasing Negative Swing Under Load



Offset Balancing Circuit



TL/K/10119-3

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ± 18V

Power Dissipation (Note 1) 500 mW Input Voltage (Note 2) ± 15V

Output Short Circuit Duration (Note 3)

LH2310 0°C to +70°C

Continuous

Storage Temperature Range -65°C to +150°C Lead Temperature (Soldering, 10 sec.) 300°C

Electrical Characteristics Each Side (Note 4)

Parameter	Conditions	Limits			Units
		LH2110	LH2210	LH2310	Jille
Input Offset Voltage	T _A = 25°C	4.0	4.0	7.5	mV Max
Input Bias Current Input Resistance	T _A = 25°C T _A = 25°C	3.0 10 ¹⁰	3.0 10 ¹⁰	7.0 10 ¹⁰	nA Max Ω Min
Input Capacitance		1.5	1.5	1.5	pF Typ
Large Signal Voltage Gain	$T_A = 25^{\circ}C, V_S = \pm 15V$ $V_{OUT} = \pm 10V, R_L = 8 k\Omega$	0.999	0.999	0.999	V/V Min
Output Resistance	T _A = 25°C	2.5	2.5	2.5	Ω Max
Supply Current (Each Amplifier)	T _A = 25°C	5.5	5.5	5.5	mA Max
Input Offset Voltage		6.0	6.0	10	mV Max
Offset Voltage Temperature Drift	-55°C ≤ T _A ≤ 85°C T _A = 125°C	6 12	6 12	10	μ∨/°С Ту
Input Bias Current		10	10	10	nA Max
Large Signal Voltage Gain	$V_{S} = \pm 15V, V_{OUT} = \pm 10V$ $R_{L} = 10 \text{ k}\Omega$	0.999	0.999	0.999	μV/V Mir
Output Voltage Swing (Note 5)	$V_S = \pm 15V$, $R_L = 10 \text{ k}\Omega$	±10	± 10	± 10	V Min
Supply Current (Each Amplifier)	T _A = 125°C	4.0	4.0		mA Max
Supply Voltage Rejection Ratio	±5V ≤ V _S ≤ ±18V	70	70	70	dB Min

Note 1: The maximum junction temperature of the LH2110 is 150°C, while that of the LH2210 is 100°C and that of the LH2310 is 85°C. For operating devices in the flat package at elevated temperatures, the derating is based on a thermal resistance of 185°C/W when mounted on a 1/1e-inch-thick epoxy glass board with 0.03-inch-wide, 2-ounce copper conductors. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

Note 2: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

Note 3: Continuous short circuit is allowed for case temperatures to 125°C and ambient temperatures to 70°C. It is necessary to insert a resistor greater than 2 kΩ in series with the input when the amplifier is driven from low impedance sources to prevent damage when the output is shorted.

Note 4: These specifications apply for $\pm 5V \le V_S \le \pm 18V$ and $-55^{\circ}C \le T_A \le 125^{\circ}C$, unless otherwise specified. With the LM210, however, all temperature specifications are limited to $-25^{\circ}C \le T_A \le 85^{\circ}C$, and for the LH2310, all temperature specifications are limited to $0^{\circ}C \le T_A \le 70^{\circ}C$.

Note 5: Increased output swing under load can be obtained by connecting an external resistor between the booster and V- terminals.