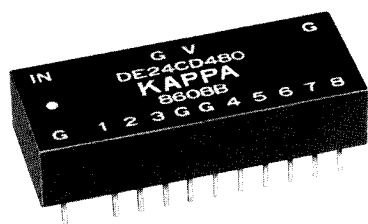


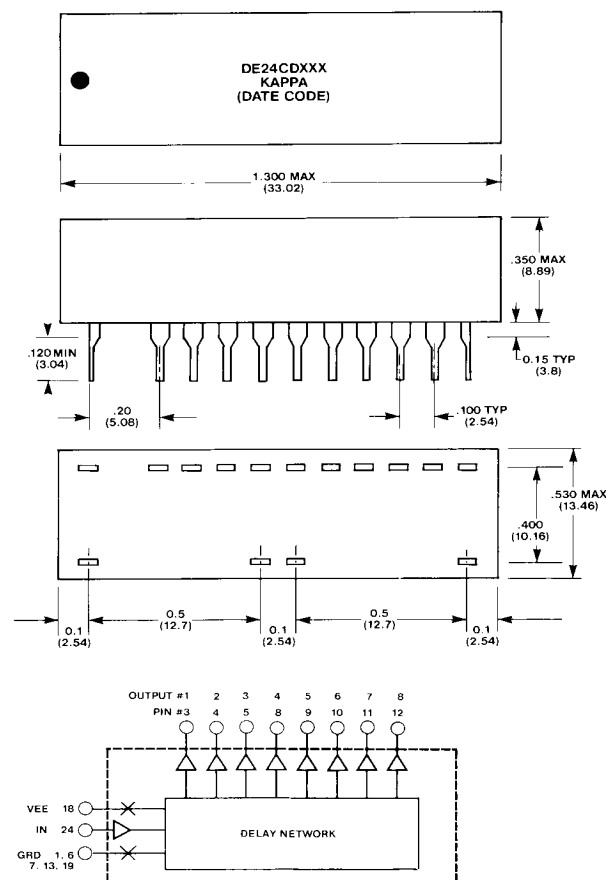
ECL 100K DELAY LINE



FEATURES

- ECL 100K Input and Output
- 24-Pin Package
- 8 Equally-Spaced Taps
- 70 ECL Fan Out Capability
- Operating temperature range: 0°C to +85°C

MARKINGS AND DIMENSIONS, in (mm)



RECOMMENDED OPERATING CONDITIONS

	MIN	TYP	MAX	UNIT
V _{EE} Supply Voltage		-4.5		V
V _{CC} Ground		0		V
T _A Operating Free-Air Temperature	0	+25	+85	C
V _{IH} High-Level Input Voltage	-1.16			V
V _{IL} Low Level Input Voltage			-1.63	V

DC ELECTRICAL CHARACTERISTICS

TEST CONDITIONS

V _{OH} High-Level Output Voltage	V _{IN} = V _{IH} (MAX)	-1.02		V
V _{OL} Low-Level Output Voltage	V _{IN} = V _{IL} (MIN)		-1.62	V
I _{IH} High-Level Input Current	V _{IN} = V _{IH} (MAX)		18	mA
I _{IL} Low-Level Input Current	V _{IN} = V _{IL} (MIN)	0.5		μA
I _{EE} Supply Current	V _{IN} = Open		100	mA

INPUT PULSE TEST CONDITIONS

E _{IN} Pulse Voltage (0.9V to 1.7V)		0.8		V
T _{RI} Pulse Risettime		1		ns
T _W Pulse Width of Total Delay	100	300		%
d Duty Cycle			50	%

PART NUMBER	Total Delay (ns)	Tap Delay ± (ns)
DE24CD080	8	1 ± 0.5
DE24CD160	16	2
DE24CD240	24	3
DE24CD320	32	4
DE24CD400	40	5
DE24CD480	48	6
DE24CD560	56	7
DE24CD640	64	8
DE24CD720	72	9
DE24CD800	80	10
DE24CD161	160	20
DE24CD201	200	25

Notes:

1. Delays measured at -1.3V level on leading edge only.
2. Risettime measured from 20% to 80% of waveform.
3. Tap tolerances: ±5% or ±1 ns, whichever is greater.
4. Tap delays referenced from input.
5. Other delays also available upon request.
6. Output load with a 50 ohms pulldown resistor to -2.0V.

KAPPA[®]

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