

# DIM400GDM33-F000

# **Dual Switch IGBT Module**

PDS5616-1.1 June 2003

### **FEATURES**

- Soft Punch Through Silicon
- 10µs Short Circuit Withstand
- Isolated MMC Base with AIN Substrates
- High Thermal Cycling Capability

### **APPLICATIONS**

- High Reliability Inverters
- Motor Controllers
- Traction Auxiliaries

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 3600A.

The DIM400GDM33-F000 is a dual switch 3300V, soft punch through, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full  $10\mu s$  short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### **ORDERING INFORMATION**

Order As:

#### DIM400GDM33-F000

Note: When ordering, please use the whole part number.

### **KEY PARAMETERS**

V <sub>CES</sub>		3300V
V <sub>CE(sat)</sub> *	(typ)	3.0V
I <sub>C</sub>	(max)	400A
I <sub>C(BK)</sub>	(max)	800A

\*(Measured at the auxiliary terminals)

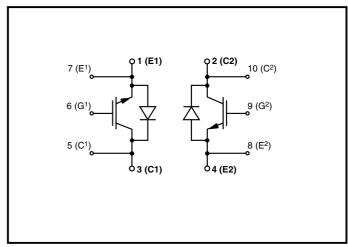


Fig. 1 Dual switch circuit diagram

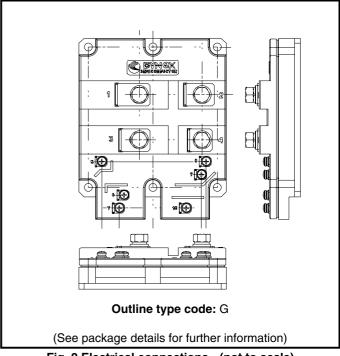


Fig. 2 Electrical connections - (not to scale)



### **ABSOLUTE MAXIMUM RATINGS - PER ARM**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V, T_{j} = -25^{\circ}C$	3300	٧
V <sub>GES</sub>	Gate-emitter voltage	-	±20	٧
I <sub>c</sub>	Continuous collector current	$T_{case} = 90^{\circ}C$	400	Α
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 115°C	800	Α
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	5.2	kW
l²t	Diode l²t value	$V_{R} = 0, t_{p} = 10 \text{ms}, T_{vj} = 125^{\circ}\text{C}$	80	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage - per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
$Q_{_{PD}}$	Partial discharge - per module	IEC1287. V <sub>1</sub> = 3500V, V <sub>2</sub> = 2600V, 50Hz RMS	10	pC



# THERMAL AND MECHANICAL RATINGS

Internal insulation material: AIN
Baseplate material: AISiC
Creepage distance: 33mm
Clearance: 20mm
CTI (Critical Tracking Index): 175

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - transistor (per arm)	Continuous dissipation -	-	-	24	°C/kW
		junction to case				
R <sub>th(j-c)</sub>	Thermal resistance - diode (per arm)	Continuous dissipation -	-	-	48	°C/kW
		junction to case				
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink	Mounting torque 5Nm	-	-	8	°C/kW
	(per module)	(with mounting grease)				
T <sub>j</sub>	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting - M6	-	-	5	Nm
		Electrical connections - M4	-	-	2	Nm
		Electrical connections - M8	-	-	10	Nm



# **ELECTRICAL CHARACTERISTICS**

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
I <sub>CES</sub>	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$	-	-	2	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$	-	-	30	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	1	μА
$V_{\text{GE(TH)}}$	Gate threshold voltage	$I_{\rm C} = 40 {\rm mA}, V_{\rm GE} = V_{\rm CE}$	4.2	5.1	6.0	V
V <sub>CE(sat)</sub> †	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A	-	3.0	-	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A, , T <sub>case</sub> = 125°C	-	3.6	-	V
I <sub>F</sub>	Diode forward current	DC	-	400	-	А
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms	-	800	-	А
V <sub>F</sub> <sup>†</sup>	Diode forward voltage	I <sub>F</sub> = 400A	-	2.45	-	V
		I <sub>F</sub> = 400A, T <sub>case</sub> = 125°C	-	2.35	-	V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz	-	50	-	nF
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz	-	2.6	-	nF
L <sub>M</sub>	Module inductance - per arm	-	-	25	-	nH
R <sub>INT</sub>	Internal transistor resistance - per arm	-	-	0.26	-	mΩ
SC <sub>Data</sub>	Short circuit. I <sub>sc</sub>	$T_j = 125^{\circ}C, V_{CC} = 2500V,$ $I_1$	-	1900	-	Α
		$t_p \le 10\mu s$ , $V_{CE(max)} = V_{CES} - L^*$ . di/dt $I_2$	-	1750	-	А
		IEC 60747-9				

### Note:

<sup>&</sup>lt;sup>†</sup> Measured at the auxiliary terminals.

 $L^*$  is the circuit inductance +  $L_M$ 



# **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

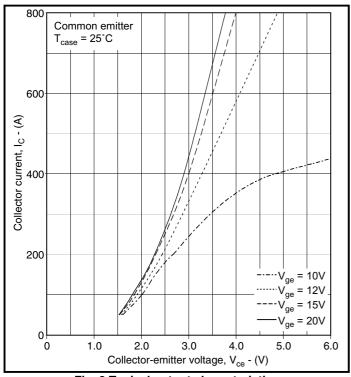
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 400A	-	2300	-	ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$	-	200	-	ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 1800V	-	320	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 8.2\Omega$	-	700	-	ns
t <sub>r</sub>	Rise time	C <sub>ge</sub> = 110nF	-	200	-	ns
$Q_g$	Gate charge	L ~ 80nH	-	10	-	μС
E <sub>on</sub>	Turn-on energy loss	$I_{\rm C} = 400 {\rm A}, \ V_{\rm GE} = \pm 15 {\rm V}, \ V_{\rm CE} = 1800 {\rm V}, \ R_{\rm G(ON)} = 3.9 \Omega, \ C_{\rm ge} = 110 {\rm nF}, \ L \sim 80 {\rm nH}$	-	600	-	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 400A, V_R = 1800V,$ $dI_F/dt = 1800A/\mu s$	-	200	-	μС
I <sub>rr</sub>	Diode reverse current		-	400	-	Α
E <sub>REC</sub>	Diode reverse recovery energy		-	120	-	mJ

T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 400A	-	2300	-	ns
t <sub>r</sub>	Fall time	$V_{GE} = \pm 15V$	-	300	-	ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 1800V	-	480	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 8.2\Omega$	-	700	-	ns
t <sub>r</sub>	Rise time	C <sub>ge</sub> = 110nF, L ~ 80nH	-	200	-	ns
E <sub>on</sub>	Turn-on energy loss	$I_{\rm C} = 400{\rm A},\ V_{\rm GE} = \pm 15{\rm V},\ V_{\rm CE} = 1800{\rm V},\ R_{\rm G(ON)} = 3.9\Omega,\ C_{\rm ge} = 110{\rm nF},\ L \sim 80{\rm nH}$	-	800	-	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 400A$ , $V_R = 1800V$ , $dI_F/dt = 1800A/\mu s$	-	370	-	μC
I <sub>m</sub>	Diode reverse current		-	460	-	Α
E <sub>REC</sub>	Diode reverse recovery energy		-	280	-	mJ



# **TYPICAL CHARACTERISTICS**



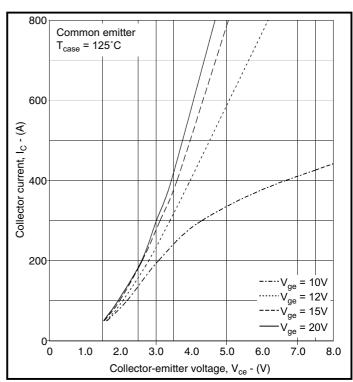
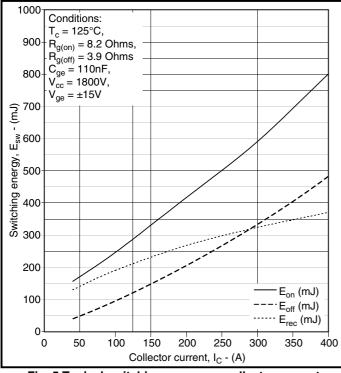


Fig. 3 Typical output characteristics







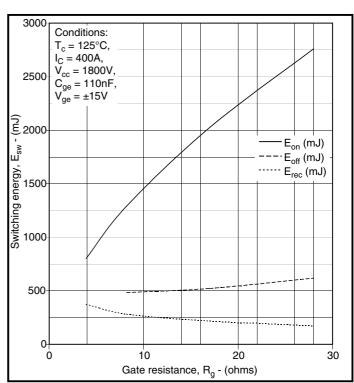
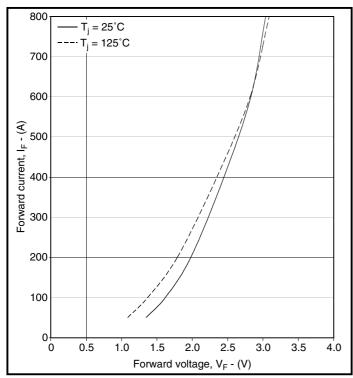


Fig. 6 Typical switching energy vs gate resistance

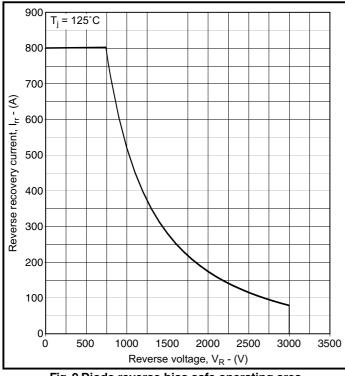




900 800 700 Chip Collector current, I<sub>C</sub> - (A) 000 000 000 000 Module 200 Conditions:  $T_{case} = 125^{\circ}C,$   $V_{ge} = \pm 15V,$ 100  $R_{g(off)} = 8.2 \text{ Ohms},$  $C_{ge}^{3} = 110nF$ 0 0 500 1000 1500 2000 2500 3000 3500 Collector emitter voltage,  $V_{ce}$  - (V)

Fig. 7 Diode typical forward characteristics

Fig. 8 Reverse bias safe operating area





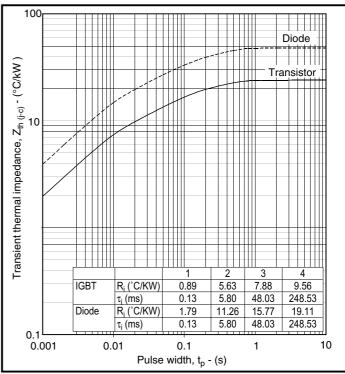
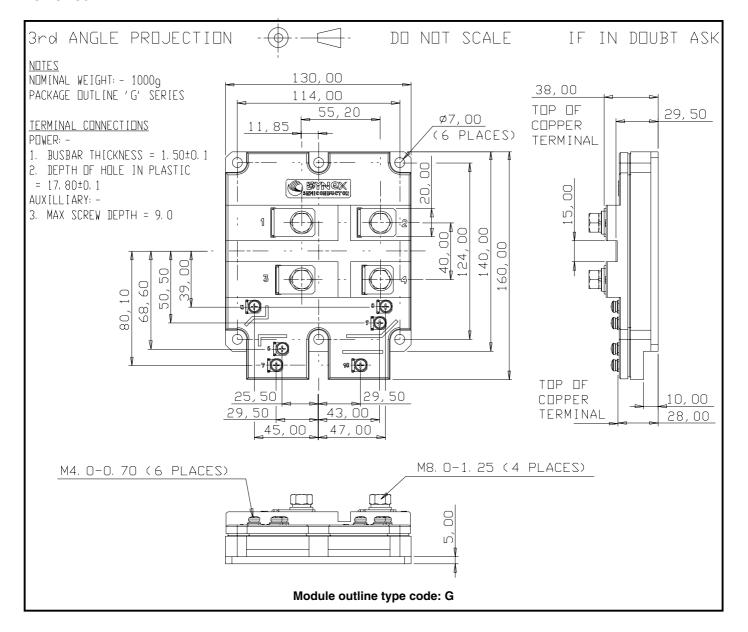


Fig. 10 Transient thermal impedance



# **PACKAGE DETAILS**

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





### **POWER ASSEMBLY CAPABILITY**

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

### **HEATSINKS**

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



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- P:- Preliminary information represents the product as it is understood but details may change as the product is in design and development.
- A:- Advance information denotes the product design is complete and final characterisation for volume production is well in hand.
- F:- The product parameters are fixed and the product is available to datasheet specification.

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