

SERIES DL1020 8 PIN SINGLE OUTPUT DIGITAL DELAY MODULES

- 8 Pin DIL Package • Low Profile Encapsulation Construction • TTL and DTL Compatible

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C

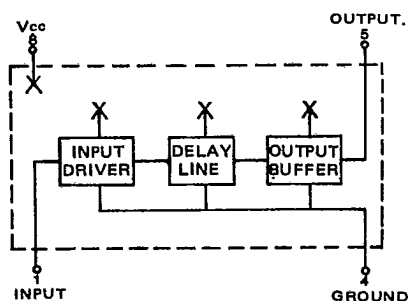
**Input Test Conditions**

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3 NS
- Input Current : 60mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay Nanosecond	Rise Time NSEC Max (1)
DL1021	5 $\pm$ 1	3
DL1022	10 $\pm$ 1	3
DL1023	15 $\pm$ 1	3
DL1024	20 $\pm$ 1	3
DL1025	25 $\pm$ 1.2	3
DL1026	30 $\pm$ 1.5	3
DL1027	35 $\pm$ 1.5	3
DL1028	40 $\pm$ 2	3
DL1029	45 $\pm$ 2	3
DL1030	50 $\pm$ 2.5	3
DL1031	55 $\pm$ 2.5	3
DL1032	60 $\pm$ 3	3
DL1033	65 $\pm$ 3	3
DL1034	70 $\pm$ 3.5	3
DL1035	75 $\pm$ 3.5	3
DL1036	80 $\pm$ 4	3
DL1037	85 $\pm$ 4	3
DL1038	90 $\pm$ 4	3
DL1039	95 $\pm$ 4	3
DL1040	100 $\pm$ 5	4
DL1041	125 $\pm$ 6	4
DL1042	150 $\pm$ 7.5	4
DL1043	175 $\pm$ 8	4
DL1044	200 $\pm$ 10	4
DL1045	225 $\pm$ 11	4
DL1046	250 $\pm$ 12	4

Note: (1) Measured from 0.75v to 2.4v.



SCHEMATIC

