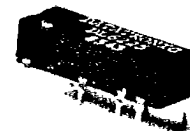


SERIES DL6225 DIGITAL DELAY MODULES

• 14 Pin DIP • TTL and DTL Compatible • 5 Equally Spaced Taps

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C

**Input Test Conditions**

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3 NS
- Input Current : 60mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay NS $\pm 5\%$	Tap to Tap Delay NS(1)	Rise Time NS Max(2)
DL6225	25	5	3
DL6226	30	6	3
DL6227	50	10	3
DL6228	60	12	3
DL6229	75	15	3
DL6230	100	20	3
DL6231	125	25	3
DL6232	150	30	3
DL6233	200	40	3
DL6234	250	50	4
DL6235	300	60	5
DL6236	350	70	5
DL6237	400	80	5
DL6238	450	90	5
DL6239	500	100	5
DL6240	1000	200	7

Note: (1) Measured at 1.5v level leading edge, Tol. $\pm 5\%$ or ± 2 NS, whichever is greater.

(2) Measured from 0.75v to 2.4v.

Version B low profile 0.210 inch max height is also available.

Please add 'B' to above part numbers if 0.210 inch height is required (i.e. DL6227B).

