

SERIES DL6380 DUAL TRIGGERABLE DIGITAL DELAY MODULES

- 14 Pin Package • TTL and DTL Compatible • 5 Equally Spaced Taps
- High Performance • Each unit provides 10 timing edges

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C



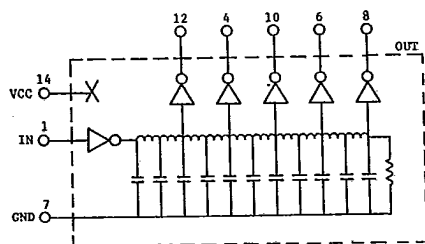
Input Test Conditions

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3 NS
- Input Current : 60mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay NSEC $\pm 5\%$	Tap to Tap Delay NS ⁽¹⁾	Rise Time NS Max ⁽²⁾
DL6382	25	5 $\pm$ 2	3
DL6383	50	10 $\pm$ 2	3
DL6384	75	15 $\pm$ 2	3
DL6385	100	20 $\pm$ 2	3
DL6558	125	25 $\pm$ 2	4
DL6386	150	30 $\pm$ 3	4
DL6387	200	40 $\pm$ 4	4
DL6388	250	50 $\pm$ 5	4
DL6576	300	60 $\pm$ 3	5
DL6577	400	80 $\pm$ 4	5
DL6389	500	100 $\pm$ 5	5 typ

Note: (1) Measured at 1.5v level, either leading or trailing edge.
(2) Measured from 0.75v to 2.4v.



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